Unit in mm

TOSHIBA Photocoupler GaAłAs Ired & Photo-IC

TLP2601

Isolated Line Receiver Simplex / Multiplex Data Transmission Computer-Peripheral Interface Microprocessor System Interface Digital Isolation For A/D, D/A Conversion Direct Replacement For HCPL-2601

The TOSHIBA TLP2601 a photocoupler which combines a GaAlAs IRed as the emitter and an integrated high gain, high speed photodetector. The output of the detector circuit is an open collector, Schottky clamped transistor.

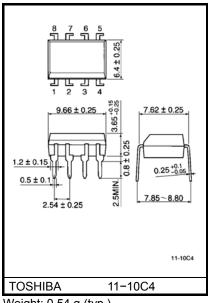
A Faraday shield integrated on the photodetector chip reduces the effects of capacitive coupling between the input LED emitter and the high gain stages of the detector. This provides an effective common mode transient immunity of $1000V/\mu s$.

- Input current thresholds: $I_F = 5 mA max$.
- Isolation voltage: 2500Vrms min.
- Switching speed: 10MBd
- Common mode transient immunity: 1000V/µs min.
- Guaranteed performance over temp.: $0^{\circ}C \sim 70^{\circ}C$
- UL Recognized: UL1577, file No. E67349

Truth Table (positive logic)

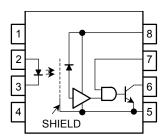
Input	Enable	Output
Н	Н	L
L	Н	Н
Н	L	Н
L	L	Н

A 0.01 to 0.1 μ F bypass capacitor must be connected between pins 8 and 5 (see Note 1).

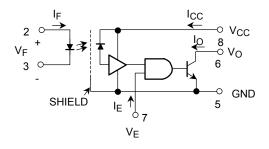


Weight: 0.54 g (typ.)

Pin Configuration (top view)



Schematic



Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Input current, low level	I _{FL}	0		250	μA
Input current, high level	I _{FH}	6.3 (*)	_	20	mA
Supply voltage**, output	V _{CC}	4.5	_	5.5	V
High level enable voltage	V _{EH}	2.0	_	V _{CC}	V
Low level enable voltage	V _{EL}	0	_	0.8	V
Fan out (TTL load)	N	_	—	8	—
Operating temperature	T _{opr}	0	_	70	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

(*) 6.3mA is a guard banded value which allows for at least 20% CTR degradation.

Initial input current threshold value is 5.0mA or less.

**This item denotes operating ranges, not meaning of recommended operating conditions.

Absolute Maximum Ratings (no derating required)

	Characteristic	Symbol	Rating	Unit	
D	Forward current	lF	20	mA	
LED	Reverse voltage	V _R	5	V	
	Output current	lo	25	mA	
	Output voltage	Vo	-0.5~7	V	
tor	Supply voltage	Vcc	7	V	
Detector	(1 minute maximum)	•00	•	÷	
ă	Enable input voltage	VF	5.5	V	
	(not to exceed V_{CC} by more than 500mV)	٧E	5.5	v	
	Output collector power dissipation	Po	40	mW	
Oper	Operating temperature range		-40~85	°C	
Stora	ge temperature range	T _{stg}	-55~125	°C	
Lead	solder temperature (10s) (**)	T _{sol}	260	°C	
Isolat	tion voltage	P\/a	2500	Vrms	
(R.H.	≤ 60%,AC 1min., (Note 10)	BVS	3540	V _{dc}	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(**) 1.6mm below seating plane.

Electrical Characteristics (Ta = 0°C ~70°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
High level output current	I _{ОН}	V_{CC} = 5.5V, V_{O} = 5.5V I _F = 250µA, V _E = 2.0V	_	1	250	μΑ
Low level output voltage	V _{OL}	V_{CC} = 5.5V, I _F = 5mA V_E = 2.0V, I _{OL} (sinking) = 13mA	_	0.4	0.6	v
High level supply current	Іссн	V _{CC} = 5.5V, I _F = 0, V _E = 0.5V	_	7	15	mA
Low level supply current	ICCL	V _{CC} = 5.5V, I _F = 10mA V _E = 0.5V	_	12	19	mA
Low level enable current	IEL	V _{CC} = 5.5V, V _E = 0.5V	_	-1.6	-2.0	mA
High level enable current	IEH	V _{CC} = 5.5V, V _E = 2.0V	_	-1	_	mA
High level enable voltage	V _{EH}	(Note 11)	2.0	_	_	V
Low level enable voltage	V _{EL}	_	_	_	0.8	
Input forward voltage	V _F	I _F = 10mA, Ta = 25°C	_	1.65	1.75	V
Input reverse breakdown voltage	BV _R	I _R = 10μΑ, Ta = 25°C	5		_	v
Input capacitance	C _{IN}	V _F = 0, f = 1MHz	_	45	_	pF
Input diode temperature coefficient	ΔV _F /ΔT _A	I _F = 10mA	_	-2.0		mV / °C
Input-output insulation leakage current	II-O	Relative humidity = 45% Ta=25°C, t = 5 second V_{I-O} = 3000Vdc, (Note 10)	_		1	μA
Resistance (input–output)	R _{I–O}	V _{I–O} = 500V, R.H.≤ 60% (Note 10)	5×10 ¹⁰	10 ¹⁴		Ω
Capacitance (input-output)	C _{I–O}	f = 1MHz, (Note 10)		0.6		pF

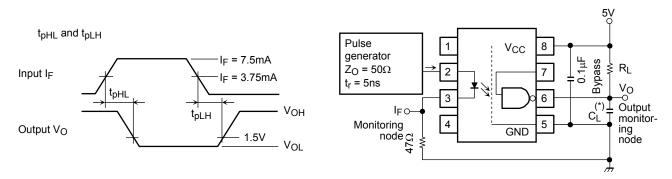
(**)All typ.values are at V_{CC} = 5V, Ta = 25° C.

Switching Characteristics (Ta = 25° C, V_{CC} = 5 V)

Characteristic	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
Propagation delay time to high output level	t _{pLH}		R _L = 350Ω, C _L = 15pF	_	60	75	ns
Propagation delay time to low output level	t _{pHL}	1	I _F = 7.5mA (Note 2), (Note 3),	_	60	75	ns
Output rise time(10-90%)	tr		(Note 4)&(Note 5)	_	30	_	ns
Output fall time(90–10%)	t _f			_	30		ns
Propagation delay time of enable from V_{EH} to V_{EL}	tELH		R _L = 350Ω, C _L = 15pF I _F = 7.5mA	-	25	Ι	ns
Propagation delay time of enable from V_{EL} to V_{EH}	^t EHL	2	V _{EH} = 3.0V V _{EL} = 0.5V (Note 6)&(Note 7)	_	25	Ι	ns
Common mode transient immunity at high output level	CM _H	3	$V_{CM} = 400V$ $R_L = 350\Omega$ $V_{O(min.)} = 2V$ $I_F = 0mA, \qquad (Note 9)$	1000	10000	Ι	V/µs
Common mode transient immunity at low output level	CML		$V_{CM} = 400V$ $R_L = 350\Omega$ $V_{O(max.)} = 0.8V$ $I_F = 7.5mA$, (Note 8)	-1000	-10000	_	V/µs

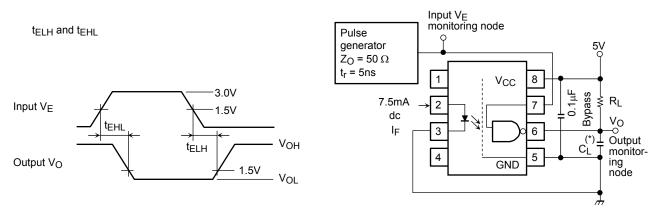
<u>TOSHIBA</u>

Test Circuit 1.



(*) C_L is approximately 15pF which includes probe and stray wiring capacitance.

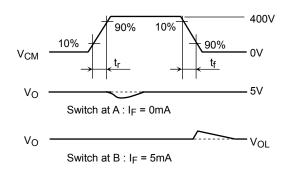
Test Circuit 2.

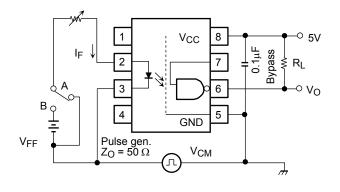


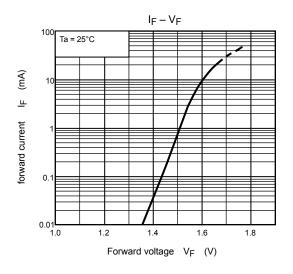
(*) CL is approximately 15pF which includes probe and stray wiring capacitance.

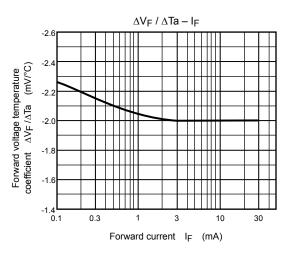
Test Circuit 3.

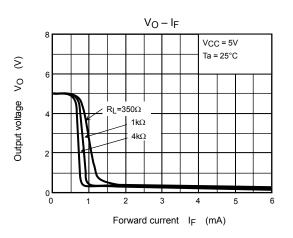
Transient Immunity and Typ. Waveforms.

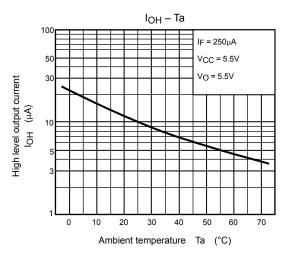


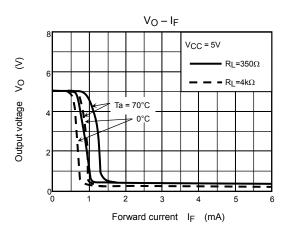


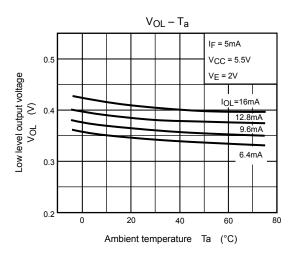


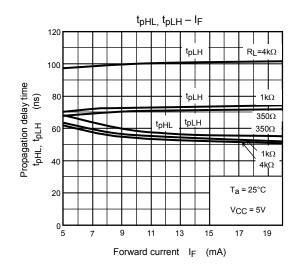


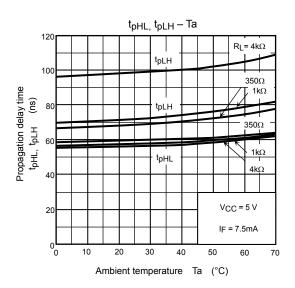


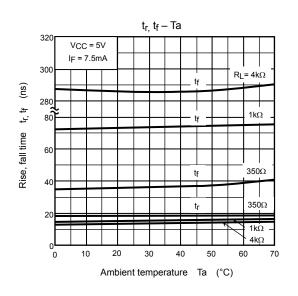


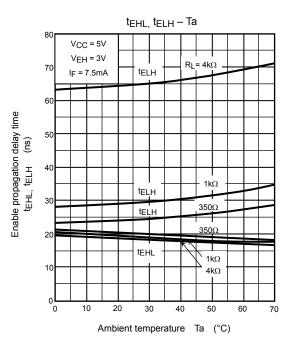












Notes

- $\label{eq:constraint} 1. \quad The V_{CC} \mbox{ supply voltage to each TLP2601 isolator must be bypassed by a 0.1 \mu F capacitor of larger. This can be either a ceramic or solid tantalum capacitor with good high frequency characteristic and should be connected as close as possible to the package V_{CC} and GND pins of each device.$
- $\label{eq:phi} 2. \quad t_{pHL} \quad \cdot \quad \mbox{Propagation delay is measured from the 3.75mA level on the low to high transition of the input current pulse to the 1.5V level on the high to low transition of the output voltage pulse. }$
- 4. t_f Fall time is measured from the 10% to the 90% levels of the high to low transition on the output pulse.
- 5. t_r · Rise time is measured from the 90% to 10% levels of the low to high transition on the output pulse.
- 6. tEHL · Enable input propagation delay is measured from the 1.5V level on the low to high transition of the input voltage pulse to the 1.5V level on the high to low transition of the output voltage pulse.
- 7. tELH · Enable input propagation delay is measured from the 1.5V level on the high to low transition of the input voltage pulse to the 1.5V level on the low to high transition of the output voltage pulse.
- CML

 The maximum tolerable rate of fall of the common mode voltage to ensure the output will remain in the low output state (i.e., V_{OUT} < 0.8V). Measured in volts per microsecond (V / μs).
- 9. CM_H · The maximum tolerable rate of fall of the common mode voltage to ensure the output will remain in the high state (i.e., V_{OUT} > 2.0V). Measured in volts per microsecond(V / μs). Volts/microsecond can be translated to sinusoidal voltages:

$$V / \mu s = \frac{(\text{dv}_{CM})}{\text{dt}}_{Max.} = f_{CM} V_{CM} (p.p.)$$

Example:

 V_{CM} = 318V_{pp} when f_{CM} = 1MHz using CM_L and CM_H = 1000V / μs data sheet specified minimum.

- 10. Device considered a two-terminal device: Pins 1, 2, 3 and 4 shorted together, and Pins 5, 6, 7 and 8 shorted together.
- 11. Enable No pull up resistor required as the device has an internal pull up resistor. input

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