



# P-Channel 20-V (D-S), 1.5-V (G-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
- 20	0.024 at V <sub>GS</sub> = - 4.5 V	- 7			
	0.030 at V <sub>GS</sub> = - 2.5 V	- 6.2			
	0.038 at V <sub>GS</sub> = - 1.8 V	- 5.2			
	0.048 at V <sub>GS</sub> = - 1.5 V	- 5.0			

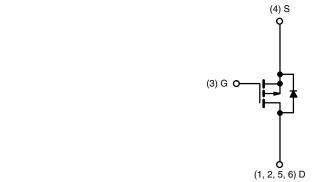
## **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET: 1.5 V Rated
- Ultra-Low On-Resistance
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

· Load Switch and PA Switch for Portable Devices



P-Channel MOSFET

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Ordering Information: Si3495DV-T1-E3 (Lead (Pb)-free)

Si3495DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 95xxx

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 5			
Continuous Dunis Comment /T 450 0008	T <sub>A</sub> = 25 °C	I_	- 7	- 5.3		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C	- I <sub>D</sub>	- 3.6	- 3.9	Δ.	
Pulsed Drain Current		I <sub>DM</sub>	- 20		А	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.7	- 0.9		
Mariana Barra Biraira di ang	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0	1.1	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C	' D	1.0	0.6	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian de Analian to	t ≤ 5 s	R <sub>thJA</sub>	45	62.5	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	□thJA	90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	25	30	

#### Note:

a. Surface Mounted on 1" x 1" FR4 board.

# Vishay Siliconix



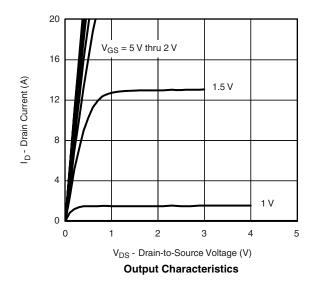
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.35		- 0.75	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	nA		
Zana Oata Wallana Busin Ourmant	1	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C			- 10	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 20			Α		
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7 A		0.020	0.024			
	D	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 6.2 A		0.024	0.030	0		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 5.2 A		0.030	0.038	Ω		
		V <sub>GS</sub> = - 1.5 V, I <sub>D</sub> = - 3 A		0.036	0.048			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 7 A		25		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.62	- 1.1	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			25	38			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7 \text{ A}$		2.5		nC		
Gate-Drain Charge	$Q_{gd}$			7				
Gate Resistance	$R_{g}$		4	8.5	13	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			19	30			
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		36	55			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$		200	300	ns		
Fall Time	t <sub>f</sub>			106	160			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dI/dt = 100 A/μs		35	60			

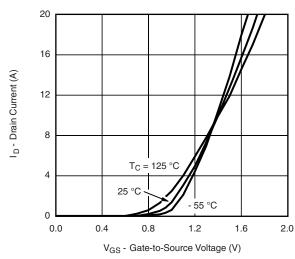
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



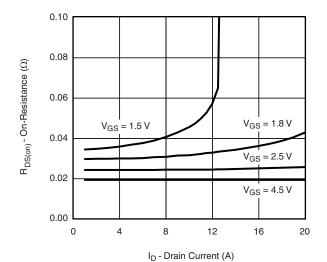




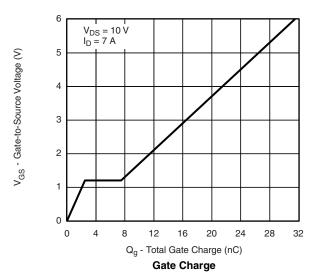




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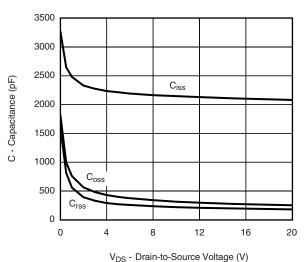


On-Resistance vs. Drain Current



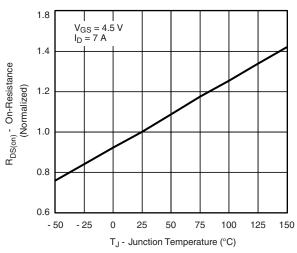
20 10 T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C 1 0.1 0.0 0.2 0.4 0.6 0.8 1.0 1.2 V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

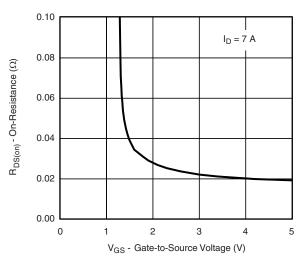


VDS - Dialii-to-Source voltage (v)





On-Resistance vs. Junction Temperature



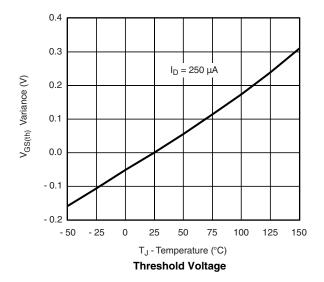
On-Resistance vs. Gate-to-Source Voltage

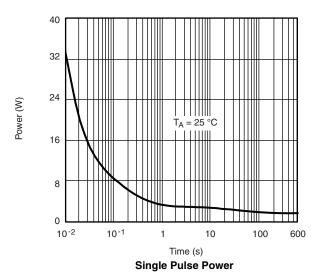
Is - Source Current (A)

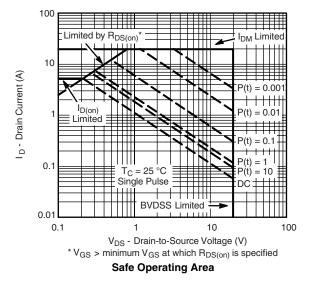
## Vishay Siliconix

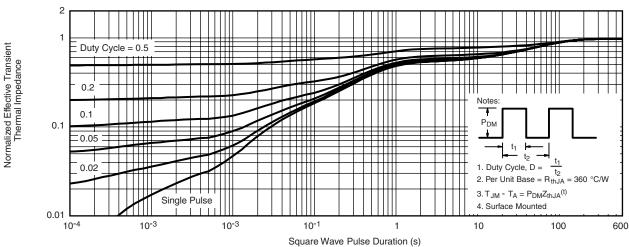
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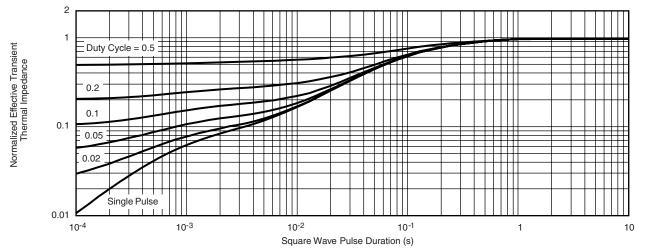






Normalized Thermal Transient Impedance, Junction-to-Ambient

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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