

## Dual P-Channel 60-V (D-S), 175 °C MOSFET

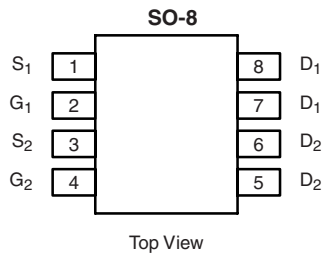
PRODUCT SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
- 60	0.120 at V <sub>GS</sub> = - 10 V	± 3.1
	0.150 at V <sub>GS</sub> = - 4.5 V	± 2.8

### FEATURES

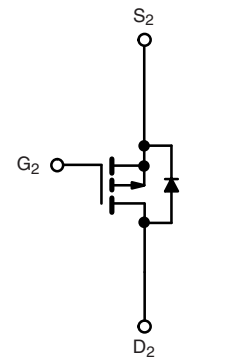
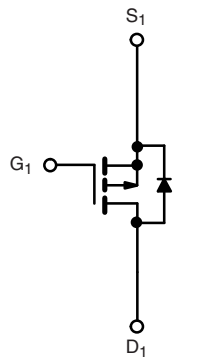
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available



Ordering Information: Si4948EY-T1-E3 (Lead (Pb)-free)  
Si4948EY-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 60	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>A</sub> = 25 °C	± 3.1
		T <sub>A</sub> = 70 °C	± 2.6
Pulsed Drain Current	I <sub>DM</sub>	± 30	A
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	- 2.0	
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	2.4
		T <sub>A</sub> = 70 °C	1.7
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Maximum Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	62.5	°C/W

Notes:

a. Surface Mounted on FR4 board, t ≤ 10 s.

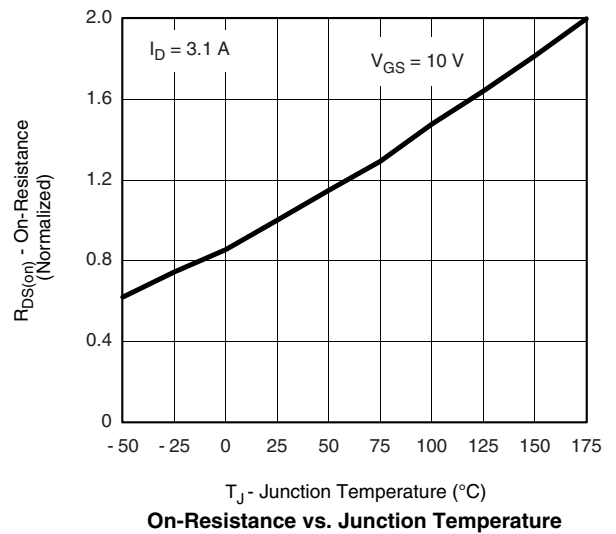
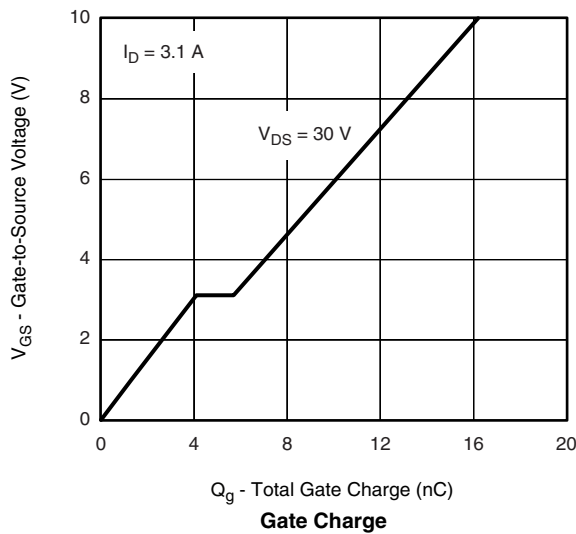
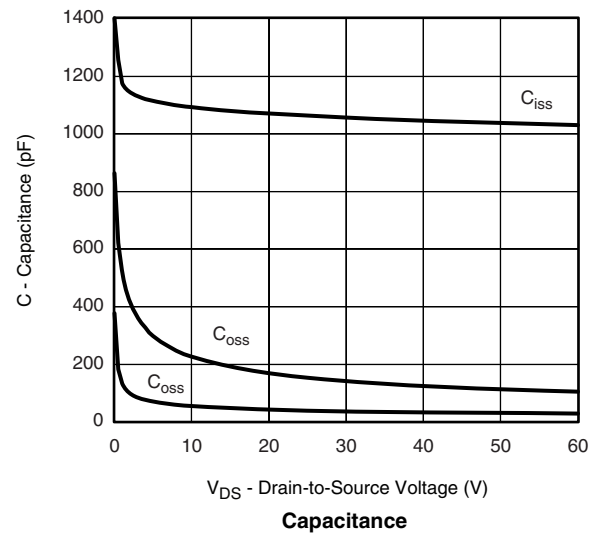
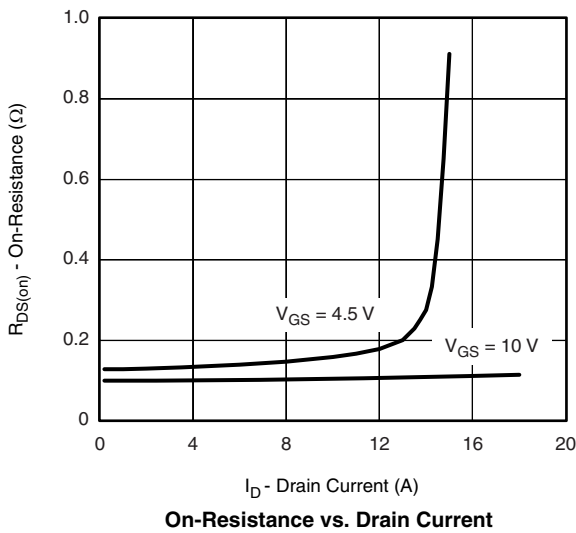
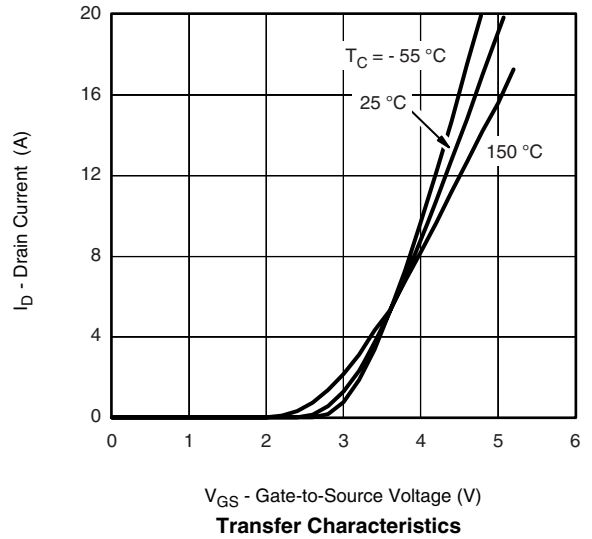
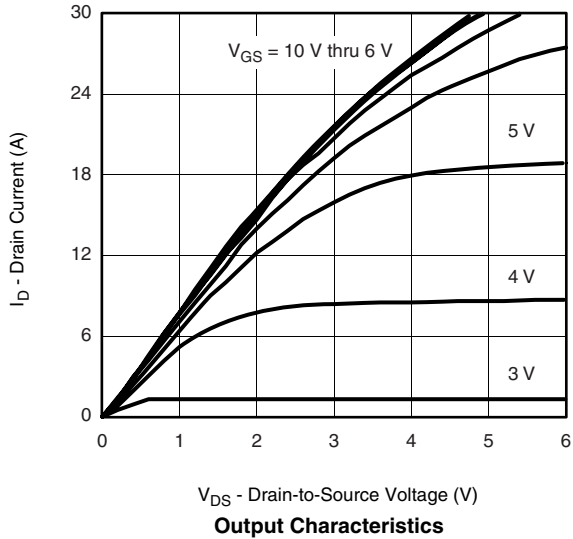
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$			- 2	$\mu\text{A}$
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			- 25	
On-State Drain Current <sup>b</sup>	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -10\text{ V}$	- 20			A
Drain-Source On-State Resistance <sup>b</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -3.1\text{ A}$		0.100	0.120	$\Omega$
		$V_{GS} = -4.5\text{ V}, I_D = -2.8\text{ A}$		0.125	0.150	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -3.1\text{ A}$		7.5		S
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$I_S = -2.0\text{ A}, V_{GS} = 0\text{ V}$		- 0.8	- 1.2	V
<b>Dynamic<sup>a</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -30\text{ V}, V_{GS} = -10\text{ V}, I_D = -3.1\text{ A}$		16	25	nC
Gate-Source Charge	$Q_{gs}$			4		
Gate-Drain Charge	$Q_{gd}$			1.6		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 30\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -10\text{ V}, R_g = 6\text{ }\Omega$		8	15	ns
Rise Time	$t_r$			10	20	
Turn-Off Delay Time	$t_{d(off)}$			35	50	
Fall Time	$t_f$			12	25	
Source-Drain Reverse Recovery Time	$t_{rr}$		$I_F = -2.0\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		60	

## Notes:

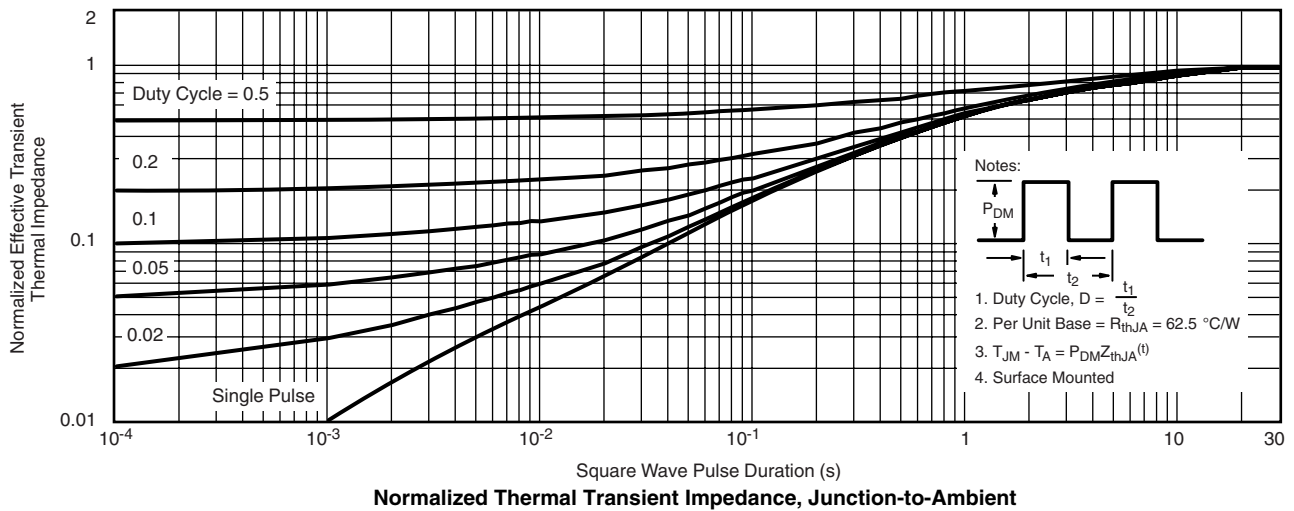
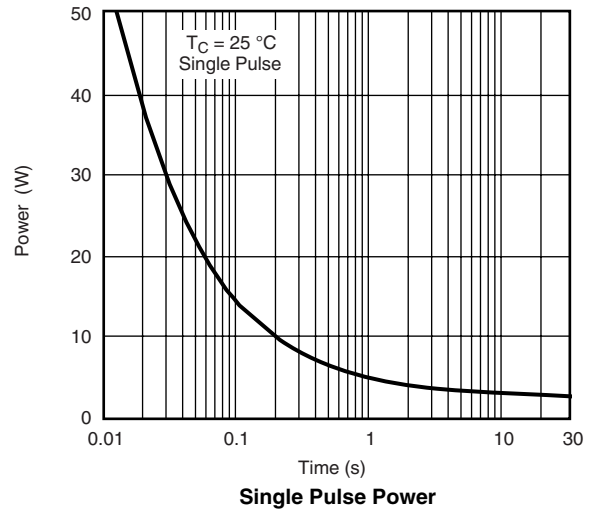
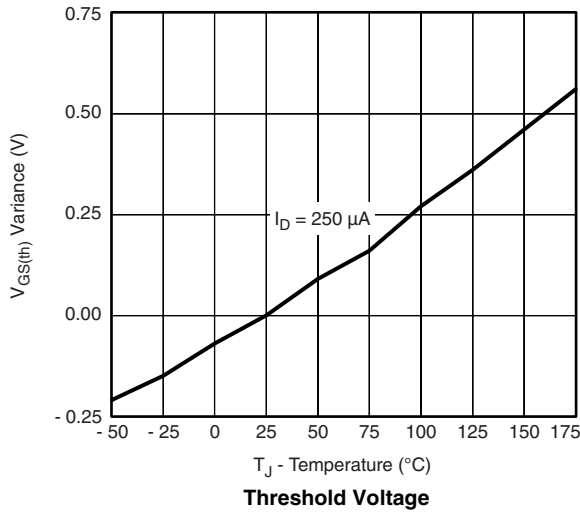
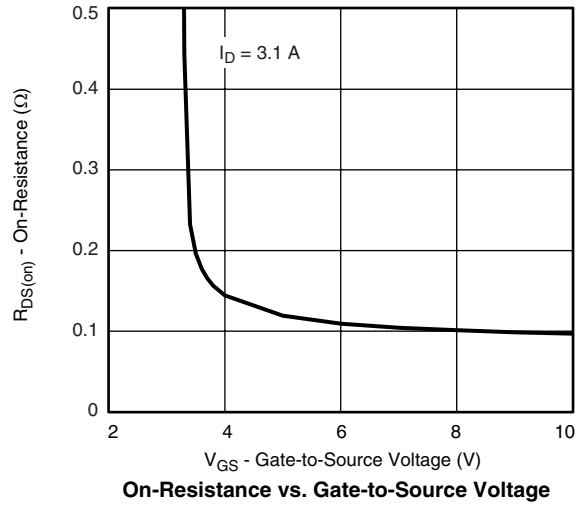
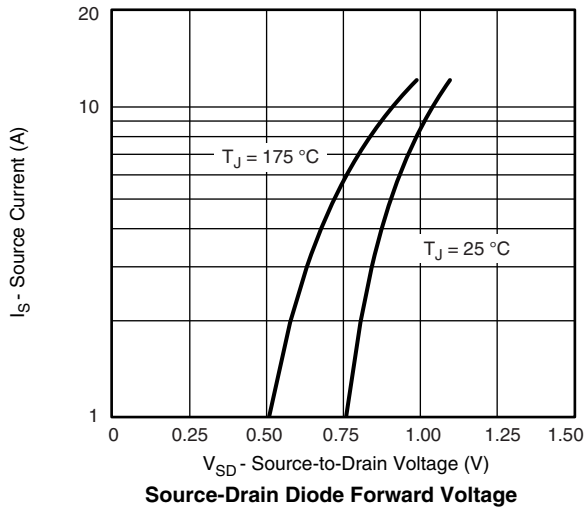
- a. Guaranteed by design, not subject to production testing.  
 b. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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