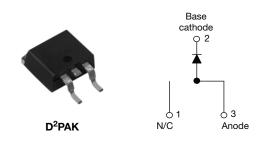


Vishay High Power Products

### Schottky Rectifier, 10 A



PRODUCT SUMMARY						
I <sub>F(AV)</sub>	10 A					
V <sub>R</sub>	35 V/45 V					
I <sub>RM</sub>	15 mA at 125 °C					

#### FEATURES

• 150 °C T<sub>J</sub> operation

encapsulation

- TO-220 and D<sup>2</sup>PAK packages
- Low forward voltage drop
- High frequency operationHigh purity, high t



HALOGEN

FREE

- strength and moisture resistanceGuard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

#### DESCRIPTION

This Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U							
I <sub>F(AV)</sub>	Rectangular waveform	10	А						
I <sub>FRM</sub>	T <sub>C</sub> = 135 °C	20	7						
V <sub>RRM</sub>		35/45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1060	А						
V <sub>F</sub>	10 Apk, T <sub>J</sub> = 125 °C	0.57	V						
TJ	Range	- 65 to 150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-MBRB1035PbF	VS-MBRB1045PbF	UNITS					
Maximum DC reverse voltage	V <sub>R</sub>	35	45	M					
Maximum working peak reverse voltage	V <sub>RWM</sub>		45	v					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CON	VALUES	UNITS				
Maximum average forward current	I <sub>F(AV)</sub>	$T_{C}$ = 135 °C, rated $V_{R}$		10				
Peak repetitive forward current	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 I	<Ηz, T <sub>C</sub> = 135 °C	20				
Non-repetitive surge current	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	1060	A				
		Surge applied at rated load single phase, 60 Hz	150					
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 2 \ A, \ L = 4 \ r$	8	mJ				
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to Frequency limited by T <sub>J</sub> max	2	А				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		20 A	T <sub>J</sub> = 25 °C	0.84				
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	10 A	− T <sub>.1</sub> = 125 °C	0.57	V			
		20 A	$1_{\rm J} = 125$ C	0.72				
Maximum instantaneous reverse	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Rated DC voltage	0.1	mA			
current	IRM ("	T <sub>J</sub> = 125 °C	haled DC vollage	15	ШA			
Threshold voltage	V <sub>F(TO)</sub>			0.354	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$		17.6	mΩ			
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	600	pF				
Typical series inductance	L <sub>S</sub>	Measured from top of ter	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

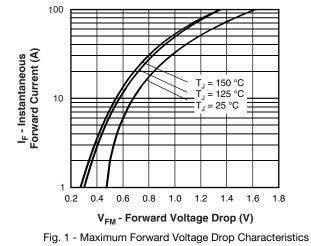
#### Note

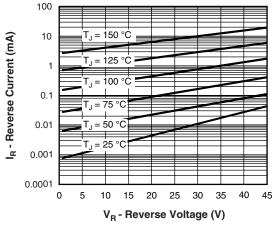
<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

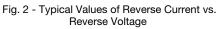
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction tempera	ture range	TJ		- 65 to 150	°C			
Maximum storage temperat	ure range	T <sub>Stg</sub>		- 65 to 175	U			
Maximum thermal resistance	e,	R <sub>thJC</sub>	DC operation	2.0				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased (Only for TO-220)	0.50	°C/W			
Annewigete weight				2	g			
Approximate weight				0.07	oz.			
minimum				6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf $\cdot$ in)			
Marking device			$\rho_{\rm res}$ at $\rho_{\rm res}^2 \rho_{\rm res} / \rho_{\rm res}^2$	MBRE	31035			
			Case style D <sup>2</sup> PAK	MBRE	31045			



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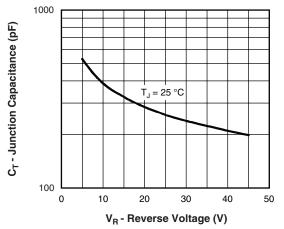


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

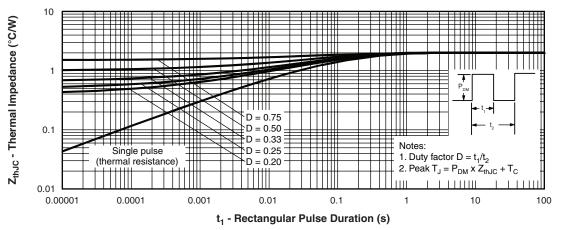
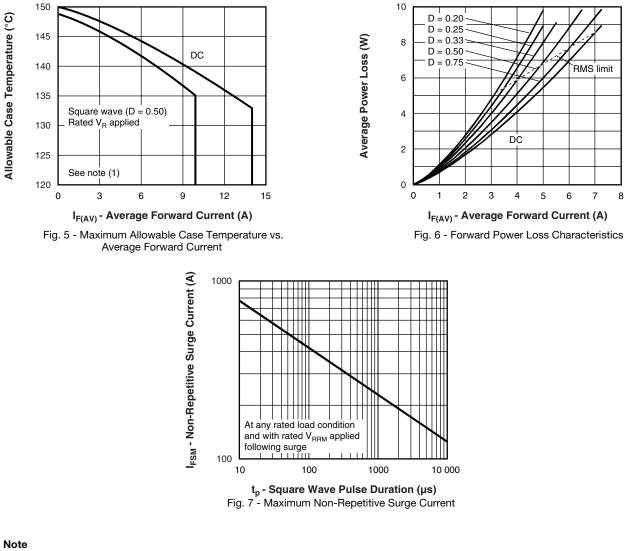


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

Vishay High Power Products

Schottky Rectifier, 10 A



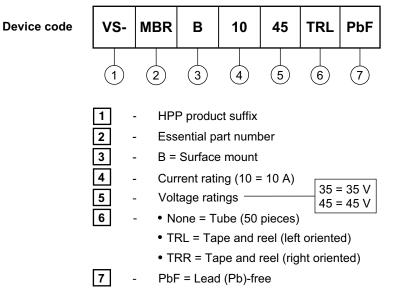
#### Note

- (1) Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{th,JC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D); I_R at V_{R1} = Rated V_R$



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#### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information www.vishay.com/doc?95054						
Packaging information	www.vishay.com/doc?95032					
SPICE model	www.vishay.com/doc?95293					

### **Outline Dimensions**

**Vishay Semiconductors** 

D<sup>2</sup>PAK



Conforms to JEDEC outline D<sup>2</sup>PAK (SMD-220) в Pad layout (2)(3)A 11.00 MIN.-(E) F (0.43)ŧ (3) L1 4 ( |(0.38)<sup>MIN.</sup> (D1) (3) Detail A D 17.90 (0.70) Н 15.00 (0.625) (2) З 0.15)<sup>0.01</sup> Ľ L2 Ĥ ţ В В 2.32 MIN. (0.08) 2.64 (0.103) 2.41 (0.096) (3)Ċ 2 x b2 С View A - A 2 x h // ± 0.004 M B ⊕ 0.010 M A M B Base Plating (4) Metal 2 x e Н b1, b3 Gauge plane c1 (4) (c) В 0° to 8° ŧ. Seating Lead assignments plane L3 A1 Lead tip (b, b2) Diodes Section B - B and C - C 1. - Anode (two die)/open (one die) Scale: None 2., 4. - Cathode Detail "A" 3. - Anode

Rotated 90 °CW Scale: 8:1

SYMBOL	MILLIMETERS		INCHES		NOTES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3		
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3		
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3		
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC			
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625			
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110			
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3		
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070			
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC			
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208			

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

Document Number: 95046 For technical questions within your region, please contact one of the following: Revision: 31-Mar-11 DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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#### **DIMENSIONS** in millimeters and inches



Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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