## S-1206 Series

## ULTRA LOW CURRENT CONSUMPTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

www.ablic.com

The S-1206 Series is a positive voltage regulator with ultra low current consumption, low dropout voltage, high-accuracy output voltage, and 250 mA output current developed based on CMOS technology.
I/O capacitors are as small as $0.1 \mu \mathrm{~F}$. S-1206 Series operates at ultra low current consumption of $1.0 \mu \mathrm{~A}$ (typ.).
The built-in low-on-resistance transistor realizes low dropout voltage and a large output current. A built-in overcurrent protection circuit prevents the load current from exceeding the current capacity of the output transistor. Three packages, SOT-23-3, SOT-89-3, and SNT-6A(H) are available.
Compared with voltage regulators using a conventional CMOS technology, more types of capacitors, including small I/O capacitors, can be used with the S-1206 Series. The S-1206 Series features ultra low current consumption and comes in a small package, making them most suitable for portable equipment.

## ■ Features

- Output voltage:
- Input voltage:
- Output voltage accuracy:
- Dropout voltage:
- Current consumption:
- Output current:
- Input and output capacitors:
- Built-in overcurrent protection circuit:
- Operation temperature range:
- Lead-free, Sn 100\%, halogen-free ${ }^{* 2}$
1.2 V to 5.2 V , selectable in 0.05 V step
1.7 V to 6.5 V
$\pm 1.0 \%$ ( 1.2 V to 1.45 V output product: $\pm 15 \mathrm{mV}$ )
150 mV typ. ( 3.0 V output product, $\mathrm{I}_{\text {Out }}=100 \mathrm{~mA}$ )
During operation: $1.0 \mu \mathrm{~A}$ typ., $1.5 \mu \mathrm{~A}$ max.
Possible to output $250 \mathrm{~mA}\left(3.0 \mathrm{~V}\right.$ output product, $\mathrm{V}_{\mathbb{I N}} \geq \mathrm{V}_{\mathrm{OUT}(\mathrm{S})}+1.0 \mathrm{~V}$ ) ${ }^{* 1}$
A ceramic capacitor of $0.1 \mu \mathrm{~F}$ or more can be used.
Limits overcurrent of output transistor.
$\mathrm{Ta}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
*1. Attention should be paid to the power dissipation of the package when the output current is large.
*2. Refer to "■ Product Name Structure" for details.


## Applications

- Constant-voltage power supply for battery-powered device
- Constant-voltage power supply for cellular phone
- Constant-voltage power supply for portable equipment


## Packages

- SOT-23-3
- SOT-89-3
- SNT-6A(H)


## Block Diagram



Figure 1

## ■ Product Name Structure

The output voltage value and package types for the S-1206 Series can be selected at the user's request. Refer to "1. Product Name" regarding the contents of product name, "2. Packages" regarding the package drawings and "3. Product Name List" regarding details of the product name.

## 1. Product Name

1.1 SOT-23-3, SOT-89-3


```
U : Lead-free (Sn 100\%), halogen-free
G : Lead-free (for details, please contact our sales office)
Package name (abbreviation) and packing specifications* \({ }^{* 1}\)
M3T1 : SOT-23-3, Tape
U3T1: SOT-89-3, Tape
Output voltage value
12 to 52
(e.g. When output voltage is 1.2 V , it is expressed as 12.)
```

*1. Refer to the tape drawing.

1. 2 SNT-6A(H)

*1. Refer to the tape drawing.

## 2. Packages

| Package Name | Drawing Code |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
|  | Package | Tape | Reel | Land |
| SOT-23-3 | MP003-C-P-SD | MP003-C-C-SD | MP003-Z-R-SD | - |
| SOT-89-3 | UP003-A-P-SD | UP003-A-C-SD | UP003-A-R-SD | - |
| SNT-6A(H) | PI006-A-P-SD | PI006-A-C-SD | PI006-A-R-SD | PI006-A-L-SD |

ULTRA LOW CURRENT CONSUMPTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR<br>S-1206 Series

## 3. Product Name List

Table 1

| Output Voltage | SOT-23-3 | SOT-89-3 | SNT-6A(H) |
| :---: | :---: | :---: | :---: |
| $1.2 \mathrm{~V} \pm 15 \mathrm{mV}$ | S-1206B12-M3T1x | S-1206B12-U3T1x | S-1206B12-I6T2U |
| $1.3 \mathrm{~V} \pm 15 \mathrm{mV}$ | S-1206B13-M3T1x | S-1206B13-U3T1x | S-1206B13-16T2U |
| $1.4 \mathrm{~V} \pm 15 \mathrm{mV}$ | S-1206B14-M3T1x | S-1206B14-U3T1x | S-1206B14-16T2U |
| $1.5 \mathrm{~V} \pm 1.0 \%$ | S-1206B15-M3T1x | S-1206B15-U3T1x | S-1206B15-I6T2U |
| $1.6 \mathrm{~V} \pm 1.0 \%$ | S-1206B16-M3T1x | S-1206B16-U3T1x | S-1206B16-16T2U |
| $1.7 \mathrm{~V} \pm 1.0 \%$ | S-1206B17-M3T1x | S-1206B17-U3T1x | S-1206B17-16T2U |
| $1.8 \mathrm{~V} \pm 1.0 \%$ | S-1206B18-M3T1x | S-1206B18-U3T1x | S-1206B18-16T2U |
| $1.85 \mathrm{~V} \pm 1.0 \%$ | S-1206B1J-M3T1x | S-1206B1J-U3T1x | S-1206B1J-16T2U |
| $1.9 \mathrm{~V} \pm 1.0 \%$ | S-1206B19-M3T1x | S-1206B19-U3T1x | S-1206B19-16T2U |
| $2.0 \mathrm{~V} \pm 1.0 \%$ | S-1206B20-M3T1x | S-1206B20-U3T1x | S-1206B20-16T2U |
| $2.1 \mathrm{~V} \pm 1.0 \%$ | S-1206B21-M3T1x | S-1206B21-U3T1x | S-1206B21-I6T2U |
| $2.2 \mathrm{~V} \pm 1.0 \%$ | S-1206B22-M3T1x | S-1206B22-U3T1x | S-1206B22-16T2U |
| $2.3 \mathrm{~V} \pm 1.0 \%$ | S-1206B23-M3T1x | S-1206B23-U3T1x | S-1206B23-16T2U |
| $2.4 \mathrm{~V} \pm 1.0 \%$ | S-1206B24-M3T1x | S-1206B24-U3T1x | S-1206B24-I6T2U |
| $2.5 \mathrm{~V} \pm 1.0 \%$ | S-1206B25-M3T1x | S-1206B25-U3T1x | S-1206B25-I6T2U |
| $2.6 \mathrm{~V} \pm 1.0 \%$ | S-1206B26-M3T1x | S-1206B26-U3T1x | S-1206B26-16T2U |
| $2.7 \mathrm{~V} \pm 1.0 \%$ | S-1206B27-M3T1x | S-1206B27-U3T1x | S-1206B27-I6T2U |
| $2.8 \mathrm{~V} \pm 1.0 \%$ | S-1206B28-M3T1x | S-1206B28-U3T1x | S-1206B28-16T2U |
| $2.85 \mathrm{~V} \pm 1.0 \%$ | S-1206B2J-M3T1x | S-1206B2J-U3T1x | S-1206B2J-16T2U |
| $2.9 \mathrm{~V} \pm 1.0 \%$ | S-1206B29-M3T1x | S-1206B29-U3T1x | S-1206B29-16T2U |
| $3.0 \mathrm{~V} \pm 1.0 \%$ | S-1206B30-M3T1x | S-1206B30-U3T1x | S-1206B30-16T2U |
| $3.1 \mathrm{~V} \pm 1.0 \%$ | S-1206B31-M3T1x | S-1206B31-U3T1x | S-1206B31-I6T2U |
| $3.2 \mathrm{~V} \pm 1.0 \%$ | S-1206B32-M3T1x | S-1206B32-U3T1x | S-1206B32-I6T2U |
| $3.3 \mathrm{~V} \pm 1.0 \%$ | S-1206B33-M3T1x | S-1206B33-U3T1x | S-1206B33-I6T2U |
| $3.4 \mathrm{~V} \pm 1.0 \%$ | S-1206B34-M3T1x | S-1206B34-U3T1x | S-1206B34-16T2U |
| $3.5 \mathrm{~V} \pm 1.0 \%$ | S-1206B35-M3T1x | S-1206B35-U3T1x | S-1206B35-I6T2U |
| $3.6 \mathrm{~V} \pm 1.0 \%$ | S-1206B36-M3T1x | S-1206B36-U3T1x | S-1206B36-I6T2U |
| $3.7 \mathrm{~V} \pm 1.0 \%$ | S-1206B37-M3T1x | S-1206B37-U3T1x | S-1206B37-I6T2U |
| $3.8 \mathrm{~V} \pm 1.0 \%$ | S-1206B38-M3T1x | S-1206B38-U3T1x | S-1206B38-16T2U |
| $3.9 \mathrm{~V} \pm 1.0 \%$ | S-1206B39-M3T1x | S-1206B39-U3T1x | S-1206B39-I6T2U |
| $4.0 \mathrm{~V} \pm 1.0 \%$ | S-1206B40-M3T1x | S-1206B40-U3T1x | S-1206B40-16T2U |
| $4.1 \mathrm{~V} \pm 1.0 \%$ | S-1206B41-M3T1x | S-1206B41-U3T1x | S-1206B41-I6T2U |
| $4.2 \mathrm{~V} \pm 1.0 \%$ | S-1206B42-M3T1x | S-1206B42-U3T1x | S-1206B42-I6T2U |
| $4.3 \mathrm{~V} \pm 1.0 \%$ | S-1206B43-M3T1x | S-1206B43-U3T1x | S-1206B43-I6T2U |
| $4.4 \mathrm{~V} \pm 1.0 \%$ | S-1206B44-M3T1x | S-1206B44-U3T1x | S-1206B44-16T2U |
| $4.5 \mathrm{~V} \pm 1.0 \%$ | S-1206B45-M3T1x | S-1206B45-U3T1x | S-1206B45-I6T2U |
| $4.6 \mathrm{~V} \pm 1.0 \%$ | S-1206B46-M3T1x | S-1206B46-U3T1x | S-1206B46-I6T2U |
| $4.7 \mathrm{~V} \pm 1.0 \%$ | S-1206B47-M3T1x | S-1206B47-U3T1x | S-1206B47-I6T2U |
| $4.8 \mathrm{~V} \pm 1.0 \%$ | S-1206B48-M3T1x | S-1206B48-U3T1x | S-1206B48-16T2U |
| $4.9 \mathrm{~V} \pm 1.0 \%$ | S-1206B49-M3T1x | S-1206B49-U3T1x | S-1206B49-16T2U |
| $5.0 \mathrm{~V} \pm 1.0 \%$ | S-1206B50-M3T1x | S-1206B50-U3T1x | S-1206B50-16T2U |
| $5.1 \mathrm{~V} \pm 1.0 \%$ | S-1206B51-M3T1x | S-1206B51-U3T1x | S-1206B51-16T2U |
| $5.2 \mathrm{~V} \pm 1.0 \%$ | S-1206B52-M3T1x | S-1206B52-U3T1x | S-1206B52-I6T2U |

Remark 1. Please contact our sales office for products with output voltage values other than the above.
2. $x: G$ or $U$
3. Please select products of environmental code $=U$ for $S n 100 \%$, halogen-free products.

## ■ Pin Configurations

SOT-23-3
Top view


Figure 2

SOT-89-3
Top view


Figure 3


Figure 4

Table 2

| Pin No. | Symbol | Description |
| :---: | :---: | :--- |
| 1 | VIN | Input voltage pin |
| 2 | VSS | GND pin |
| 3 | VOUT | Output voltage pin |

Table 3

| Pin No. | Symbol | Description |
| :---: | :---: | :--- |
| 1 | VSS | GND pin |
| 2 | VIN | Input voltage pin |
| 3 | VOUT | Output voltage pin |

Table 4

| Pin No. | Symbol | Description |
| :---: | :---: | :--- |
| 1 | VOUT | Output voltage pin |
| 2 | VIN | Input voltage pin |
| 3 | VSS | GND pin |
| 4 | NC $^{* 1}$ | No connection |
| 5 | VIN | Input voltage pin |
| 6 | NC $^{* 1}$ | No connection |

*1. The NC pin is electrically open.
The NC pin can be connected to VIN or VSS.

## Absolute Maximum Ratings

Table 5
*1. When mounted on board
[Mounted board]
(1) Board size : $114.3 \mathrm{~mm} \times 76.2 \mathrm{~mm} \times$ t1.6 mm
(2) Board name : JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.


Figure 5 Power Dissipation of Package (When Mounted on Board)

ULTRA LOW CURRENT CONSUMPTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR
Rev.3.2_02 S-1206 Series

## ■ Electrical Characteristics

Table 6

| Item | Symbol | Conditions |  | Min. | Typ. | Max. | Unit | Test Circuit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output voltage ${ }^{* 1}$ | $\mathrm{V}_{\text {OUT(E) }}$ | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=30 \mathrm{~mA}, \\ & 1.2 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }}<1.5 \mathrm{~V} \\ & \hline \end{aligned}$ |  | $V_{\text {OUT(S) }}$ -15 mV <br> $-15 \mathrm{mV}$ | $V_{\text {OUT(S) }}$ | $\begin{array}{r} \hline V_{\text {OUT }(S)} \\ +15 \mathrm{mV} \\ \hline \end{array}$ | V | 1 |
|  |  | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=30 \mathrm{~mA}, \\ & 1.5 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }} \end{aligned}$ |  | $\begin{aligned} & \hline V_{\text {OUT (S) }} \\ & \times 0.99 \\ & \hline \end{aligned}$ | $V_{\text {OUT(S) }}$ | $\begin{aligned} & V_{\text {out }}(\mathrm{S}) \\ & \times 1.01 \\ & \hline \end{aligned}$ | V | 1 |
| Output current ${ }^{*}{ }^{2}$ | lout | $\mathrm{V}_{\text {IN }} \geq \mathrm{V}_{\text {OUT }} \mathrm{S}$ ( $+1.0 \mathrm{~V}, 1.2 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }}<1.5 \mathrm{~V}$ |  | $150^{* 5}$ | - | - | mA | 3 |
|  |  | $\mathrm{V}_{\text {IN }} \geq \mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}, 1.5 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }}$ |  | $250{ }^{* 5}$ | - | - | mA | 3 |
| Dropout voltage ${ }^{* 3}$ | $\mathrm{V}_{\text {drop }}$ | $\mathrm{l}_{\text {Out }}=100 \mathrm{~mA}$ | $1.2 \mathrm{~V} \leq \mathrm{V}_{\text {OUT (S) }}<1.3 \mathrm{~V}$ | 0.5 | 0.54 | 0.81 | V | 1 |
|  |  |  | $1.3 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }(\text { S }}<1.4 \mathrm{~V}$ | - | 0.50 | 0.73 | V | 1 |
|  |  |  | $1.4 \mathrm{~V} \leq \mathrm{V}_{\text {OUt(S) }}<1.5 \mathrm{~V}$ | - | 0.43 | 0.66 | V | 1 |
|  |  |  | $1.5 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }(\text { S }}<1.7 \mathrm{~V}$ | - | 0.35 | 0.53 | V | 1 |
|  |  |  | $1.7 \mathrm{~V} \leq \mathrm{V}_{\text {OUT (S) }}<1.9 \mathrm{~V}$ | - | 0.33 | 0.50 | V | 1 |
|  |  |  | $1.9 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }}<2.1 \mathrm{~V}$ | - | 0.26 | 0.43 | V | 1 |
|  |  |  | $2.1 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }}(\mathrm{S})<3.0 \mathrm{~V}$ | - | 0.23 | 0.36 | V | 1 |
|  |  |  | $3.0 \mathrm{~V} \leq \mathrm{V}_{\text {OUT(S) }} \leq 5.2 \mathrm{~V}$ | - | 0.15 | 0.23 | V | 1 |
| Line regulation | $\Delta \mathrm{V}_{\text {OUT1 }}$ | $\mathrm{V}_{\text {OUT(S) }}+0.5 \mathrm{~V} \leq \mathrm{V}_{\mathbb{N}} \leq 6.5 \mathrm{~V}$ | $\mathrm{I}_{\text {OUT }}=1 \mu \mathrm{~A}$ | - | 0.05 | 0.2 | \%/V | 1 |
|  | $\overline{\Delta \mathrm{V}_{\text {IN }} \bullet \mathrm{V}_{\text {OUT }}}$ |  | $\mathrm{l}_{\text {OUT }}=30 \mathrm{~mA}$ | - | 0.05 | 0.2 | \%/V | 1 |
| Load regulation | $\Delta \mathrm{V}_{\text {OUT2 }}$ | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}, 1 \mu \mathrm{~A} \leq \mathrm{I}_{\text {OUT }} \leq 100 \mathrm{~mA}$ |  | - | 20 | 40 | mV | 1 |
| Output voltage temperature coefficient ${ }^{*}$ | $\frac{\Delta \mathrm{V}_{\text {OUT }}}{\Delta \mathrm{Ta} \bullet \mathrm{~V}_{\text {OUT }}}$ | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT }(S)}+1.0 \mathrm{~V}, \text { I IUT } \\ & -40^{\circ} \mathrm{C} \leq \mathrm{Ta} \leq 85^{\circ} \mathrm{C} \end{aligned}$ |  | - | $\pm 120$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 1 |
| Current consumption during operation | $\mathrm{I}_{\text {S } 1}$ | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}$, no load |  | - | 1.0 | 1.5 | $\mu \mathrm{A}$ | 2 |
| Input voltage | $\mathrm{V}_{\text {IN }}$ | $\mathrm{V}^{-}$ |  | 1.7 | - | 6.5 | V | - |
| Short-circuit current | ISHORT | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT (S) }}+1.0 \mathrm{~V}, \\ & \mathrm{~V}_{\text {OUT }}=0 \mathrm{~V} \end{aligned}$ | $1.2 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }}<2.3 \mathrm{~V}$ | - | 130 | - | mA | 3 |
|  |  |  | $2.3 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }} \leq 5.2 \mathrm{~V}$ | - | 100 | - | mA | 3 |

*1. $\mathrm{V}_{\mathrm{OUT}(\mathrm{s})}$ : Set output voltage
$V_{\text {OUT(E) }}$ : Actual output voltage
Output voltage when fixing $\mathrm{I}_{\text {OUT }}(=30 \mathrm{~mA})$ and inputting $\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}$
*2. The output current at which the output voltage becomes $95 \%$ of $\mathrm{V}_{\mathrm{OUT}(\mathrm{E})}$ after gradually increasing the output current.
*3. $\mathrm{V}_{\text {drop }}=\mathrm{V}_{\text {IN } 1}-\left(\mathrm{V}_{\text {OUT3 }} \times 0.98\right)$
$\mathrm{V}_{\text {OUT3 }}$ is the output voltage when $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }}+1.0 \mathrm{~V}$ and $\mathrm{I}_{\text {OUT }}=100 \mathrm{~mA}$.
$\mathrm{V}_{\text {IN } 1}$ is the input voltage at which the output voltage becomes $98 \%$ of $\mathrm{V}_{\text {оит3 }}$ after gradually decreasing the input voltage.
*4. A change in the temperature of the output voltage $\left[\mathrm{mV} /{ }^{\circ} \mathrm{C}\right]$ is calculated using the following equation.
$\frac{\Delta \mathrm{V}_{\text {OUT }}}{\Delta \mathrm{Ta}}\left[\mathrm{mV} /{ }^{\circ} \mathrm{C}\right]^{* 1}=\mathrm{V}_{\text {OUT(S) }}[\mathrm{V}]^{* 2} \times \frac{\Delta \mathrm{V}_{\text {OUT }}}{\Delta \mathrm{Ta} \cdot \mathrm{V}_{\text {OUT }}}\left[\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right]^{* 3} \div 1000$
*1. Change in temperature of output voltage
*2. Set output voltage
*3. Output voltage temperature coefficient
*5. The output current can be at least this value.
Due to restrictions on the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.
This specification is guaranteed by design.

## $■$ Test Circuits

1. 



Figure 6
2.


Figure 7
3.


Figure 8

## ■ Standard Circuit


*1. A capacitor for stabilizing the input.
*2. A ceramic capacitor of $0.1 \mu \mathrm{~F}$ or more can be used.
Figure 9
Caution The above connection diagram and constant will not guarantee successful operation. Perform thorough evaluation using the actual application to set the constant.

## ■ Condition of Application

Input capacitor $\left(\mathrm{C}_{\mathbb{N}}\right)$ : $0.1 \mu \mathrm{~F}$ or more
Output capacitor ( $\mathrm{C}_{\mathrm{L}}$ ): $0.1 \mu \mathrm{~F}$ or more
Caution Generally a series regulator may cause oscillation, depending on the selection of external parts. Confirm that no oscillation occurs in the application for which the above capacitors are used.

## ■ Selection of Input and Output Capacitors ( $\mathrm{C}_{\mathrm{IN}}, \mathrm{C}_{\mathrm{L}}$ )

The S-1206 Series requires an output capacitor between the VOUT pin and VSS pin for phase compensation. Operation is stabilized by a ceramic capacitor with an output capacitance of $0.1 \mu \mathrm{~F}$ or more in the entire temperature range. When using an OS capacitor, a tantalum capacitor, or an aluminum electrolytic capacitor, the capacitance must be $0.1 \mu \mathrm{~F}$ or more.
The value of the output overshoot or undershoot transient response varies depending on the value of the output capacitor.
The required capacitance of the input capacitor differs depending on the application.
The recommended value for an application is $\mathrm{C}_{\mathbb{N}} \geq 0.1 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{L}} \geq 0.1 \mu \mathrm{~F}$; however, when selecting these capacitors, perform sufficient evaluation, including evaluation of temperature characteristics, on the actual device.

## ■ Explanation of Terms

## 1. Low Dropout Voltage Regulator

This voltage regulator has the low dropout voltage due to its built-in low on-resistance transistor.

## 2. Low Equivalent Series Resistance

A capacitor whose equivalent series resistance ( $\mathrm{R}_{\text {ESR }}$ ) is low. The $\mathrm{S}-1206$ Series enables use of a low equivalent series resistance capacitor, such as a ceramic capacitor, for the output-side capacitor ( $\mathrm{C}_{\mathrm{L}}$ ).

## 3. Output Voltage (Vout)

The accuracy of the output voltage is ensured at $\pm 1.0 \%$ or $\pm 15 \mathrm{mV}^{* 2}$ under the specified conditions of fixed input voltage ${ }^{* 1}$, fixed output current, and fixed temperature.
*1. Differs depending on the product.
*2. When $\mathrm{V}_{\text {OUt }}<1.5 \mathrm{~V}: \pm 15 \mathrm{mV}$, When $1.5 \mathrm{~V} \leq \mathrm{V}_{\text {OUt }}: \pm 1.0 \%$
Caution If the above conditions change, the output voltage value may vary and exceed the accuracy range of the output voltage. Refer to the " $■$ Electrical Characteristics" and "■ Characteristics (Typical Data)" for details.
4. Line Regulation $\left(\frac{\Delta V_{\text {out1 }}}{\Delta \mathrm{VIN} \bullet \text { Vout }}\right)$

Indicates the dependency of the output voltage on the input voltage. That is, the values show how much the output voltage changes due to a change in the input voltage with the output current remaining unchanged.

## 5. Load Regulation ( $\Delta \mathbf{V}_{\text {OUT2 }}$ )

Indicates the dependency of the output voltage on the output current. That is, the values show how much the output voltage changes due to a change in the output current with the input voltage remaining unchanged.

## 6. Dropout Voltage ( $\mathrm{V}_{\text {drop }}$ )

Indicates the difference between input voltage $\left(\mathrm{V}_{\mathbb{I N} 1}\right)$ and the output voltage when; decreasing input voltage $\left(\mathrm{V}_{\mathbb{I N}}\right)$ gradually until the output voltage has dropped out to the value of $98 \%$ of output voltage $\left(\mathrm{V}_{\text {OUT3 }}\right)$, which is at $\mathrm{V}_{\mathbb{I N}}=$ $\mathrm{V}_{\text {OUt(s) }}+1.0 \mathrm{~V}$.

$$
V_{\text {drop }}=V_{\text {IN } 1}-\left(V_{\text {OUT3 }} \times 0.98\right)
$$

7. Output Voltage Temperature Coefficient $\left(\frac{\Delta \text { Vout }}{\Delta \mathrm{Ta} \bullet \text { Vout }}\right)$

The shaded area in Figure 10 is the range where $V_{\text {OUT }}$ varies in the operation temperature range when the output voltage temperature coefficient is $\pm 120 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$.

Example of S-1206B30 typ. product

*1. $\mathrm{V}_{\text {OUT(E) }}$ is the value of the output voltage measured at $\mathrm{Ta}=+25^{\circ} \mathrm{C}$.
Figure 10
A change in the temperature of the output voltage $\left[\mathrm{mV} /{ }^{\circ} \mathrm{C}\right]$ is calculated using the following equation.

$$
\frac{\Delta \mathrm{V}_{\text {OUT }}}{\Delta \mathrm{Ta}}\left[\mathrm{mV} /{ }^{\circ} \mathrm{C}\right]^{* 1}=\mathrm{V}_{\text {OUT(S) }}[\mathrm{V}]^{* 2} \times \frac{\Delta \mathrm{V}_{\text {OUT }}}{\Delta \mathrm{Ta} \bullet \mathrm{~V}_{\text {OUT }}}\left[\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right]^{* 3} \div 1000
$$

*1. Change in temperature of output voltage
*2. Set output voltage
*3. Output voltage temperature coefficient

## Operation

## 1. Basic Operation

Figure 11 shows the block diagram of the S-1206 Series.
The error amplifier compares the reference voltage ( $\mathrm{V}_{\text {ref }}$ ) with feedback voltage $\left(\mathrm{V}_{\mathrm{fb}}\right)$, which is the output voltage resistance-divided by feedback resistors ( $\mathrm{R}_{\mathrm{s}}$ and $\mathrm{R}_{\mathrm{f}}$ ). It supplies the gate voltage necessary to maintain the constant output voltage which is not influenced by the input voltage and temperature change, to the output transistor.

*1. Parasitic diode

Figure 11

## 2. Output Transistor

In the S-1206 Series, a low on-resistance P-channel MOS FET is used as the output transistor.
Be sure that $\mathrm{V}_{\text {OUT }}$ does not exceed $\mathrm{V}_{\mathbb{N}}+0.3 \mathrm{~V}$ to prevent the voltage regulator from being damaged due to reverse current flowing from the VOUT pin through a parasitic diode to the VIN pin, when the potential of $\mathrm{V}_{\text {OUt }}$ became higher than $\mathrm{V}_{\mathrm{in}}$.

## 3. Overcurrent Protection Circuit

The S-1206 Series includes an overcurrent protection circuit having the characteristics shown in "1. Output Voltage vs. Output Current ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )" in "■ Characteristics (Typical Data)", in order to protect the output transistor against an excessive output current and short circuiting between the VOUT and VSS pins. The current when the output pin is short-circuited ( $\mathrm{I}_{\text {SHORT }}$ ) is internally set at approx. 130 mA (typ.) ( $1.2 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }}<2.3 \mathrm{~V}$ ) or approx. 100 mA (typ.) $\quad\left(2.3 \mathrm{~V} \leq \mathrm{V}_{\text {OUT }} \leq 5.2 \mathrm{~V}\right)$, and the normal value is restored for the output voltage, if releasing a short circuit once.

Caution This overcurrent protection circuit does not work as for thermal protection. If this IC long keeps short circuiting inside, pay attention to the conditions of input voltage and load current so that, under the usage conditions including short circuit, the loss of the IC will not exceed power dissipation of the package.

## ■ Precautions

- Wiring patterns for the VIN pin, the VOUT pin and GND should be designed so that the impedance is low. When mounting an output capacitor between the VOUT pin and the VSS pin $\left(C_{L}\right)$ and a capacitor for stabilizing the input between the VIN pin and the VSS pin ( $\mathrm{C}_{\mathbb{I}}$ ), the distance from the capacitors to these pins should be as short as possible.
- Note that generally the output voltage may increase when a series regulator is used at low load current ( $10 \mu \mathrm{~A}$ or less).
- Note that generally the output voltage may increase due to the leakage current from an output driver when a series regulator is used at high temperature.
- Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for the S-1206 Series. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics. Refer to " 5 . Equivalent Series Resistance vs. Output Current Characteristics Example ( $\mathrm{Ta}=\mathbf{2 5 ^ { \circ }} \mathbf{C}$ )" in "■ Reference Data" for the equivalent series resistance ( $\mathrm{R}_{\mathrm{ESR}}$ ) of the output capacitor.

```
Input capacitor (\mp@subsup{C}{|N}{}): 0.1 \mu\textrm{F}\mathrm{ or more}
Output capacitor (\mp@subsup{C}{L}{}): 0.1 \mu\textrm{F}\mathrm{ or more}
```

Use an I/O capacitor with good temperature characteristics (conforming to the ceramic capacitor EIA X5R (JIS B) characteristics).

- The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitance is small or an input capacitor is not connected.
- If the output capacitance is small, power supply's fluctuation and the characteristics of load fluctuation become worse. Sufficiently evaluate the output voltage's fluctuation with the actual device.
- Overshoot may occur in the output voltage momentarily if the voltage is rapidly raised at power-on or when the power supply fluctuates. Sufficiently evaluate the output voltage at power-on with the actual device.
- The application conditions for the input voltage, the output voltage, and the load current should not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- In determining the output current, attention should be paid to the output current value specified in Table 6 in " Electrical Characteristics" and footnote *5 of the table.
- ABLIC Inc. claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.


## ■ Characteristics (Typical Data)

1. Output Voltage vs. Output Current ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

S-1206B12


S-1206B50

2. Output Voltage vs. Input Voltage $\left(\mathbf{T a}=25^{\circ} \mathrm{C}\right)$

S-1206B12


S-1206B50


S-1206B30


Remark In determining the output current, attention should be paid to the following.

1. Minimum output current value specified in Table 6 and footnote *5 in "■ Electrical Characteristics"
2. Power dissipation of package

S-1206B30


## 3. Dropout Voltage vs. Output Current

## S-1206B12 <br> 

S-1206B30


S-1206B50

4. Dropout Voltage vs. Set Output Voltage


## 5. Output Voltage vs. Ambient Temperature

S-1206B12


S-1206B50


## 6. Current Consumption vs. Input Voltage

## S-1206B12



S-1206B50


S-1206B30

S-1206B30


## 7. Current Consumption vs. Ambient Temperature

S-1206B12


S-1206B50


S-1206B30


## ■ Reference Data

## 1. Input Transient Response Characteristics ( $\mathrm{Ta}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )

S-1206B12


S-1206B30


S-1206B50





## 2. Load Transient Response Characteristics ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

## S-1206B12


$\mathrm{V}_{\text {IN }}=2.2 \mathrm{~V}$, $\mathrm{l}_{\text {OUT }}=50 \mathrm{~mA} \leftrightarrow 100 \mathrm{~mA}$


S-1206B30


S-1206B50

$\mathrm{V}_{\text {IN }}=6.0 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=50 \mathrm{~mA} \leftrightarrow 100 \mathrm{~mA}$


## 3. Input Voltage Transient Response Characteristics ( $\mathrm{Ta}=\mathbf{2 5 ^ { \circ }} \mathrm{C}$ )

S-1206B12


S-1206B30
$\mathrm{V}_{\text {IN }}=4.0 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=1 \mathrm{~mA}$


S-1206B50





## 4. Ripple Rejection $\left(\mathbf{T a}=25^{\circ} \mathrm{C}\right)$

S-1206B12


S-1206B50


S-1206B30

5. Equivalent Series Resistance vs. Output Current Characteristics Example ( $\mathrm{Ta}=\mathbf{2 5}^{\circ} \mathrm{C}$ )

$$
\begin{aligned}
\mathrm{C}_{\mathrm{L}}: & \text { Murata Manufacturing Co., Ltd. } \\
& \text { GRM115R71C104K }(0.1 \mu \mathrm{~F})
\end{aligned}
$$



## ■ Marking Specifications

## 1. SOT-23-3


(1) to (3) : Product abbreviation (refer to Product Name vs. Product Code)
(4) : Lot number

Product Name vs. Product Code

| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B12-M3T1x | S | A | A |
| S-1206B13-M3T1x | S | A | B |
| S-1206B14-M3T1x | S | A | C |
| S-1206B15-M3T1x | S | A | D |
| S-1206B16-M3T1x | S | A | E |
| S-1206B17-M3T1x | S | A | F |
| S-1206B18-M3T1x | S | A | G |
| S-1206B1J-M3T1x | S | A | H |
| S-1206B19-M3T1x | S | A | I |
| S-1206B20-M3T1x | S | A | J |
| S-1206B21-M3T1x | S | A | K |
| S-1206B22-M3T1x | S | A | L |
| S-1206B23-M3T1x | S | A | M |
| S-1206B24-M3T1x | S | A | N |
| S-1206B25-M3T1x | S | A | O |
| S-1206B26-M3T1x | S | A | P |
| S-1206B27-M3T1x | S | A | Q |
| S-1206B28-M3T1x | S | A | R |
| S-1206B2J-M3T1x | S | A | S |
| S-1206B29-M3T1x | S | A | T |
| S-1206B30-M3T1x | S | A | U |
| S-1206B31-M3T1x | S | A | V |


| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B32-M3T1x | S | A | W |
| S-1206B33-M3T1x | S | A | X |
| S-1206B34-M3T1x | S | A | Y |
| S-1206B35-M3T1x | S | A | Z |
| S-1206B36-M3T1x | S | B | A |
| S-1206B37-M3T1x | S | B | B |
| S-1206B38-M3T1x | S | B | C |
| S-1206B39-M3T1x | S | B | D |
| S-1206B40-M3T1x | S | B | E |
| S-1206B41-M3T1x | S | B | F |
| S-1206B42-M3T1x | S | B | G |
| S-1206B43-M3T1x | S | B | H |
| S-1206B44-M3T1x | S | B | I |
| S-1206B45-M3T1x | S | B | J |
| S-1206B46-M3T1x | S | B | K |
| S-1206B47-M3T1x | S | B | L |
| S-1206B48-M3T1x | S | B | M |
| S-1206B49-M3T1x | S | B | N |
| S-1206B50-M3T1x | S | B | O |
| S-1206B51-M3T1x | S | B | P |
| S-1206B52-M3T1x | S | B | Q |

Remark 1. $\mathrm{x}: \mathrm{G}$ or U
2. Please select products of environmental code $=U$ for $S n 100 \%$, halogen-free products.

ULTRA LOW CURRENT CONSUMPTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

## 2. SOT-89-3

Top view

(1) to (3) : Product abbreviation (refer to Product Name vs. Product Code)
(4) to (6) : Lot number

Product Name vs. Product Code

| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B12-U3T1x | S | A | A |
| S-1206B13-U3T1x | S | A | B |
| S-1206B14-U3T1x | S | A | C |
| S-1206B15-U3T1x | S | A | D |
| S-1206B16-U3T1x | S | A | E |
| S-1206B17-U3T1x | S | A | F |
| S-1206B18-U3T1x | S | A | G |
| S-1206B1J-U3T1x | S | A | H |
| S-1206B19-U3T1x | S | A | I |
| S-1206B20-U3T1x | S | A | J |
| S-1206B21-U3T1x | S | A | K |
| S-1206B22-U3T1x | S | A | L |
| S-1206B23-U3T1x | S | A | M |
| S-1206B24-U3T1x | S | A | N |
| S-1206B25-U3T1x | S | A | O |
| S-1206B26-U3T1x | S | A | P |
| S-1206B27-U3T1x | S | A | Q |
| S-1206B28-U3T1x | S | A | $R$ |
| S-1206B2J-U3T1x | S | A | S |
| S-1206B29-U3T1x | S | A | T |
| S-1206B30-U3T1x | S | A | U |
| S-1206B31-U3T1x | S | A | V |


| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B32-U3T1x | S | A | W |
| S-1206B33-U3T1x | S | A | X |
| S-1206B34-U3T1x | S | A | Y |
| S-1206B35-U3T1x | S | A | Z |
| S-1206B36-U3T1x | S | B | A |
| S-1206B37-U3T1x | S | B | B |
| S-1206B38-U3T1x | S | B | C |
| S-1206B39-U3T1x | S | B | D |
| S-1206B40-U3T1x | S | B | E |
| S-1206B41-U3T1x | S | B | F |
| S-1206B42-U3T1x | S | B | G |
| S-1206B43-U3T1x | S | B | H |
| S-1206B44-U3T1x | S | B | I |
| S-1206B45-U3T1x | S | B | J |
| S-1206B46-U3T1x | S | B | K |
| S-1206B47-U3T1x | S | B | L |
| S-1206B48-U3T1x | S | B | M |
| S-1206B49-U3T1x | S | B | N |
| S-1206B50-U3T1x | S | B | O |
| S-1206B51-U3T1x | S | B | P |
| S-1206B52-U3T1x | S | B | Q |

Remark 1. $\mathrm{x}: \mathrm{G}$ or U
2. Please select products of environmental code $=U$ for $S n 100 \%$, halogen-free products.

## 3. SNT-6A(H)

Top view

(1) to (3) : Product abbreviation (refer to Product Name vs. Product Code)
(4) to (6) : Lot number

Product Name vs. Product Code

| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B12-I6T2U | S | A | A |
| S-1206B13-I6T2U | S | A | B |
| S-1206B14-I6T2U | S | A | C |
| S-1206B15-I6T2U | S | A | D |
| S-1206B16-I6T2U | S | A | E |
| S-1206B17-I6T2U | S | A | F |
| S-1206B18-I6T2U | S | A | G |
| S-1206B1J-I6T2U | S | A | H |
| S-1206B19-I6T2U | S | A | I |
| S-1206B20-I6T2U | S | A | J |
| S-1206B21-I6T2U | S | A | K |
| S-1206B22-I6T2U | S | A | L |
| S-1206B23-I6T2U | S | A | M |
| S-1206B24-I6T2U | S | A | N |
| S-1206B25-I6T2U | S | A | O |
| S-1206B26-I6T2U | S | A | P |
| S-1206B27-I6T2U | S | A | Q |
| S-1206B28-I6T2U | S | A | R |
| S-1206B2J-I6T2U | S | A | S |
| S-1206B29-I6T2U | S | A | T |
| S-1206B30-I6T2U | S | A | U |
| S-1206B31-I6T2U | S | A | V |


| Product Name | Product Code |  |  |
| :--- | :---: | :---: | :---: |
|  | $(1)$ | $(2)$ | $(3)$ |
| S-1206B32-I6T2U | S | A | W |
| S-1206B33-I6T2U | S | A | X |
| S-1206B34-I6T2U | S | A | Y |
| S-1206B35-I6T2U | S | A | Z |
| S-1206B36-I6T2U | S | B | A |
| S-1206B37-I6T2U | S | B | B |
| S-1206B38-I6T2U | S | B | C |
| S-1206B39-I6T2U | S | B | D |
| S-1206B40-I6T2U | S | B | E |
| S-1206B41-I6T2U | S | B | F |
| S-1206B42-I6T2U | S | B | G |
| S-1206B43-I6T2U | S | B | H |
| S-1206B44-I6T2U | S | B | I |
| S-1206B45-I6T2U | S | B | J |
| S-1206B46-I6T2U | S | B | K |
| S-1206B47-I6T2U | S | B | L |
| S-1206B48-I6T2U | S | B | M |
| S-1206B49-I6T2U | S | B | N |
| S-1206B50-I6T2U | S | B | O |
| S-1206B51-I6T2U | S | B | P |
| S-1206B52-I6T2U | S | B | Q |



| TITLE | SOT233-C-PKG Dimensions |
| :---: | :---: |
| No. | MP003-C-P-SD-1.1 |
| ANGLE | $\square$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP003-C-C-SD-2.0

| TITLE | SOT233-C-Carrier Tape |
| :---: | :---: |
| No. | MP003-C-C-SD-2.0 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP003-Z-R-SD-1.0

| TITLE | SOT233-C-Reel |  |  |
| :---: | :---: | :---: | :---: |
|  | MP003-Z-R-SD-1.0 |  |  |
| ANGLE |  | QTY. |  |
| UNIT | mm |  |  |
|  |  |  |  |
|  | ABLIC Inc. |  |  |



No. UP003-A-P-SD-2.0

| TITLE | SOT893-A-PKG Dimensions |
| :---: | :---: |
| No. | UP003-A-P-SD-2.0 |
| ANGLE | $\rightarrow$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. UP003-A-C-SD-2.0

| TITLE | SOT893-A-Carrier Tape |
| :---: | :---: |
| No. | UP003-A-C-SD-2.0 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |




No. P1006-A-P-SD-2. 1

| TITLE | SNT-6A(H)-A-PKG Dimensions |
| :---: | :---: |
| No. | PI006-A-P-SD-2.1 |
| ANGLE | $\nrightarrow$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. PI006-A-C-SD-2.0

| TITLE | SNT-6A(H)-A-Carrier Tape |
| :---: | :---: |
| No. | PI006-A-C-SD-2.0 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. PI006-A-R-SD-1.0

| TITLE | SNT-6A(H)-A-Reel |  |  |
| :---: | :---: | :---: | :---: |
| No. | PIO06-A-R-SD-1.0 |  |  |
| ANGLE |  | QTY. |  |
| UNIT | mm | 50 |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

ABLIC Inc.

※1．ランドパターンの幅に注意してください（ 0.25 mm min．／ 0.30 mm typ．）。
※2．パッケージ中央にランドパターンを広げないでください $(1.30 \mathrm{~mm} \sim 1.40 \mathrm{~mm})$ 。
注意 1．パッケージのモールド樹脂下にシルク印刷やハンダ印刷などしないでください。
2．パッケージ下の配線上のソルダーレジストなどの厚みをランドパターン表面から 0.03 mm以下にしてください。
3．マスク開ロサイズと開ロ位置はランドパターンと合わせてください。
4．詳細は＂SNTパッケージ活用の手引き＂を参照してください。
※1．Pay attention to the land pattern width（ 0.25 mm min．／ 0.30 mm typ．）．
$※ 2$ ．Do not widen the land pattern to the center of the package（ 1.30 mm to 1.40 mm ）．
Caution 1．Do not do silkscreen printing and solder printing under the mold resin of the package．
2．The thickness of the solder resist on the wire pattern under the package should be 0.03 mm or less from the land pattern surface．
3．Match the mask aperture size and aperture position with the land pattern．
4．Refer to＂SNT Package User＇s Guide＂for details．
※1．请注意焊盘模式的宽度（ $0.25 \mathrm{~mm} \mathrm{~min} . / 0.30 \mathrm{~mm}$ typ．）。
※2．请勿向封装中间扩展焊盘模式（ $1.30 \mathrm{~mm} \sim 1.40 \mathrm{~mm}$ ）。
注意 1．请勿在树脂型封装的下面印刷丝网，焊锡。
2．在封装下，布线上的阻焊膜厚度（从焊盘模式表面起）请控制在 0.03 mm 以下。
3．钢网的开口尺寸和开口位置请与焊盘模式对齐。
4．详细内容请参阅＂SNT 封装的应用指南＂。

No．PI006－A－L－SD－4． 1

| TITLE | SNT－6A（H）－A <br> －Land Recommendation |
| :---: | :---: |
| No． | PI006－A－L－SD－4．1 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc． |  |

## Disclaimers (Handling Precautions)

1. All the information described herein (product data, specifications, figures, tables, programs, algorithms and application circuit examples, etc.) is current as of publishing date of this document and is subject to change without notice.
2. The circuit examples and the usages described herein are for reference only, and do not guarantee the success of any specific mass-production design.
ABLIC Inc. is not responsible for damages caused by the reasons other than the products described herein (hereinafter "the products") or infringement of third-party intellectual property right and any other right due to the use of the information described herein.
3. ABLIC Inc. is not responsible for damages caused by the incorrect information described herein.
4. Be careful to use the products within their specified ranges. Pay special attention to the absolute maximum ratings, operation voltage range and electrical characteristics, etc.
ABLIC Inc. is not responsible for damages caused by failures and / or accidents, etc. that occur due to the use of the products outside their specified ranges.
5. When using the products, confirm their applications, and the laws and regulations of the region or country where they are used and verify suitability, safety and other factors for the intended use.
6. When exporting the products, comply with the Foreign Exchange and Foreign Trade Act and all other export-related laws, and follow the required procedures.
7. The products must not be used or provided (exported) for the purposes of the development of weapons of mass destruction or military use. ABLIC Inc. is not responsible for any provision (export) to those whose purpose is to develop, manufacture, use or store nuclear, biological or chemical weapons, missiles, or other military use.
8. The products are not designed to be used as part of any device or equipment that may affect the human body, human life, or assets (such as medical equipment, disaster prevention systems, security systems, combustion control systems, infrastructure control systems, vehicle equipment, traffic systems, in-vehicle equipment, aviation equipment, aerospace equipment, and nuclear-related equipment), excluding when specified for in-vehicle use or other uses. Do not apply the products to the above listed devices and equipments without prior written permission by ABLIC Inc.
Especially, the products cannot be used for life support devices, devices implanted in the human body and devices that directly affect human life, etc.
Prior consultation with our sales office is required when considering the above uses.
ABLIC Inc. is not responsible for damages caused by unauthorized or unspecified use of our products.
9. Semiconductor products may fail or malfunction with some probability.

The user of the products should therefore take responsibility to give thorough consideration to safety design including redundancy, fire spread prevention measures, and malfunction prevention to prevent accidents causing injury or death, fires and social damage, etc. that may ensue from the products' failure or malfunction.
The entire system must be sufficiently evaluated and applied on customer's own responsibility.
10. The products are not designed to be radiation-proof. The necessary radiation measures should be taken in the product design by the customer depending on the intended use.
11. The products do not affect human health under normal use. However, they contain chemical substances and heavy metals and should therefore not be put in the mouth. The fracture surfaces of wafers and chips may be sharp. Be careful when handling these with the bare hands to prevent injuries, etc.
12. When disposing of the products, comply with the laws and ordinances of the country or region where they are used.
13. The information described herein contains copyright information and know-how of ABLIC Inc.

The information described herein does not convey any license under any intellectual property rights or any other rights belonging to ABLIC Inc. or a third party. Reproduction or copying of the information from this document or any part of this document described herein for the purpose of disclosing it to a third-party without the express permission of ABLIC Inc. is strictly prohibited.
14. For more details on the information described herein, contact our sales office.

## X-ON Electronics

Largest Supplier of Electrical and Electronic Components
Click to view similar products for Ablic manufacturer:
Other Similar products are found below :
S-1009N25I-M5T1U S-93C86BD4I-T8T1U S-4610A-CHIP S-80133CNMC-JKST2G S-80818CLUA-B6DT2G S-80821CLY-B-G S-80821CNNB-B8GT2G S-80822CNMC-B8HT2G S-80923CNMC-G8TT2G S-812C25AY-B-G S-812C33BPI-C4NTFG S-812C36AY-B-G S-812C40AMC-C2U-T2 S-817B60AUA-CXXT2G S-8341A50AFT-T2-G S-L2985A25-H4T1 S-L2985B33-H4T1 S-24CS08AFJ-TBK-1G S-80927CNMC-G8XT2U S-1003NA27I-I6T1U S-1206B33-M3T1U S-8261ABRMD-G3RT2S S-85S1PD25-I8T1U S-812C40AMCC2UT2U S-85S1AB18-I6T1U S-82B1AAB-I6T1U S-8254AAJFT-TB-S S-5712ACSL1-M3T1U S-5743NBH1A-M3T4U S-5712BNDL2M3T1U S-8261DAI-M6T1U S-5716ANDL3-M3T1U S-5470A21I-M5T1U S-8261AAGMD-G2GT2S S-1002CA29I-M5T1U S-1741A21A6T2U4 S-1009C31I-N4T1U S-80842CNNB-B83T2U S-5715ENSL1-M3T1U S-85S1AB20-I6T1U S-1172B30-E6T1U S-8261ACEMDG4ET2S S-58LM20A-I4T1U S-1003NA23I-M5T1U S-1313A28-M5T1U3 S-817A18ANB-CUHT2G S-1313A28-N4T1U3 S-8520F50MCBOJT2U S-89431ACNC-HBVTFG S-5712BCDH2-I4T1U

