STEP-UP, SUPER-SMALL PACKAGE, 600 kHz , PWM CONTROL or
PWM/PFM SWITCHABLE SWITCHING REGULATOR CONTROLLER

The S-8355/56/57/58 Series is a CMOS step-up switching regulator controller which mainly consists of a reference voltage source, an oscillation circuit, an error amplifier, a phase compensation circuit, a PWM control circuit (S-8355/57 Series) and a PWM/PFM switching control circuit (S-8356/58 Series).
With an external low-ON-resistance Nch Power MOS, this product is ideal for applications requiring high efficiency and a high output current.
The S-8355/57 Series realizes low ripple, high efficiency, and excellent transient characteristics due to its PWM control circuit whose duty ratio can be varied linearly from 0 to $83 \%$ (from 0 to $78 \%$ for $250 \mathrm{kHz}, 300 \mathrm{kHz}$, and 600 kHz models), an excellently designed error amplifier and a phase compensation circuits.
S-8356/58 Series features a PWM/PFM switching controller that can switch the operation to a PFM controller with a duty ratio is $15 \%$ under a light load to prevent a decline in the efficiency due to the IC operating current.

## Features

- Low voltage operation : Startup at 0.9 V min. (lout $=1 \mathrm{~mA})$ guaranteed
- Low current consumption: During operation $25.9 \mu \mathrm{~A}(3.3 \mathrm{~V}, 100 \mathrm{kHz}$, typ.) During shutdown $0.5 \mu \mathrm{~A}$ (max.)
- Duty ratio : Built-in PWM/PFM switching control circuit (S-8356/58 Series)

15 to 83\% (100 kHz models)
15 to $78 \%$ ( $250 \mathrm{kHz}, 300 \mathrm{kHz}$, and 600 kHz models)

- External parts : Coil, diode, capacitor, and transistor
- Output voltage : Selectable in 0.1 V steps between 1.5 and 6.5 V (for $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {out }}$ separate types)

Selectable in 0.1 V steps between 2.0 and 6.5 V (for other than $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {out }}$ separate types)

- Output voltage accuracy : $\pm 2.4 \%$
- Oscillation frequency : $100 \mathrm{kHz}, 250 \mathrm{kHz}, 300 \mathrm{kHz}, 600 \mathrm{kHz}$ selectable
- Soft start function : 6 ms ( 100 kHz , typ.)
- Shutdown function
- Lead-free, Sn 100\%, halogen-free ${ }^{* 1}$
*1. Refer to "■ Product Name Structure" for details.


## Applications

- Power supplies for portable equipment such as digital cameras, electronic notebooks, and PDAs
- Power supplies for audio equipment such as portable CD / MD players
- Constant voltage power supplies for cameras, VCRs, and communications devices
- Power supplies for microcomputers


## Packages

- SOT-23-3
- SOT-23-5
-SOT-89-3


## ■ Block Diagrams

(1) S-8357/58 Series B, H and F Types (Without Shutdown Function)


Figure 1
(3) S-8357/58 Series E, J, G and P Types
( $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {Out }}$ Separate Type)


Figure 3
(2) S-8357/58 Series B, H, F and N Types (With Shutdown Function)


Figure 2
(4) S-8355/56 Series K, L, M and Q Types (With Shutdown Function, $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {out }}$ Separate Type)


Figure 4

STEP-UP, SUPER-SMALL PACKAGE, 600 kHz , PWM CONTROL or PWMPFFM SWITCHABLE SWITCHING REGULATOR CONTROLLER

## ■ Product Name Structure

The control system, product types, output voltage, and packages for the S-8355/56/57/58 Series can be selected at the user's request. Please refer to the "3. Product Name" for the definition of the product name, "4. Package" regarding the package drawings and "5. Product Name List" for the full product names.

1. Function List
(1) PWM Control Products

Table 1

| Product Name | Switching <br> Frequency <br> kHz | Shutdown <br> Function | $V_{\text {DD }} / V_{\text {OuT }}$ <br> Separate <br> Type | Package | Application |
| :--- | :---: | :---: | :---: | :--- | :--- |
| S-8355KxxMC | 100 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage <br> and a shutdown function |
| S-8355LxxMC | 250 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, <br> a shutdown function, and a thin coil |
| S-8355MxxMC | 300 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, <br> a shutdown function, and a thin coil |
| S-8355QxxMC | 600 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, <br> a shutdown function, and a thin coil |
| S-8357BxxMC | 100 | Yes | - | SOT-23-5 | Applications requiring a shutdown function |
| S-8357BxMMA | 100 | - | - | SOT-23-3 | Applications not requiring a shutdown function |
| S-8357BxxUA | 100 | - | - | SOT-89-3 | Applications not requiring a shutdown function |
| S-8357ExxMC | 100 | - | Yes | SOT-23-5 | Applications in which output voltage is <br> adjusted by external resistor |
| S-8357FxxMC | 300 | Yes | - | SOT-23-5 | Applications requiring a shutdown function <br> and a thin coil |
| S-8357GxxMC | 300 | - | Yes | SOT-23-5 | Applications requiring variable output voltage <br> and a thin coil |
| S-8357HxxMC | 250 | Yes | - | SOT-23-5 | Applications requiring a shutdown function <br> and a thin coil |
| S-8357JxxMC | 250 | - | Yes | SOT-23-5 | Applications requiring variable output voltage <br> with an external resistor and a thin coil |
| S-8357NxxMC | 600 | Yes | - | SOT-23-5 | Applications requiring a shutdown function <br> and a thin coil |
| S-8357PxxMC | 600 | - | Yes | SOT-23-5 | Applications requiring variable output voltage <br> with an external resistor and a thin coil |

(2) PWM / PFM Switching Control Products

Table 2

| Product Name | Switching <br> Frequency kHz | Shutdown Function | $V_{D D} / V_{\text {OUT }}$ Separate Type | Package | Application |
| :---: | :---: | :---: | :---: | :---: | :---: |
| S-8356KxxMC | 100 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage and a shutdown function |
| S-8356LxxMC | 250 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, a shutdown function, and a thin coil |
| S-8356MxxMC | 300 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, a shutdown function, and a thin coil |
| S-8356QxxMC | 600 | Yes | Yes | SOT-23-5 | Applications requiring variable output voltage, a shutdown function, and a thin coil |
| S-8358BxxMC | 100 | Yes | - | SOT-23-5 | Applications requiring a shutdown function |
| S-8358BxxMA | 100 | - | - | SOT-23-3 | Applications not requiring a shutdown function |
| S-8358BxxUA | 100 | - | - | SOT-89-3 | Applications not requiring a shutdown function |
| S-8358ExxMC | 100 | - | Yes | SOT-23-5 | Applications in which output voltage is adjusted by external resistor |
| S-8358FxxMC | 300 | Yes | - | SOT-23-5 | Applications requiring a shutdown function and a thin coil |
| S-8358GxxMC | 300 | - | Yes | SOT-23-5 | Applications requiring variable output voltage and a thin coil |
| S-8358HxxMC | 250 | Yes | - | SOT-23-5 | Applications requiring a shutdown function and a thin coil |
| S-8358JxxMC | 250 | - | Yes | SOT-23-5 | Applications requiring variable output voltage with an external resistor and a thin coil |
| S-8358NxxMC | 600 | Yes | - | SOT-23-5 | Applications requiring a shutdown function and a thin coil |
| S-8358PxxMC | 600 | - | Yes | SOT-23-5 | Applications requiring variable output voltage with an external resistor and a thin coil |

2. Package and Function List by Product Type

Table 3

| Series Name | Type | Package Name (Abbreviation) | Shutdown Function Yes / No | $V_{D D} / V_{\text {OuT }}$ Separate Type Yes/No |
| :---: | :---: | :---: | :---: | :---: |
| S-8355 Series, S-8356 Series | K, L, M, Q (Shutdown function + $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}$ separate type) $\begin{aligned} & \mathrm{K}=100 \mathrm{kHz}, \mathrm{~L}=250 \mathrm{kHz}, \mathrm{M}=300 \mathrm{kHz}, \\ & \mathrm{Q}=600 \mathrm{kHz} \end{aligned}$ | MC | Yes | Yes |
| S-8357 Series | B, H, F (Normal product)$\mathrm{B}=100 \mathrm{kHz}, \mathrm{H}=250 \mathrm{kHz}, \mathrm{~F}=300 \mathrm{kHz}$ | MA / UA | No | No |
|  |  | MC | Yes |  |
|  | $\begin{aligned} & \mathrm{N} \text { (Normal product) } \\ & \mathrm{N}=600 \mathrm{kHz} \\ & \hline \end{aligned}$ | MC | Yes | No |
|  | $\begin{aligned} & \mathrm{E}, \mathrm{~J}, \mathrm{G}, \mathrm{P}\left(\mathrm{~V}_{\mathrm{DD}} / V_{\text {out separate type }}\right. \\ & \mathrm{E}=100 \mathrm{kHz}, \mathrm{~J}=250 \mathrm{kHz}, \mathrm{G}=300 \mathrm{kHz}, \\ & \mathrm{P}=600 \mathrm{kHz} \end{aligned}$ | MC | No | Yes |
| S-8358 Series | B, H, F (Normal product)$\mathrm{B}=100 \mathrm{kHz}, \mathrm{H}=250 \mathrm{kHz}, \mathrm{~F}=300 \mathrm{kHz}$ | MA / UA | No | No |
|  |  | MC | Yes |  |
|  | $\begin{aligned} & \hline \mathrm{N} \text { (Normal product) } \\ & \mathrm{N}=600 \mathrm{kHz} \\ & \hline \end{aligned}$ | MC | Yes | No |
|  | $\begin{aligned} & \mathrm{E}, \mathrm{~J}, \mathrm{G}, \mathrm{P}\left(\mathrm{~V}_{\mathrm{DD}} / V_{\text {out separate type }}\right. \\ & \mathrm{E}=100 \mathrm{kHz}, \mathrm{~J}=250 \mathrm{kHz}, \mathrm{G}=300 \mathrm{kHz}, \\ & \mathrm{P}=600 \mathrm{kHz} \end{aligned}$ | MC | No | Yes |

## 3. Product Name

(1) SOT-23-3 Packages

*1. Refer to the tape specifications.
*2. Refer to the Table 4 to Table 11 in the " 5 . Product Name List".

## (2) SOT-23-5, SOT-89-3 Packages

```
S-835 x m 
U : Lead-free (Sn 100\%), halogen-free
G : Lead-free (for details, please contact our sales office)
IC direction in tape specifications \({ }^{* 1}\)
T2 : SOT-23-5, SOT-89-3
- Product name (abbreviation) \({ }^{* 2}\)
Package name (abbreviation)
MC : SOT-23-5
UA : SOT-89-3
Output voltage
15 to 65
(e.g. When the output voltage is 1.5 V , it is expressed as 15 .)
Product type
B : Normal product, \(\quad f_{\text {osc }}=100 \mathrm{kHz}\) (S-8357/58 Series)
H: Normal product,
\(\mathrm{f}_{\mathrm{OSC}}=250 \mathrm{kHz}\)
(S-8357/58 Series)
F : Normal product,
\(\mathrm{f}_{\mathrm{Osc}}=300 \mathrm{kHz}\) (S-8357/58 Series)
N : Normal product,
\(\mathrm{f}_{\mathrm{OSC}}=600 \mathrm{kHz}\)
(S-8357/58 Series)
\(\mathrm{E}: \mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\quad \mathrm{f}_{\mathrm{OSC}}=100 \mathrm{kHz}\) (S-8357/58 Series)
\(\mathrm{J}: \mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\quad \mathrm{f}_{\mathrm{OSC}}=250 \mathrm{kHz}\) (S-8357/58 Series)
\(\mathrm{G}: \mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\quad \mathrm{f}_{\mathrm{OSC}}=300 \mathrm{kHz}\) (S-8357/58 Series)
\(\mathrm{P}: \mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\quad \mathrm{f}_{\mathrm{OSC}}=600 \mathrm{kHz}\) (S-8357/58 Series)
K : With shutdown function \(+\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\mathrm{f}_{\mathrm{OSC}}=100 \mathrm{kHz}\) (S-8355/56 Series)
L : With shutdown function \(+\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}\) separate type, \(\mathrm{f}_{\mathrm{OSC}}=250 \mathrm{kHz}\) (S-8355/56 Series)
\(M\) :With shutdown function \(+V_{D D} / V_{\text {OUT }}\) separate type, \(f_{\text {OSC }}=300 \mathrm{kHz}\) (S-8355/56 Series)
\(Q\) :With shutdown function \(+V_{D D} / V_{\text {OUT }}\) separate type, \(f_{\text {OSC }}=600 \mathrm{kHz}\) (S-8355/56 Series)
Control system
5 or 7 : PWM control
6 or 8 : PWM / PFM switching control
```

*1. Refer to the tape specifications.
*2. Refer to the Table 4 to Table 11 in the " 5 . Product Name List".

## 4. Package

| Package Name | Drawing Code |  |  |
| :--- | :---: | :---: | :---: |
|  | Package | Tape | Reel |
| SOT-23-3 | MP003-A-P-SD | MP003-A-C-SD | MP003-A-R-SD |
| SOT-23-5 | MP005-A-P-SD | MP005-A-C-SD | MP005-A-R-SD |
| SOT-89-3 | UP003-A-P-SD | UP003-A-C-SD | UP003-A-R-SD |

STEP-UP, SUPER-SMALL PACKAGE, 600 kHz, PWM CONTROL or PWMPFFM SWITCHABLE SWITCHING REGULATOR CONTROLLER

## 5. Product Name List

(1) S-8355 Series

Table 4

| Output voltage | $\begin{gathered} \text { S-8355KxxMC } \\ \text { Series } \\ \hline \end{gathered}$ | S-8355LxxMC Series | S-8355MxxMC Series | $\begin{gathered} \hline \text { S-8355QxxMC } \\ \text { Series } \\ \hline \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: |
| 1.5 V | - | S-8355L15MC-NCAT2x | - | S-8355Q15MC-OWAT2x |
| 1.8 V | S-8355K18MC-NADT2x | - | S-8355M18MC-MCDT2x | S-8355Q18MC-OWDT2x |
| 2.0 V | S-8355K20MC-NAFT2x | S-8355L20MC-NCFT2x | S-8355M20MC-MCFT2x | S-8355Q20MC-OWFT2x |
| 2.4 V | S-8355K24MC-NAJT2x | - | - | S-8355Q24MC-OWJT2x |
| 2.8 V |  |  |  | S-8355Q28MC-OWNT2x |
| 3.0 V | S-8355K30MC-NAPT2x | - | S-8355M30MC-MCPT2x | S-8355Q30MC-OWPT2x |
| 3.1 V | S-8355K31MC-NAQT2x | - | S-8355M31MC-MCQT2x | S-8355Q31MC-OWQT2x |
| 3.2 V | - | - | S-8355M32MC-MCRT2x | - |
| 3.3 V | S-8355K33MC-NAST2x | - | - | S-8355Q33MC-OWST2x |
| 3.4 V | - | - | S-8355M34MC-MCTT2x | S-8355Q34MC-OWTT2x |
| 4.5 V |  |  |  | S-8355Q45MC-OXET2x |
| 5.0 V | S-8355K50MC-NBJT2x | - | S-8355M50MC-MDJT2x | S-8355Q50MC-OXJT2x |
| 5.1 V |  |  |  | S-8355Q51MC-OXKT2x |
| 5.5 V | - | - | S-8355M55MC-MDOT2x | - |
| 6.0 V | - | - | S-8355M60MC-MDTT2x | S-8355Q60MC-OXTT2x |
| 6.5 V | - | - | S-8355M65MC-MDYT2x | - |

Remark 1. Please contact the ABLIC Inc. marketing department for products with an output voltage other than those specified above.
2. $\mathrm{x}: \mathrm{G}$ or U
3. Please select products of environmental code $=U$ for $\mathrm{Sn} 100 \%$, halogen-free products.
(2) S-8356 Series

Table 5

| Output <br> voltage | S-8356KxxMC <br> Series | S-8356LxxMC <br> Series | S-8356MxxMC <br> Series | S-8356QxxMC <br> Series |
| :---: | :---: | :---: | :---: | :---: |
| 1.5 V | - | - | S-8356M15MC-MEAT2x | - |
| 1.8 V | S-8356K18MC-NEDT2x | - | S-8356M18MC-MEDT2x | S-8356Q18MC-OYDT2x |
| 2.8 V |  |  |  | S-8356Q28MC-OYNT2x |
| 3.0 V | S-8356K30MC-NEPT2x | S-8356L30MC-NGPT2x | S-8356M30MC-MEPT2x | S-8356Q30MC-OYPT2x |
| 3.1 V | - | - | S-8356M31MC-MEQT2x | S-8356Q31MC-OYQT2x |
| 3.3 V | S-8356K33MC-NEST2x | - | S-8356M33MC-MEST2x | S-8356Q33MC-OYST2x |
| 3.5 V | - | - | S-8356M35MC-MEUT2x | S-8356Q35MC-OYUT2x |
| 3.6 V | S-8356K36MC-NEVT2x | - | S-8356M36MC-MEVT2x | - |
| 4.0 V | S-8356K40MC-NEZT2x | - | - | S-8356Q40MC-OYZT2x |
| 5.0 V | S-8356K50MC-NFJT2x | - | S-8356M50MC-MFJT2x | S-8356Q50MC-OVJT2x |
| 5.3 V |  |  |  | S-8356Q53MC-OVMT2x |

Remark 1. Please contact the ABLIC Inc. marketing department for products with an output voltage other than those specified above.
2. $\mathrm{x}: \mathrm{G}$ or U
3. Please select products of environmental code $=U$ for $\mathrm{Sn} 100 \%$, halogen-free products.
(3) S-8357 Series

Table 6

| Output voltage | $\begin{gathered} \hline \text { S-8357BxxMC } \\ \text { Series } \\ \hline \end{gathered}$ | $\begin{gathered} \hline \text { S-8357BxxMA } \\ \text { Series } \end{gathered}$ | $\begin{gathered} \hline \text { S-8357BxxUA } \\ \text { Series } \end{gathered}$ | $\begin{gathered} \hline \text { S-8357ExxMC } \\ \text { Series } \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: |
| 1.5 V | - | - | - | S-8357E15MC-NKAT2x |
| 2.0 V | - | - | - | S-8357E20MC-NKFT2x |
| 2.5 V | S-8357B25MC-NIKT2x | - | - | - |
| 2.6 V | S-8357B26MC-NILT2x | - | - | - |
| 2.7 V | S-8357B27MC-NIMT2x | - | - | - |
| 2.8 V | S-8357B28MC-NINT2x | - | - | - |
| 3.0 V | S-8357B30MC-NIPT2x | S-8357B30MA-NIPT2G | - | S-8357E30MC-NKPT2x |
| 3.3 V | S-8357B33MC-NIST2x | S-8357B33MA-NIST2G | S-8357B33UA-NIST2x | - |
| 3.6 V | S-8357B36MC-NIVT2x | - | - | - |
| 3.8 V | - | - | S-8357B38UA-NIXT2x | - |
| 4.0 V | S-8357B40MC-NIZT2x | - | - | - |
| 4.8 V | S-8357B48MC-NJHT2x | - | S-8357B48UA-NJHT2x | - |
| 5.0 V | S-8357B50MC-NJJT2x | S-8357B50MA-NJJT2G | S-8357B50UA-NJJT2x | S-8357E50MC-NLJT2x |
| 5.2 V | S-8357B52MC-NJLT2x | - | - | - |
| 5.4 V | S-8357B54MC-NJNT2x | - | - | - |
| 6.0 V | S-8357B60MC-NJTT2x | - | - | - |

Table 7

| Output <br> voltage | S-8357FxxMC <br> Series | S-8357GxxMC <br> Series | S-8357HxxMC <br> Series | S-8357JxxMC <br> Series |
| :---: | :---: | :---: | :---: | :---: |
| 2.0 V | - | - | - | S-8357J20MC-NOFT2x |
| 2.5 V | - | - | - | S-8357J25MC-NOKT2x |
| 3.0 V | S-8357F30MC-MGPT2x | - | S-8357H30MC-NMPT2G | - |
| 3.1 V | - | - | S-8357H31MC-NMQT2G | - |
| 3.2 V | S-8357F32MC-MGRT2x | S-8357G32MC-MIRT2x | - | - |
| 3.3 V | S-8357F33MC-MGST2x | S-8357G33MC-MIST2x | - | - |
| 3.5 V | - | - | S-8357H35MC-NMUT2x | - |
| 3.6 V | S-8357F36MC-MGVT2x | - | S-8357H36MC-NMVT2x | - |
| 4.2 V | - | - | S-8357H42MC-NNBT2x | - |
| 5.0 V | S-8357F50MC-MHJT2x | S-8357G50MC-MJJT2x | S-8357H50MC-NNJT2x | S-8357J50MC-NPJT2x |
| 5.2 V | S-8357F52MC-MHLT2x | - | S-8357H52MC-NNLT2x | - |
| 6.5 V | S-8357F65MC-MHYT2x | - | - | - |

Table 8

| Output <br> voltage | S-8357NxxMC <br> Series |
| :---: | :---: |
| 3.0 V | S-8357N30MC-O2PT2x |
| 3.3 V | S-8357N33MC-O2ST2x |
| 5.0 V | S-8357N50MC-O3JT2x |
| 5.3 V | S-8357N53MC-O3MT2U |

Remark 1. Please contact the ABLIC Inc. marketing department for products with an output voltage other than those specified above.
2. $x: G$ or $U$
3. Please select products of environmental code $=U$ for $\mathrm{Sn} 100 \%$, halogen-free products.

STEP-UP, SUPER-SMALL PACKAGE, 600 kHz, PWM CONTROL or PWMPFFM SWITCHABLE SWITCHING REGULATOR CONTROLLER
(4) S-8358 Series

Table 9

| Output <br> voltage | S-8358BxxMC <br> Series | S-8358BxxMA <br> Series | S-8358BxxUA <br> Series | S-8358ExxMC <br> Series |
| :---: | :---: | :---: | :---: | :---: |
| 2.0 V | - | - | - | - |
| 2.3 V | S-8358B23MC-NQIT2x | - | - | - |
| 2.5 V | S-8358B25MC-NQKT2x | - | - | - |
| 2.6 V | S-8358B26MC-NQLT2x | - | - | - |
| 2.7 V | S-8358B27MC-NQMT2x | - | - | - |
| 2.8 V | S-8358B28MC-NQNT2x | - | - | - |
| 3.0 V | S-8358B30MC-NQPT2x | S-8358B30MA-NQPT2G | - |  |
| 3.1 V | S-8358B31MC-NQQT2x | - | - | - |
| 3.2 V | S-8358B32MC-NQRT2x | - | - | - |
| 3.3 V | S-8358B33MC-NQST2x | - | - | - |
| 3.5 V | S-8358B35MC-NQUT2x | - | - | - |
| 3.6 V | S-8358B36MC-NQVT2x | - | - | - |
| 3.8 V | S-8358B38MC-NQXT2x | - | - | - |
| 4.0 V | S-8358B40MC-NQZT2x | - | - | - |
| 5.0 V | S-8358B50MC-NRJT2x | S-8358B50MA-NRJT2G | S-8358B50UA-NRJT2x | S-8358E50MC-NTJT2x |
| 5.3 V | S-8358B53MC-NRMT2x | - | - | - |
| 6.0 V | S-8358B60MC-NRTT2x | - | - | - |

Table 10

| Output <br> voltage | S-8358FxxMC <br> Series | S-8358GxxMC <br> Series | S-8358HxxMC <br> Series | S-8358JxxMC <br> Series |
| :---: | :---: | :---: | :---: | :---: |
| 2.3 V | - | - | S-8358H23MC-NUIT2x | - |
| 2.6 V | S-8358F26MC-MKLT2x | - | - | - |
| 2.7 V | S-8358F27MC-MKMT2x | - | - | - |
| 3.0 V | S-8358F30MC-MKPT2x | - | S-8358H30MC-NUPT2x | - |
| 3.2 V | - | - | S-8358H32MC-NURT2x | - |
| 3.3 V | S-8358F33MC-MKST2x | - | S-8358H33MC-NUST2x | S-8358J33MC-NWST2x |
| 3.6 V | S-8358F36MC-MKVT2x | - | - | - |
| 4.0 V | - | - | S-8358H40MC-NUZT2x | - |
| 5.0 V | S-8358F50MC-MLJT2x | S-8358G50MC-MNJT2x | S-8358H50MC-NVJT2x | S-8358J50MC-NXJT2x |
| 5.3 V | S-8358F53MC-MLMT2x | - | - | - |
| 5.7 V | S-8358F57MC-MLQT2x | - | - | - |
| 6.0 V | S-8358F60MC-MLTT2x | - | - | - |

Table 11

| Output <br> voltage | S-8358NxxMC <br> Series | S-8358PxxMC <br> Series |
| :---: | :---: | :---: |
| 2.0 V | - | S-8358P20MC-O8FT2x |
| 3.0 V | S-8358N30MC-O6PT2x | - |
| 3.3 V | S-8358N33MC-O6ST2x | - |
| 5.0 V | S-8358N50MC-O7JT2x | - |
| 5.2 V | - | S-8358P52MC-O9LT2x |
| 5.3 V | S-8358N53MC-O7MT2x | - |

Remark 1. Please contact the ABLIC Inc. marketing department for products with an output voltage other than those specified above.
2. $\mathrm{x}: \mathrm{G}$ or U
3. Please select products of environmental code $=U$ for $\mathrm{Sn} 100 \%$, halogen-free products.

## ■ Pin Configurations

SOT-23-3
Top view


Figure 5

SOT-23-5
Top view


Figure 6

Table 12 S-8357/58 Series B, H and F Types (Without shutdown function, $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}$ non-separate type)

| Pin No. | Symbol | Pin Description |
| :---: | :---: | :--- |
| 1 | VOUT | Output voltage pin and IC power supply pin |
| 2 | VSS | GND pin |
| 3 | EXT | External transistor connection pin |

Table 13 S-8355/56 Series K, L, M and Q Types (With shutdown function, $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\mathrm{OUT}}$ separate type)

| Pin No. | Symbol | Pin Description |
| :---: | :---: | :--- |
| 1 | VOUT | Output voltage |
| 2 | VDD | IC power supply pin |
| 3 | ON/ $\overline{\text { OFF }}$ | Shutdown pin <br> "H": Normal operation (Step-up operating) <br> "L": Step-up stopped (Entire circuit stopped) |
| 4 | VSS | GND pin |
| 5 | EXT | External transistor connection pin |

Table 14 S-8357/58 Series B, H, F and N Types (With shutdown function, $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {out }}$ non-separate type)

| Pin No. | Symbol | Pin Description |
| :---: | :---: | :--- |
| 1 | ON/ $\overline{\text { OFF }}$ | Shutdown pin <br> "H": Normal operation (Step-up operating) <br> "L": Step-up stopped (Entire circuit stopped) |
| 2 | VOUT | Output voltage pin and IC power supply pin |
| 3 | NC $^{* 1}$ | No connection |
| 4 | VSS | GND pin |
| 5 | EXT | External transistor connection pin |

*1. The NC pin indicates electrically open.
Table 15 S-8357/58 Series E, J, G and P Types (Without shutdown function, $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}$ separate type)

| Pin No. | Symbol | Pin Description |
| :---: | :---: | :--- |
| 1 | VOUT | Output voltage pin |
| 2 | VDD | IC power supply pin |
| 3 | NC $^{* 1}$ | No connection |
| 4 | VSS | GND pin |
| 5 | EXT | External transistor connection pin |

*1. The NC pin indicates electrically open.

SOT-89-3
Top view


Figure 7

## Absolute Maximum Ratings

Table 17
( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ unless otherwise specified)

| Item |  | Symbol | Absolute maximum rating | Unit |
| :---: | :---: | :---: | :---: | :---: |
| VOUT pin voltage |  | Vout | $\mathrm{V}_{\text {SS }}-0.3$ to $\mathrm{V}_{\text {SS }}+12$ | V |
| ON/ $\overline{\text { OFF }}$ pin voltage ${ }^{*}$ |  | $\mathrm{V}_{\text {ON/OFF }}$ | $\mathrm{V}_{\text {SS }}-0.3$ to $\mathrm{V}_{\text {SS }}+12$ | V |
| VDD pin voltage ${ }^{*}$ |  | $V_{D D}$ | $\mathrm{V}_{\text {Ss }}-0.3$ to $\mathrm{V}_{\text {Ss }}+12$ | V |
| EXT pin voltage | B, H, F, N type | $\mathrm{V}_{\mathrm{EXT}}$ | $\mathrm{V}_{\text {SS }}-0.3$ to $\mathrm{V}_{\text {Out }}+0.3$ | V |
|  | Others |  | $\mathrm{V}_{S S}-0.3$ to $\mathrm{V}_{\mathrm{DD}}+0.3$ | V |
| EXT pin current |  | $\mathrm{I}_{\text {EXT }}$ | $\pm 80$ | mA |
| Power dissipation | SOT-23-3 | $\mathrm{P}_{\mathrm{D}}$ | 150 (When not mounted on board) | mW |
|  | SOT-23-3 |  | $430^{* 3}$ | mW |
|  | SOT-23-5 |  | 250 (When not mounted on board) | mW |
|  | SOT-23-5 |  | $600^{* 3}$ | mW |
|  | SOT-89-3 |  | 500 (When not mounted on board) | mW |
|  | SOT-89-3 |  | $1000{ }^{* 3}$ | mW |
| Operating ambient temperature |  | $\mathrm{T}_{\text {opr }}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature |  | $\mathrm{T}_{\text {stg }}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

*1. With shutdown function
*2. For $V_{D D} / V_{\text {out }}$ separate type
*3. When mounted on board
[Mounted board]
(1) Board size : $\quad 114.3 \mathrm{~mm} \times 76.2 \mathrm{~mm} \times \mathrm{t} 1.6 \mathrm{~mm}$
(2) Name : JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.
(1) When mounted on board

(2) When not mounted on board


Figure 8 Power Dissipation of The Package

## ■ Electrical Characteristics

(1) 100 kHz Product (B, E and K Types)

Table 18 (1 / 2)

| Item | Symbol | Condition |  | Min. | Typ. | Max. | Unit | Measurement circuit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output voltage | Vout | - |  | $\begin{array}{\|l\|} \hline \begin{array}{l} \text { Vout(S) } \\ \times 0.976 \end{array} \end{array}$ | $\mathrm{V}_{\text {Out(S) }}$ | $\begin{aligned} & \hline V_{\text {OUT }(S)} \\ & \times 1.024 \end{aligned}$ | V | 2 |
| Input voltage | $\mathrm{V}_{1 \times}$ | - |  | - | - | 10 | V | 2 |
| Operation start voltage | $\mathrm{V}_{\text {ST1 }}$ | $\mathrm{l}_{\text {OUT }}=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 2 |
| Oscillation start voltage | $\mathrm{V}_{\text {ST2 }}$ | No external parts, Voltage applied to Vout |  | - | - | 0.8 | V | 1 |
| Operation holding voltage | V HLD | lout $=1 \mathrm{~mA}$, Judged by decreasing $\mathrm{V}_{\mathbb{N}}$ voltage gradually |  | 0.7 | - | - | V | 2 |
| Current consumption 1 | Iss1 | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ | S-835xx15 to 19 | - | 14.0 | 23.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 19.7 | 32.9 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 25.9 | 43.2 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 32.6 | 54.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 39.8 | 66.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 47.3 | 78.9 | $\mu \mathrm{A}$ | 1 |
| Current consumption 2 | $\mathrm{I}_{\text {S } 2}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }}+0.5 \mathrm{~V}$ | S-835xx15 to 19 | - | 5.6 | 11.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 5.8 | 11.5 | $\mu \mathrm{A}$ |  |
|  |  |  | S-835xx30 to 39 | - | 5.9 | 11.8 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 6.1 | 12.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 6.3 | 12.5 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 6.4 | 12.8 | $\mu \mathrm{A}$ | 1 |
| Current consumption during shutdown (With shutdown function) | Isss | $\mathrm{V}_{\text {ON } / \overline{\text { FF }}}=0 \mathrm{~V}$ |  | - | - | 0.5 | $\mu \mathrm{A}$ | 1 |
| EXT pin output current | $\mathrm{l}_{\text {EXTH }}$ | $V_{\text {EXT }}=\mathrm{V}_{\text {OUT }}-0.4 \mathrm{~V}$ | S-835xx15 to 19 | -4.5 | -8.9 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | -6.2 | -12.3 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | -7.8 | -15.7 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | -10.3 | -20.7 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | -13.3 | -26.7 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | -16.1 | -32.3 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | -18.9 | -37.7 | - | mA | 1 |
|  | IExtL | $V_{\text {EXT }}=0.4 \mathrm{~V}$ | S-835xx15 to 19 | 9.5 | 19.0 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | 12.6 | 25.2 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | 15.5 | 31.0 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | 19.2 | 38.5 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | 23.8 | 47.6 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | 27.4 | 54.8 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | 30.3 | 60.6 | - | mA | 1 |
| Line regulation | $\Delta \mathrm{V}_{\text {OUT1 }}$ | $\mathrm{V}_{\mathbb{1}}=\mathrm{V}_{\text {OUT(S) }} \times 0.4$ to $\times 0.6$ |  | - | 30 | 60 | mV | 2 |
| Load regulation | $\Delta V_{\text {OUT2 }}$ | $\mathrm{I}_{\text {OUT }}=10 \mu \mathrm{~A}$ to $\mathrm{V}_{\text {OUT(S) }} / 50 \times 1.25$ |  | - | 30 | 60 | mV | 2 |
| Output voltage temperature coefficient | $\frac{\Delta \text { Vout }}{\Delta \text { Ta } \bullet \text { Vout }}$ | $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 2 |
| Oscillation frequency | $\mathrm{f}_{\text {OSC }}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 85 | 100 | 115 | kHz | 1 |
| Maximum duty ratio | MaxDuty | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 75 | 83 | 90 | \% | 1 |
| PWM / PFM switching duty ratio (For S-8356/58 Series) | PFMDuty | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {Out(S) }}-0.1 \mathrm{~V}$, No-load |  | 10 | 15 | 24 | \% | 1 |
| ON/ $\overline{\text { OFF }}$ pin input voltage (With shutdown function) | $\mathrm{V}_{\text {SH }}$ | Measured oscillation at EXT pin |  | 0.75 | - | - | V | 1 |
|  | $\mathrm{V}_{\text {SL1 }}$ | Judged oscillation stop at EXT pin | At $\mathrm{V}_{\text {Out }} \geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 1 |
|  | $\mathrm{V}_{\text {SL } 2}$ |  | At $\mathrm{V}_{\text {Out }}<1.5 \mathrm{~V}$ | - | - | 0.2 | V | 1 |

Table 18 (2 / 2)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit | Measurement circuit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ON/ OFF pin input current (For with shutdown function) | ISH | $\mathrm{V}_{\text {ON/OFF }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
|  | ISL | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
| Soft start time | tss | - | 3.0 | 6.0 | 12.0 | ms | 2 |
| Efficiency | EFFI | - | - | 85 | - | \% | 2 |

External parts
Coil:
Diode:
Capacitor:
Transistor:
Base resistor ( $\mathrm{R}_{\mathrm{b}}$ ):
Base capacitor $\left(\mathrm{C}_{\mathrm{b}}\right)$ : $\quad 2200 \mathrm{pF}$ (ceramic type)
$\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }} \times 0.6$ applied, IoUT $=\mathrm{V}_{\text {OUT(S) }} / 50 \Omega$
With shutdown function: $\quad \mathrm{ON} / \overline{\mathrm{OFF}}$ pin is connected to $\mathrm{V}_{\text {OUT }}$
For $V_{D D} / V_{\text {out }}$ separate type : VDD pin is connected to VOUT pin
Remark 1. $V_{\text {out(s) }}$ specified above is the set output voltage value, and $V_{\text {OUt }}$ is the typical value of the actual output voltage.
2. $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}$ separate type

A step-up operation is performed from $V_{D D}=0.8 \mathrm{~V}$. However, $1.8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{DD}} \leq 10 \mathrm{~V}$ is recommended stabilizing the output voltage and oscillation frequency. ( $\mathrm{V}_{\mathrm{DD}} \geq 1.8 \mathrm{~V}$ must be applied for products with a set value of less than 1.9 V .)
(2) $\mathbf{2 5 0} \mathbf{~ k H z}$ Product (H, J and L Types)

Table 19

| ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ unless otherwise specified) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Item | Symbol | Condition |  | Min. | Typ. | Max. | Unit | Measurement circuit |
| Output voltage | Vout | - |  | $\begin{aligned} & \hline \text { Vout(S) } \\ & \times 0.976 \end{aligned}$ | Vout(s) | $\begin{aligned} & \hline V_{\text {OUT }}(S) \\ & \times 1.024 \\ & \hline \end{aligned}$ | V | 2 |
| Input voltage | $\mathrm{V}_{1 \times}$ | - |  | - | - | 10 | V | 2 |
| Operation start voltage | $\mathrm{V}_{\text {ST1 }}$ | $\mathrm{l}_{\text {Out }}=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 2 |
| Oscillation start voltage | $\mathrm{V}_{\text {ST2 }}$ | No external parts, Voltage applied to $\mathrm{V}_{\text {Out }}$ |  | - | - | 0.8 | V | 1 |
| Operation holding voltage | $\mathrm{V}_{\text {HLD }}$ | lout $=1 \mathrm{~mA}$, Judged by decreasing $\mathrm{V}_{\mathbb{N}}$ voltage gradually |  | 0.7 | - | - | V | 2 |
| Current consumption 1 | Iss1 | $V_{\text {OUT }}=V_{\text {OUT(S) }} \times 0.95$ | S-835xx15 to 19 | - | 28.9 | 48.2 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 42.7 | 71.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 58.0 | 96.7 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 74.5 | 124.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 92.0 | 153.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 110.5 | 184.2 | $\mu \mathrm{A}$ | 1 |
| Current consumption 2 | Iss2 | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {Out(S) }}+0.5 \mathrm{~V}$ | S-835xx15 to 19 | - | 8.7 | 17.3 | $\mu \mathrm{A}$ |  |
|  |  |  | S-835xx20 to 29 | - | 8.8 | 17.6 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 9.0 | 18.0 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 9.2 | 18.3 | $\mu \mathrm{A}$ |  |
|  |  |  | S-835xx50 to 59 | - | 9.3 | 18.6 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 9.5 | 19.0 | $\mu \mathrm{A}$ | 1 |
| Current consumption during shutdown (With shutdown function) | Isss | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ |  | - | - | 0.5 | $\mu \mathrm{A}$ | 1 |
| EXT pin output current | IEXTH | $V_{\text {EXT }}=\mathrm{V}_{\text {OUT }}-0.4 \mathrm{~V}$ | S-835xx15 to 19 | -4.5 | -8.9 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | -6.2 | -12.3 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | -7.8 | -15.7 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | -10.3 | -20.7 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | -13.3 | -26.7 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | -16.1 | -32.3 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | -18.9 | -37.7 | - | mA | 1 |
|  | lextL | $V_{\text {EXT }}=0.4 \mathrm{~V}$ | S-835xx15 to 19 | 9.5 | 19.0 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | 12.6 | 25.2 | - | mA |  |
|  |  |  | S-835xx25 to 29 | 15.5 | 31.0 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | 19.2 | 38.5 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | 23.8 | 47.6 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | 27.4 | 54.8 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | 30.3 | 60.6 | - | mA | 1 |
| Line regulation | $\Delta \mathrm{V}_{\text {OUT1 }}$ | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }} \times 0.4$ to $\times 0.6$ |  | - | 30 | 60 | mV | 2 |
| Load regulation | $\Delta V_{\text {OUT2 }}$ | lout $=10 \mu \mathrm{~A}$ to $\mathrm{V}_{\text {OUT(S) }} / 50 \times 1.25$ |  | - | 30 | 60 | mV | 2 |
| Output voltage temperature coefficient | $\frac{\Delta \text { Vout }}{\Delta \text { Ta } \bullet \text { Vout }}$ | $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 2 |
| Oscillation frequency | fosc | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 212.5 | 250 | 287.5 | kHz | 1 |
| Maximum duty ratio | MaxDuty | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 70 | 78 | 85 | \% | 1 |
| PWM / PFM switching duty ratio (For S-8356/58 Series) | PFMDuty | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\text {OUT(S) }}-0.1 \mathrm{~V}$, No-load |  | 10 | 15 | 24 | \% | 1 |
| ON/ OFF pin input voltage (With shutdown function) | $\mathrm{V}_{\text {SH }}$ | Measured oscillation at EXT pin |  | 0.75 | - | - | V | 1 |
|  | $\mathrm{V}_{\text {SL1 }}$ | Judged oscillation stop EXT pin | At $\mathrm{V}_{\text {out }} \geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 1 |
|  | $\mathrm{V}_{\text {SL2 }}$ |  | At $\mathrm{V}_{\text {Out }}<1.5 \mathrm{~V}$ | - | - | 0.2 | V | 1 |
| $\mathrm{ON} / \overline{\mathrm{OFF}}$ pin input current (With shutdown function) | $\mathrm{I}_{\mathrm{SH}}$ | $\mathrm{V}_{\text {ON/OFF }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
|  | 1 l L | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
| Soft start time | ts | - |  | 1.5 | 3.0 | 6.0 | ms | 2 |
| Efficiency | EFFI | - |  | - | 85 | - | \% | 2 |

## External parts

| Coil: | CDRH6D28-220 of Sumida Corporation |
| :--- | :--- |
| Diode: | RB461F (Schottky type) of Rohm Co., Ltd. |
| Capacitor: | F93 (16 V, 47 $\mu$ F tantalum type) of Nichicon Corporation |
| Transistor: | CPH3210 of Sanyo Electric Co., Ltd. |
| Base resistor $\left(R_{b}\right):$ | $1.0 \mathrm{k} \Omega$ |
| Base capacitor $\left(\mathrm{C}_{\mathrm{b}}\right):$ | 2200 pF (ceramic type) |

$\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }} \times 0.6$ applied, Iout $=\mathrm{V}_{\text {out(S) }} / 50 \Omega$
With shutdown function: $\quad \mathrm{ON} / \overline{\mathrm{OFF}}$ pin is connected to $\mathrm{V}_{\text {OUT }}$
For $\mathrm{V}_{\mathrm{DD}} / \mathrm{V}_{\text {OUT }}$ separate type : VDD pin is connected to VOUT pin
Remark 1. $V_{\text {OUt(s) }}$ specified above is the set output voltage value, and $V_{\text {Out }}$ is the typical value of the actual output voltage.
2. $V_{D D} / V_{\text {OUt }}$ separate type

A step-up operation is performed from $V_{D D}=0.8 \mathrm{~V}$. However, $1.8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{DD}} \leq 10 \mathrm{~V}$ is recommended stabilizing the output voltage and oscillation frequency. ( $\mathrm{V}_{\mathrm{DD}} \geq 1.8 \mathrm{~V}$ must be applied for products with a set value of less than 1.9 V .)
(3) $\mathbf{3 0 0} \mathbf{~ k H z}$ Product (F, G and M Types)

Table 20

| ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ unless otherwise specified) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Item | Symbol | Condition |  | Min. | Typ. | Max. | Unit | Measurement <br> circuit |
| Output voltage | Vout | - |  | $\begin{aligned} & \hline V_{\text {out }}(\mathrm{S}) \\ & \times 0.976 \end{aligned}$ | Vout(S) | $\begin{array}{l\|} \hline V_{\text {out }}(S) \\ \times 1.024 \end{array}$ | V | 2 |
| Input voltage | $\mathrm{V}_{\text {IN }}$ | - |  | - | - | 10 | V | 2 |
| Operation start voltage | $\mathrm{V}_{\text {ST1 }}$ | $\mathrm{I}_{\text {Out }}=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 2 |
| Oscillation start voltage | $\mathrm{V}_{\text {ST2 }}$ | No external parts, Voltage applied to $\mathrm{V}_{\text {Out }}$ |  | - | - | 0.8 | V | 1 |
| Operation holding voltage | $V_{\text {HLD }}$ | lout $=1 \mathrm{~mA}$, Judged by decreasing $\mathrm{V}_{\mathbb{N}}$ voltage gradually |  | 0.7 | - | - | V | 2 |
| Current consumption 1 | $\mathrm{I}_{\text {s } 1}$ | $V_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ | S-835xx15 to 19 | - | 33.8 | 56.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 50.3 | 83.9 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 68.6 | 114.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 88.4 | 147.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 109.4 | 182.4 | $\mu \mathrm{A}$ |  |
|  |  |  | S-835xx60 to 65 | - | 131.6 | 219.3 | $\mu \mathrm{A}$ | 1 |
| Current consumption 2 | $\mathrm{I}_{\text {S } 2}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT }(S)}+0.5 \mathrm{~V}$ | S-835xx15 to 19 | - | 9.7 | 19.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 9.9 | 19.7 | $\mu \mathrm{A}$ |  |
|  |  |  | S-835xx30 to 39 | - | 10.0 | 20.0 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 10.2 | 20.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 10.4 | 20.7 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 10.5 | 21.0 | $\mu \mathrm{A}$ | 1 |
| Current consumption during shutdown (With shutdown function) | Isss | $\mathrm{V}_{\mathrm{ON} / \overline{\mathrm{FF}}}=0 \mathrm{~V}$ |  | - | - | 0.5 | $\mu \mathrm{A}$ | 1 |
| EXT pin output current | IEXTH | $\mathrm{V}_{\text {EXT }}=\mathrm{V}_{\text {OUT }}-0.4 \mathrm{~V}$ | S-835xx15 to 19 | -4.5 | -8.9 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | -6.2 | -12.3 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | -7.8 | -15.7 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | -10.3 | -20.7 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | -13.3 | -26.7 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | -16.1 | -32.3 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | -18.9 | -37.7 | - | mA | 1 |
|  | lextc | $V_{\text {ExT }}=0.4 \mathrm{~V}$ | S-835xx15 to 19 | 9.5 | 19.0 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | 12.6 | 25.2 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | 15.5 | 31.0 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | 19.2 | 38.5 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | 23.8 | 47.6 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | 27.4 | 54.8 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | 30.3 | 60.6 | - | mA | 1 |
| Line regulation | $\Delta \mathrm{V}_{\text {Out1 }}$ | $\mathrm{V}_{\mathbb{N}}=\mathrm{V}_{\text {OUT(S) }} \times 0.4$ to $\times 0.6$ |  | - | 30 | 60 | mV | 2 |
| Load regulation | $\Delta V_{\text {OUT2 }}$ | lout $=10 \mu \mathrm{~A}$ to $\mathrm{V}_{\text {OUT(S) }} / 50 \times 1.25$ |  | - | 30 | 60 | mV | 2 |
| Output voltage temperature coefficient | $\frac{\Delta \text { Vout }}{\Delta \mathrm{Ta} \bullet \mathrm{Vout}^{\prime}}$ | $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 2 |
| Oscillation frequency | fosc | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 255 | 300 | 345 | kHz | 1 |
| Maximum duty ratio | MaxDuty | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 70 | 78 | 85 | \% | 1 |
| PWM / PFM switching duty ratio (For S-8356/58 Series) | PFMDuty | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\text {OUT(S) }}-0.1 \mathrm{~V}$, No-load |  | 10 | 15 | 24 | \% | 1 |
| ON/ OFF pin input voltage (With shutdown function) | $\mathrm{V}_{\text {SH }}$ | Measured oscillation at EXT pin |  | 0.75 | - | - | V | 1 |
|  | $\mathrm{V}_{\text {SL1 }}$ | Judged oscillation stop at EXT pin | At $\mathrm{V}_{\text {Out }} \geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 1 |
|  | $\mathrm{V}_{\mathrm{SL} 2}$ |  | At $\mathrm{V}_{\text {out }}<1.5 \mathrm{~V}$ | - | - | 0.2 | V | 1 |
| $\mathrm{ON} / \overline{\mathrm{OFF}}$ pin input current (With shutdown function) | $\mathrm{I}_{\text {SH }}$ | $\mathrm{V}_{\text {ON/OFF }}=\mathrm{V}_{\text {OUT }(S)} \times 0.95$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
|  | $\mathrm{I}_{\text {SL }}$ | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
| Soft start time | tss | - |  | 1.5 | 3.0 | 6.0 | ms | 2 |
| Efficiency | EFFI | - |  | - | 85 | - | \% | 2 |

## External parts

| Coil: | CDRH6D28-220 of Sumida Corporation |
| :---: | :---: |
| Diode: | RB461F (Schottky type) of Rohm Co., Ltd. |
| Capacitor: | F93 (16 V, $47 \mu \mathrm{~F}$ tantalum type) of Nichicon Corporation |
| Transistor: | CPH3210 of Sanyo Electric Co., Ltd. |
| Base resistor ( $\mathrm{R}_{\mathrm{b}}$ ): | $1.0 \mathrm{k} \Omega$ |
| Base capacitor ( $\mathrm{C}_{\mathrm{b}}$ ): 2200 pF (ceramic type) |  |
| $\mathrm{V}_{\text {OUt(S) }} \times 0.6$ applied, $\mathrm{I}_{\text {lut }}=\mathrm{V}_{\text {OUt(s) }} / 50 \Omega$ |  |
| shutdown function: | ON/ $\overline{\text { OFF }}$ pin is connected to $\mathrm{V}_{\text {Out }}$ |
| DD / Vout separate typ | VDD pin is connected to VOUT pin |

Remark 1. $V_{\text {OUt(s) }}$ specified above is the set output voltage value, and $V_{\text {Out }}$ is the typical value of the actual output voltage.
2. $V_{D D} / V_{\text {OUT }}$ separate type

A step-up operation is performed from $\mathrm{V}_{\mathrm{DD}}=0.8 \mathrm{~V}$. However, $1.8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{DD}} \leq 10 \mathrm{~V}$ is recommended stabilizing the output voltage and oscillation frequency. ( $\mathrm{V}_{\mathrm{DD}} \geq 1.8 \mathrm{~V}$ must be applied for products with a set value of less than 1.9 V .)

STEP-UP, SUPER-SMALL PACKAGE, 600 kHz, PWM CONTROL or PWMPFFM SWITCHABLE SWITCHING REGULATOR CONTROLLER
(4) $\mathbf{6 0 0}$ kHz Product (N Type)

Table 21

| Item | Symbol | Condition |  | Min. | Typ. | Max. | Unit | Measurement circuit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output voltage | $V_{\text {OUT }}$ | - |  | $\begin{aligned} & \hline \mathrm{V}_{\text {OUT (S) }} \\ & \times 0.976 \end{aligned}$ | $V_{\text {OUt(S) }}$ | $\begin{array}{\|l\|} \hline V_{\text {OUT(S) }} \\ \times 1.024 \\ \hline \end{array}$ | V | 2 |
| Input voltage | $\mathrm{V}_{1 \times}$ | - |  | - | - | 10 | V | 2 |
| Operation start voltage | $\mathrm{V}_{\text {ST1 }}$ | lout $=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 2 |
| Oscillation start voltage | $\mathrm{V}_{\text {ST2 }}$ | No external parts, Voltage applied to Vout |  | - | - | 0.8 | V | 1 |
| Operation holding voltage | V HLD | lout $=1 \mathrm{~mA}$, Judged by decreasing $\mathrm{V}_{\mathbb{N}}$ voltage gradually |  | 0.7 | - | - | V | 2 |
| Current consumption 1 | $\mathrm{I}_{\text {S } 1}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ | S-835xx15 to 19 | - | 63.6 | 105.9 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 96.4 | 160.6 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 132.8 | 221.3 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 172.2 | 286.9 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 214.0 | 356.7 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 240.2 | 400.3 | $\mu \mathrm{A}$ | 1 |
| Current consumption 2 | $\mathrm{I}_{\text {S } 2}$ | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }}+0.5 \mathrm{~V}$ | S-835xx15 to 19 | - | 15.9 | 31.8 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx20 to 29 | - | 16.1 | 32.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx30 to 39 | - | 16.2 | 32.4 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx40 to 49 | - | 16.4 | 32.8 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx50 to 59 | - | 16.6 | 33.1 | $\mu \mathrm{A}$ | 1 |
|  |  |  | S-835xx60 to 65 | - | 16.7 | 33.3 | $\mu \mathrm{A}$ | 1 |
| Current consumption during shutdown | $\mathrm{I}_{\text {ss }}$ | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ |  | - | - | 0.5 | $\mu \mathrm{A}$ | 1 |
| EXT pin output current | EEXTH | $\mathrm{V}_{\text {EXT }}=\mathrm{V}_{\text {OUT }}-0.4 \mathrm{~V}$ | S-835xx15 to 19 | -4.5 | -8.9 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | -6.2 | -12.3 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | -7.8 | -15.7 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | -10.3 | -20.7 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | -13.3 | -26.7 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | -16.1 | -32.3 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | -18.9 | -37.7 | - | mA | 1 |
|  | $\mathrm{IExTL}^{\text {l }}$ | $V_{\text {EXT }}=0.4 \mathrm{~V}$ | S-835xx15 to 19 | 9.5 | 19.0 | - | mA | 1 |
|  |  |  | S-835xx20 to 24 | 12.6 | 25.2 | - | mA | 1 |
|  |  |  | S-835xx25 to 29 | 15.5 | 31.0 | - | mA | 1 |
|  |  |  | S-835xx30 to 39 | 19.2 | 38.5 | - | mA | 1 |
|  |  |  | S-835xx40 to 49 | 23.8 | 47.6 | - | mA | 1 |
|  |  |  | S-835xx50 to 59 | 27.4 | 54.8 | - | mA | 1 |
|  |  |  | S-835xx60 to 65 | 30.3 | 60.6 | - | mA | 1 |
| Line regulation | $\Delta \mathrm{V}_{\text {Out1 }}$ | $\mathrm{V}_{\mathbb{1}}=\mathrm{V}_{\text {OUT(S) }} \times 0.4$ to $\times 0.6$ |  | - | 30 | 60 | mV | 2 |
| Load regulation | $\Delta V_{\text {OUT2 }}$ | Iout $=10 \mu \mathrm{~A}$ to $\mathrm{V}_{\text {Out }}(\mathrm{s}) / 50 \times 1.25$ |  | - | 30 | 60 | mV | 2 |
| Output voltage temperature coefficient | $\frac{\Delta \text { Vout }}{\Delta \mathrm{Ta} \bullet \text { Vout }}$ | $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 2 |
| Oscillation frequency | fosc | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 510 | 600 | 690 | kHz | 1 |
| Maximum duty ratio | MaxDuty | $\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | 65 | 78 | 85 | \% | 1 |
| PWM / PFM switching duty ratio (For S-8356/58 Series) | PFMDuty | $\mathrm{V}_{10}=\mathrm{V}_{\text {out(s) }}-0.1 \mathrm{~V}, \mathrm{No}$-load |  | 10 | 15 | 24 | \% | 1 |
| ON/ $\overline{\text { OFF }}$ pin input voltage | $\mathrm{V}_{\text {SH }}$ | Measured oscillation at EXT pin |  | 0.75 | - | - | V | 1 |
|  | $\mathrm{V}_{\text {SL1 }}$ | Judged oscillation stop EXT pin | At $\mathrm{V}_{\text {Out }} \geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 1 |
|  | $\mathrm{V}_{\text {SL } 2}$ |  | At $\mathrm{V}_{\text {Out }}<1.5 \mathrm{~V}$ | - | - | 0.2 | V | 1 |
| ON/ $\overline{\text { OFF }}$ pin input current | ISH | $\mathrm{V}_{\text {ON/ } / \text { OFF }}=\mathrm{V}_{\text {OUT (S) }} \times 0.95$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
|  | ISL | $\mathrm{V}_{\text {ON/ } / \text { FFF }}=0 \mathrm{~V}$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 1 |
| Soft start time | ts | - |  | 1.5 | 3.0 | 6.0 | ms | 2 |
| Efficiency | EFFI | - |  | - | 85 | - | \% | 2 |

External parts
Coil: $\quad$ CDRH6D28-100 of Sumida Corporation
Diode:
RB461F (Schottky type) of Rohm Co., Ltd.
Capacitor:
F93 (16 V, $47 \mu \mathrm{~F}$ tantalum type) of Nichicon Corporation
Transistor:
CPH3210 of Sanyo Electric Co., Ltd.
Base resistor $\left(\mathrm{R}_{\mathrm{b}}\right)$ :
$1.0 \mathrm{k} \Omega$
Base capacitor ( $\mathrm{C}_{\mathrm{b}}$ ): $\quad 2200 \mathrm{pF}$ (ceramic type)
$\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\text {OUT }(\mathrm{S})} \times 0.6$ applied, lout $=\mathrm{V}_{\text {OUT(S) }} / 50 \Omega, \mathrm{ON} / \overline{\mathrm{OFF}}=\mathrm{V}_{\text {OUT }}$

Remark $V_{\text {OUT(S) }}$ specified above is the set output voltage value, and $V_{\text {OUt }}$ is the typical value of the actual output voltage.
(5) $\mathbf{6 0 0} \mathrm{kHz}$ Product (P and Q types)

Table 22

|  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Item | Symbol | Condition |  | Min. | Typ. | Max. | Unit | Measurement circuit |
| Output voltage | Vout | - |  | $\begin{array}{\|l\|} \hline \begin{array}{l} \text { Vout(s) } \\ \times 0.976 \end{array} \end{array}$ | Vout(s) | $\begin{array}{l\|} \hline V_{\text {OUT }}(S) \\ \times 1.024 \end{array}$ | V | 4 |
| Input voltage | $\mathrm{V}_{1 /}$ | - |  | - | - | 10 | V | 4 |
| Operation start voltage | $\mathrm{V}_{\text {ST1 }}$ | $\mathrm{l}_{\text {OUT }}=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 4 |
| Oscillation start voltage | $\mathrm{V}_{\text {ST2 }}$ | No external parts, Voltage applied to $\mathrm{V}_{\mathrm{DD}}$ |  | - | - | 0.8 | V | 3 |
| Operation holding voltage | $\mathrm{V}_{\text {HLD }}$ | $l_{\text {out }}=1 \mathrm{~mA}$, Judged by decreasing $\mathrm{V}_{\mathbb{N}}$ voltage gradually |  | 0.7 | - | - | V | 4 |
| Current consumption 1 | $\mathrm{I}_{\text {S } 1}$ | $\mathrm{V}_{\text {DD }}=3.3 \mathrm{~V}$ |  | - | 132.8 | 221.3 | $\mu \mathrm{A}$ | 3 |
| Current consumption 2 | $\mathrm{I}_{\text {S } 2}$ | $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ |  | - | 16.2 | 32.4 | $\mu \mathrm{A}$ | 3 |
| Current consumption during shutdown <br> (With shutdown function) | $\mathrm{I}_{\text {ss }}$ | $\mathrm{V}_{\mathrm{ON} / \overline{\text { OFF }}}=0 \mathrm{~V}$ |  | - | - | 0.5 | $\mu \mathrm{A}$ | 3 |
| EXT pin output current | $\mathrm{E}_{\text {EXTH }}$ | $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ |  | -10.3 | -20.7 | - | mA | 3 |
|  | $\mathrm{IEXTL}^{\text {l }}$ | $V_{D D}=3.3 \mathrm{~V}$ |  | 19.2 | 38.5 | - | mA | 3 |
| Line regulation | $\Delta \mathrm{V}_{\text {OUT1 }}$ | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT }(\text { S }} \times 0.4$ to $\times 0.6$ |  | - | 30 | 60 | mV | 4 |
| Load regulation | $\Delta \mathrm{V}_{\text {OUT2 }}$ | IOUT $=10 \mu \mathrm{~A}$ to $\mathrm{V}_{\text {OUT(S) }} / 50 \times 1.25$ |  | - | 30 | 60 | mV | 4 |
| Output voltage temperature coefficient | $\frac{\Delta \text { Vout }}{\Delta \mathrm{Ta} \bullet \text { Vout }}$ | $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm $/{ }^{\circ} \mathrm{C}$ | 4 |
| Oscillation frequency | fosc | $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ |  | 510 | 600 | 690 | kHz | 3 |
| Maximum duty ratio | MaxDuty | $\mathrm{V}_{\mathrm{DD}}=3.3 \mathrm{~V}$ |  | 65 | 78 | 85 | \% | 3 |
| PWM / PFM switching duty ratio (For S-8356/58 Series) | PFMDuty | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\text {OUT(S) }}-0.1 \mathrm{~V}$, No-load |  | 10 | 15 | 24 | \% | 3 |
| ON/ $\overline{\text { OFF }}$ pin input voltage (With shutdown function) | $\mathrm{V}_{\text {SH }}$ | Measured oscillation at EXT pin |  | 0.75 | - | - | V | 3 |
|  | $\mathrm{V}_{\text {SL1 }}$ | Judged oscillation stop at EXT pin | At $\mathrm{V}_{\text {Out }} \geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 3 |
|  | $\mathrm{V}_{\text {SL } 2}$ |  | At $\mathrm{V}_{\text {Out }}<1.5 \mathrm{~V}$ | - | - | 0.2 | V | 3 |
| ON/ OFF pin input current (With shutdown function) | $\mathrm{I}_{\mathrm{SH}}$ | $\mathrm{V}_{\text {ON/OFF }}=\mathrm{V}_{\text {OUT(S) }} \times 0.95$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 3 |
|  | ISL | $\mathrm{V}_{\text {ON/OFF }}=0 \mathrm{~V}$ |  | -0.1 | - | 0.1 | $\mu \mathrm{A}$ | 3 |
| Soft start time | tss | - |  | 1.5 | 3.0 | 6.0 | ms | 4 |
| Efficiency | EFFI | - |  | - | 85 | - | \% | 4 |

External parts
Coil:
Diode:
Capacitor:
Transistor:
Base resistor $\left(\mathrm{R}_{\mathrm{b}}\right)$ :
Base capacitor $\left(\mathrm{C}_{\mathrm{b}}\right)$ : $\quad 2200 \mathrm{pF}$ (ceramic type)
$\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {OUT(S) }} \times 0.6$ applied, $\mathrm{I}_{\text {OUT }}=\mathrm{V}_{\text {OUT(S) }} / 50 \Omega$, ON $/ \overline{\mathrm{OFF}}=\mathrm{V}_{\text {OUT }}$
Remark 1. $V_{\text {OUT(s) }}$ specified above is the set output voltage value, and $V_{\text {OUT }}$ is the typical value of the actual output voltage.
2. $V_{D D} / V_{\text {out }}$ separate type

A step-up operation is performed from $\mathrm{V}_{\mathrm{DD}}=0.8 \mathrm{~V}$. However, $1.8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{DD}} \leq 10 \mathrm{~V}$ is recommended stabilizing the output voltage and oscillation frequency. ( $\mathrm{V}_{\mathrm{DD}} \geq 1.8 \mathrm{~V}$ must be applied for products with a set value of less than 1.9 V .)

## Measurement Circuits

1. 



Figure 9
2.


Figure 10
3.


Figure 11
4.


Figure 12
*1. With shutdown function
*2. For $V_{D D} / V_{\text {out }}$ separate type

## Operation

## 1. Switching Control Types

### 1.1 PWM Control (S-8355/57 Series)

The S-8355/57 Series is a DC-DC converter using a pulse width modulation method (PWM) and features a low current consumption.
In conventional PFM DC-DC converters, pulses are skipped when the output load current is low, causing a fluctuation in the ripple frequency of the output voltage, resulting in an increase in the ripple voltage. The switching frequency does not change, although the pulse width changes from 0 to $83 \%$ ( $78 \%$ for $\mathrm{F}, \mathrm{G}, \mathrm{H}, \mathrm{J}, \mathrm{L}$, $\mathrm{M}, \mathrm{N}, \mathrm{P}$ and Q types) corresponding to each load current. The ripple voltage generated from switching can thus be removed easily through a filter because the switching frequency is constant.

### 1.2 PWM/PFM Switching Control (S-8356/58 Series)

S-8356/58 Series is a DC-DC converter that automatically switches between a pulse width modulation method (PWM) and a pulse frequency modulation method (PFM), depending on the load current, and features low current consumption.
The S-8356/58 Series operates under PWM control with the pulse width duty changing from 15 to $83 \%$ ( $78 \%$ for F , G, H, J, L, M, N, P and Q types) in a high output load current area.
The S-8356/58 Series operates under PFM control with the pulse width duty fixed at $15 \%$, and pulses are skipped according to the load current. The oscillation circuit thus oscillates intermittently so that the resultant lower self current consumption prevents a reduction in the efficiency at a low load current. The switching point from PWM control to PFM control depends on the external devices (coil, diode, etc.), input voltage and output voltage. This series are an especially highly efficient DC-DC converter at an output current around $100 \mu \mathrm{~A}$.

## 2. Soft Start Function

For this IC, the built-in soft start circuit controls the rush current and overshoot of the output voltage when powering on or when the ON/ $\overline{\mathrm{OFF}}$ pin is switched to the " H " level.
3. ON/OFF Pin (Shutdown Pin) (SOT-23-5 Package Products of S-8355/56/57/58 Series B, H, F, K, L, M, N and Q Types)

ON/ OFF pin stops or starts step-up operation.
Setting the ON/ $\overline{\text { OFF }}$ pin to the "L" level stops operation of all the internal circuits and reduces the current consumption significantly.
DO NOT use the ON/ $\overline{\text { OFF }}$ pin in a floating state because it has the structure shown in Figure 13 and is not pulled up or pulled down internally. DO NOT apply a voltage of between 0.3 V and 0.75 V to the $\mathrm{ON} / \overline{\mathrm{OFF}}$ pin because applying such a voltage increases the current consumption. If the ON/ $\overline{\mathrm{OFF}}$ pin is not used, connect it to the VOUT pin.
The ON $/ \overline{\text { OFF }}$ pin does not have hysteresis.
Table 23

| ON $/ \overline{\text { OFF }}$ pin | CR oscillation circuit | Output voltage |
| :---: | :---: | :---: |
| "H" | Operation | Fixed |
| "L" | Stop | $\cong V_{\text {IN }}{ }^{* 1}$ |

*1. Voltage obtained by subtracting the voltage drop due to the DC resistance of the inductor and the diode forward voltage from $\mathrm{V}_{\mathrm{IN}}$.

*1. VDD for $K, L, M$ and $Q$ types.
Figure 13 ON/OFF Pin Structure

## 4. Operation

The following are the basic equations [(1) through (7)] of the step-up switching regulator. (Refer to Figure 14.)


Figure 14 Step-Up Switching Regulator Circuit for Basic Equation
Voltage at CONT pin at the moment M 1 is turned $\mathrm{ON}\left(\mathrm{V}_{\mathrm{A}}\right)^{* 1}$ :

$$
\begin{equation*}
V_{A}=V_{S}{ }^{*}{ }^{2} \tag{1}
\end{equation*}
$$

*1. Current flowing through $L\left(I_{L}\right)$ is zero.
*2. Non-saturated voltage of M1.
The change in $I_{\llcorner }$over time :

$$
\begin{equation*}
\frac{\mathrm{dl} \mathrm{~L}}{\mathrm{dt}}=\frac{\mathrm{V}_{\mathrm{L}}}{\mathrm{~L}}=\frac{\mathrm{V}_{\mathrm{IN}}-\mathrm{Vs}_{\mathrm{s}}}{\mathrm{~L}} \tag{2}
\end{equation*}
$$

Integration of equation (2) ( $\mathrm{I}_{\mathrm{L}}$ ):

$$
\begin{equation*}
\mathrm{I}_{\mathrm{L}}=\left(\frac{\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\mathrm{S}}}{\mathrm{~L}}\right) \cdot \mathrm{t} \tag{3}
\end{equation*}
$$

$I_{L}$ flows while M 1 is $\mathrm{ON}\left(\mathrm{t}_{\mathrm{ON}}\right)$. The time of $\mathrm{t}_{\mathrm{ON}}$ is determined by the oscillation frequency of OSC.
The peak current ( $\mathrm{l}_{\text {PK }}$ ) after $\mathrm{t}_{\mathrm{ON}}$ :

$$
\begin{equation*}
\mathrm{I}_{\mathrm{PK}}=\left(\frac{\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\mathrm{S}}}{\mathrm{~L}}\right) \bullet \mathrm{t}_{\mathrm{ON}} \tag{4}
\end{equation*}
$$

The energy stored in $L$ is represented by $1 / 2 \bullet L\left(I_{\text {PK }}\right)^{2}$.
When M1 is turned OFF ( $\mathrm{t}_{\text {off }}$ ), the energy stored in L is emitted through a diode to the output capacitor. Then, the reverse voltage $\left(V_{\mathrm{L}}\right)$ is generated :

$$
\begin{align*}
& V_{L}=\left(V_{\text {OUT }}+V_{D}^{* 1}\right)-V_{\text {IN }} \ldots . . . .  \tag{5}\\
& \quad \text { *1. Diode forward voltage }
\end{align*}
$$

The voltage at CONT pin rises only by $\mathrm{V}_{\text {OUT }}+\mathrm{V}_{\mathrm{D}}$.
The change in the current ( $\mathrm{I}_{\mathrm{L}}$ ) flowing through the diode into $\mathrm{V}_{\text {out }}$ during toff :

$$
\begin{equation*}
\frac{d I_{L}}{d t}=\frac{V_{L}}{L}=\frac{V_{\text {OUT }}+V_{D}-V_{I N}}{L} \tag{6}
\end{equation*}
$$

Integration of the equation (6) is as follows :

$$
\begin{equation*}
I_{L}=I_{P K}-\left(\frac{V_{\text {OUT }}+V_{D}-V_{I N}}{L}\right) \cdot t \tag{7}
\end{equation*}
$$

During ton, the energy is stored in $L$ and is not transmitted to $\mathrm{V}_{\text {оut. }}$ When receiving the output current (lout) from $\mathrm{V}_{\text {out }}$, the energy of the capacitor $\left(C_{L}\right)$ is consumed. As a result, the pin voltage of $C_{L}$ is reduced, and goes to the lowest level after M1 is turned ON (ton). When M1 is turned OFF, the energy stored in $L$ is transmitted through the diode to $\mathrm{C}_{\mathrm{L}}$, and the voltage of $C_{L}$ rises rapidly. $V_{\text {Out }}$ is a time function, and therefore indicates the maximum value (ripple voltage $\left(V_{P-p}\right)$ ) when the current flowing through into $\mathrm{V}_{\text {out }}$ and load current (lout) match.
Next, the ripple voltage is determined as follows.
lout vs. $\mathrm{t}_{1}$ (time) from when M1 is turned OFF (after ton) to when Vout reaches the maximum level :

$$
\begin{align*}
& \mathrm{I}_{\mathrm{OUT}}=\mathrm{I}_{\mathrm{PK}}-\left(\frac{\mathrm{V}_{\text {OUT }}+\mathrm{V}_{\mathrm{D}}-\mathrm{V}_{\mathrm{IN}}}{\mathrm{~L}}\right) \cdot \mathrm{t}_{1} \ldots .  \tag{8}\\
& \therefore \mathrm{t}_{1}=\left(\mathrm{I}_{\mathrm{PK}}-\mathrm{I}_{\mathrm{OUT}}\right) \cdot\left(\frac{\mathrm{L}}{\mathrm{~V}_{\text {OUT }}+\mathrm{V}_{\mathrm{D}}-\mathrm{V}_{\text {IN }}}\right)
\end{align*}
$$

When M 1 is turned OFF (toff), $\mathrm{L}_{\mathrm{L}}=0$ (when the energy of the inductor is completely transmitted). Based on equation (7):

$$
\begin{equation*}
\left(\frac{L}{V_{\mathrm{OUT}}+V_{D}-V_{\mathrm{IN}}}\right)=\frac{t_{\mathrm{OFF}}}{l_{\mathrm{PK}}} \tag{10}
\end{equation*}
$$

When substituting equation (10) for equation (9) :

$$
\begin{equation*}
\mathrm{t}_{1}=\mathrm{t}_{\mathrm{OFF}}-\left(\frac{\mathrm{l}_{\mathrm{OUT}}}{\mathrm{I}_{\mathrm{PK}}}\right) \bullet \mathrm{t}_{\mathrm{OFF}} \tag{11}
\end{equation*}
$$

Electric charge $\Delta Q_{1}$ which is charged in $C_{L}$ during $t_{1}$ :

$$
\begin{equation*}
\Delta Q_{1}=\int_{0}^{t 1} I_{\mathrm{L}} \mathrm{dt}=I_{\mathrm{PK}} \cdot \int_{0}^{\mathrm{t} 1} \mathrm{dt}-\frac{\mathrm{V}_{\mathrm{OUT}}+\mathrm{V}_{\mathrm{D}}-\mathrm{V}_{\mathrm{IN}}}{\mathrm{~L}} \cdot \int_{0}^{\mathrm{t} 1} \mathrm{tdt}=\mathrm{I}_{\mathrm{PK}} \bullet \mathrm{t}_{1}-\frac{\mathrm{V}_{\mathrm{OUT}}+\mathrm{V}_{\mathrm{D}}-\mathrm{V}_{\mathrm{IN}}}{\mathrm{~L}} \cdot \frac{1}{2} \mathrm{t}_{1}{ }^{2} \tag{12}
\end{equation*}
$$

When substituting equation (12) for equation (9) :

$$
\begin{equation*}
\Delta Q 1=I_{\mathrm{PK}}-\frac{1}{2}\left(\mathrm{l}_{\mathrm{PK}}-\mathrm{I}_{\mathrm{OUT}}\right) \bullet \mathrm{t}_{1}=\frac{\mathrm{I}_{\mathrm{PK}}+\mathrm{l}_{\mathrm{OUT}}}{2} \bullet \mathrm{t}_{1} \tag{13}
\end{equation*}
$$

A rise in voltage $\left(\mathrm{V}_{\mathrm{P}-\mathrm{P}}\right)$ due to $\Delta \mathrm{Q}_{1}$ :

$$
\begin{equation*}
\mathrm{V}_{\mathrm{P}-\mathrm{P}}=\frac{\Delta \mathrm{Q}_{1}}{\mathrm{C}_{\mathrm{L}}}=\frac{1}{\mathrm{C}_{\mathrm{L}}} \cdot\left(\frac{\mathrm{I}_{\mathrm{PK}}+\mathrm{I}_{\mathrm{OUT}}}{2}\right) \cdot \mathrm{t}_{1} \tag{14}
\end{equation*}
$$

When taking into consideration lout to be consumed during $t_{1}$ and the Equivalent Series Resistance ( $R_{\text {ESR }}$ ) of $C_{L}$ :

$$
\begin{equation*}
\mathrm{V}_{\mathrm{P}-\mathrm{P}}=\frac{\Delta \mathrm{Q}_{1}}{\mathrm{C}_{\mathrm{L}}}=\frac{1}{\mathrm{C}_{\mathrm{L}}} \cdot\left(\frac{\mathrm{I}_{\mathrm{PK}}+\mathrm{I}_{\mathrm{OUT}}}{2}\right) \cdot \mathrm{t} 1+\left(\frac{\mathrm{I}_{\mathrm{PK}}+\mathrm{I}_{\mathrm{OUT}}}{2}\right) \cdot \mathrm{R}_{\mathrm{ESR}}-\frac{\mathrm{I}_{\mathrm{OUT}} \bullet \mathrm{t}_{1}}{\mathrm{C}_{\mathrm{L}}} \tag{15}
\end{equation*}
$$

When substituting equation (11) for equation (15) :

$$
\begin{equation*}
V_{\mathrm{P}-\mathrm{P}}=\frac{\left(\mathrm{I}_{\mathrm{PK}}-\mathrm{I}_{\mathrm{OUT}}\right)^{2}}{2 \mathrm{I}_{\mathrm{PK}}} \cdot \frac{\mathrm{t}_{\mathrm{OFF}}}{\mathrm{C}_{\mathrm{L}}}+\left(\frac{\mathrm{I}_{\mathrm{PK}}+\mathrm{I}_{\mathrm{OUT}}}{2}\right) \cdot \mathrm{R}_{\mathrm{ESR}} \tag{16}
\end{equation*}
$$

Therefore to reduce the ripple voltage, it is important that the capacitor connected to the output pin has a large capacity and a small $R_{\text {ESR }}$.

## ■ External Parts Selection

The relationship between the major characteristics of the step-up circuit and the characteristics parameters of the external parts are shown in Figure 15.


Figure 15 Relationship between Major Characteristics of Step-up Circuit and External Parts

## 1. Inductor

The inductance value ( $L$ value) has a strong influence on the maximum output current (lout) and efficiency ( $\eta$ ). Figure 16 shows the relationship between the lout and $\eta$ dependency on $L$ of $S-8355 / 56 / 57 / 58$ series.

F, G, H, J, L and M types
CDRH6D28, $\mathrm{V}_{\text {OUT }}=5.0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=3.0 \mathrm{~V}$


Figure 16 L Value vs. lоut Characteristics, L Value vs. $\eta$ Characteristics
The peak current ( $\mathrm{l}_{\mathrm{PK}}$ ) increases by decreasing L value and the stability of the circuit improves and $\mathrm{l}_{\mathrm{OUT}}$ increases. If L value is decreased, the efficiency falls causing a decline in the current drive capacity for the switching transistor, and lout decreases.
The loss of $\mathrm{I}_{\mathrm{PK}}$ by the switching transistor decreases by increasing $L$ value and the efficiency becomes maximum at a certain $L$ value. Further increasing $L$ value decreases the efficiency due to the loss of the direct current resistance of the coil. lout also decreases.
A higher oscillation frequency allows selection of a lower $L$ value, making the coil smaller.
The recommended inductances are a 22 to $100 \mu \mathrm{H}$ for B, E, and K types, a 4.7 to $47 \mu \mathrm{H}$ for $\mathrm{F}, \mathrm{G}, \mathrm{H}, \mathrm{J}, \mathrm{L}$, and M types, 3.0 to $22 \mu \mathrm{H}$ for $\mathrm{N}, \mathrm{P}$ and Q types.
Be careful of the allowable inductor current when choosing an inductor. Exceeding the allowable current of the inductor causes magnetic saturation, much lower efficiency and destruction of the IC chip due to a large current.
Choose an inductor so that $\mathrm{I}_{\mathrm{PK}}$ does not exceed the allowable current. $\mathrm{I}_{\mathrm{PK}}$ in discontinuous mode is calculated by the following equation:
$\mathrm{I}_{\mathrm{PK}}=\sqrt{\frac{2 \mathrm{I}_{\mathrm{OUT}}\left(\mathrm{V}_{\text {OUT }}+\mathrm{V}_{\mathrm{D}}-\mathrm{V}_{\text {IN }}\right)}{\mathrm{f}_{\mathrm{OSC}} \cdot \mathrm{L}}}$ (A)
$\mathrm{f}_{\text {osc }}=$ oscillation frequency, $\mathrm{V}_{\mathrm{D}} \cong 0.4 \mathrm{~V}$.

## 2. Diode

Use an external diode that meets the following requirements :

- Low forward voltage : $\mathrm{V}_{\mathrm{F}}<0.3 \mathrm{~V}$
- High switching speed : 500 ns max.
- Reverse voltage : $V_{\text {OUt }}+\mathrm{V}_{\mathrm{F}}$ or more
- Current rate : $\quad \mathrm{l}_{\mathrm{PK}}$ or more


## 3. Capacitor ( $C_{I N}, C_{L}$ )

A capacitor on the input side $\left(\mathrm{C}_{\mathrm{IN}}\right)$ improves the efficiency by reducing the power impedance and stabilizing the input current. Select a $\mathrm{C}_{\mathbb{1}}$ value according to the impedance of the power supply used.
A capacitor on the output side $\left(\mathrm{C}_{\llcorner }\right)$is used for smoothing the output voltage. For step-up types, the output voltage flows intermittently to the load current, so step-up types need a larger capacitance than step-down types. Therefore, select an appropriate capacitor in accordance with the ripple voltage, which increases in case of a higher output voltage or a higher load current. The capacitor value should be $10 \mu \mathrm{~F}$ or more.
Select an appropriate capacitor the equivalent series resistance ( $\mathrm{R}_{\mathrm{ESR}}$ ) for stable output voltage. The stable voltage range in this IC depends on the $\mathrm{R}_{\text {ESR }}$. Although the inductance value ( L value) is also a factor, an $\mathrm{R}_{\text {ESR }}$ of 30 to 500 $\mathrm{m} \Omega$ maximizes the characteristics. However, the best $R_{\text {ESR }}$ value may depend on the $L$ value, the capacitance, the wiring, and the applications (output load). Therefore, fully evaluate the $\mathrm{R}_{\text {ESR }}$ under the actual operating conditions to determine the best value.
Refer to the "3. Example of Ceramic Capacitor Application" (Figure 26) in the "■ Application Circuit" for the circuit example using a ceramic capacitor and the external resistance of the capacitor ( $\mathrm{R}_{\mathrm{ESR}}$ ).

## 4. External Transistor

An enhancement (N-channel) MOS FET type or A bipolar (NPN) type can be used as the external transistor.

### 4.1 Enhancement (N-channel) MOS FET Type

Figure 17 is a circuit example using a MOS FET transistor (N-channel).

*1. For $V_{D D} / V_{\text {Out }}$ separate type.
*2. With shutdown function.
Figure 17 Circuit Example Using MOS FET (N-channel) Type
An N-channel power MOS FET should be used for the MOS FET. Because the gate voltage and current of the external power MOS FET are supplied from the stepped-up output voltage ( $\mathrm{V}_{\mathrm{OUT}}$ ), the MOS FET is driven more effectively.
A large current may flow during startup, depending on the MOS FET selection. So perform sufficient evaluation using the actual devices. Also recommend to use a MOS FET with an input capacitance of 700 pF or less.
Since the ON-resistance of the MOS FET might depend on the difference between the output voltage ( $\mathrm{V}_{\text {OUT }}$ ) and the threshold voltage of the MOS FET, and affect the output current as well as the efficiency, the threshold voltage should be low. When the output voltage is low, the circuit operates only when the MOS FET has a threshold voltage lower than the output voltage.

### 4.2 Bipolar (NPN) Type

A circuit example using the CPH3210 ( $\mathrm{h}_{\mathrm{FE}}=200$ to 560 ) from Sanyo Electric Co., Ltd. As a bipolar transistor (NPN) is shown in Figure 19 to 24 in the " $\square$ Standard Circuits". The $h_{F E}$ value and $R_{b}$ value of the bipolar transistor determine the driving capacity to increase the output current using a bipolar transistor. A peripheral circuit example of the transistor is shown in Figure 18.

*1. VDD for E, G, J, K, L, M, P and Q types.
Figure 18 External Transistor Peripheral Circuit
The recommended $R_{b}$ value is around $1 \mathrm{k} \Omega$. Actually, calculate the necessary base current $\left(I_{b}\right)$ from the bipolar transistor ( $h_{F E}$ ) using $I_{b}=\frac{I_{P K}}{h_{F E}}$, and select the smaller $R_{b}$ value than $R_{b}=\frac{V_{O U T}-0.7}{I_{b}}-\frac{0.4}{\left|I_{\text {EXTH }}\right|}{ }^{* 1}$.
A small $R_{b}$ value can increase the output current, but the efficiency decreases. Since a current may flow on the pulse and the voltage may drop due to wiring resistance or other factors in the actual circuit, therefore the optimum $R_{b}$ value should be determined by experiment.
Connecting the speed-up capacitor $\left(C_{b}\right)$ in parallel with the $R_{b}$ resistance as shown in Figure 18, decreases switching loss and improves the efficiency.
The $C_{b}$ value is calculated according to $C_{b} \leq \frac{1}{2 \pi \bullet R_{b} \bullet f_{\text {osc }} \bullet 0.7}$.
Select a $C_{b}$ value after performing sufficient evaluation since the optimum $C_{b}$ value differs depending upon the characteristics of the bipolar transistor.
*1. For $E, G, J, K, L, M, P$ and $Q$ type, $R_{b}=\frac{V_{D D}-0.7}{I_{b}}-\frac{0.4}{\left|I_{E X T H}\right|}$.
5. VDD $/ V_{\text {Out }}$ Separate Type (For E, G, J, K, L, M, P and Q Types)

The E, G, J, K, L, M, P and Q types provides separate internal circuit power supply (VDD pin) and output voltage setting pin (VOUT pin) in the IC, making it ideal for the following applications.
(1) When changing the output voltage with external resistance.
(2) When outputting a high voltage such as +15 V or +20 V .

Choose the products in the Table 24 according to the applications (1) or (2) above.
Table 24

| Output Voltage $\left(\mathrm{V}_{\mathrm{CC}}\right)$ | $1.8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}}<5 \mathrm{~V}$ | $5 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}}$ | Reference Circuit |
| :---: | :---: | :---: | :---: |
| $\mathrm{S}-835 \mathrm{x} \times 18$ | Yes | Yes | Application circuit 1 (Figure 25) |
| $\mathrm{S}-835 \mathrm{x} \times 50$ | - | Yes | Application circuit 1 (Figure 25) |
| Connection to VDD pin | $\mathrm{V}_{\text {IN }}$ or $\mathrm{V}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{IN}}$ | - |

Cautions 1. This IC starts a step-up operation at $\mathrm{V}_{\mathrm{DD}}=0.8 \mathrm{~V}$, but set $1.8 \leq \mathrm{V}_{\mathrm{DD}} \leq 10 \mathrm{~V}$ to stabilize the output voltage and frequency of the oscillator. (Input a voltage of 1.8 V or more at the VDD pin for all products with a setting less than 1.9 V .) An input voltage of 1.8 V or more at the VDD pin allows connection of the VDD pin to either the input voltage VIN pin or output VOUT pin.
2. Choose external resistors $R_{A}$ and $R_{B}$ so as to not affect the output voltage, considering that there is impedance between the VOUT pin and VSS pin in the IC chip. The internal resistance between the VOUT pin and VSS pin is as follows :
(1) $\mathrm{S}-835 \mathrm{xx} 18: 2.1$ to $14.8 \mathrm{M} \Omega$
(2) $\mathrm{S}-835 \mathrm{xx} 20: 1.4$ to $14.8 \mathrm{M} \Omega$
(3) $\mathrm{S}-835 \mathrm{xx} 30: 1.4$ to $14.2 \mathrm{M} \Omega$
(4) S-835xx50 : 1.4 to $12.1 \mathrm{M} \Omega$
3. Attach a capacitor $\left(C_{C}\right)$ in parallel to the $R_{A}$ resistance when an unstable event such as oscillation of the output voltage occurs. Calculate $\mathrm{C}_{\mathrm{c}}$ using the following equation :

$$
\mathrm{C}_{\mathrm{c}}[\mathrm{~F}]=\frac{1}{2 \bullet \pi \bullet \mathrm{R}_{\mathrm{A}} \cdot 20 \mathrm{kHz}}
$$

## - Standard Circuits

(1) S-8357BxxMA, S-8357BxxUA, S-8358BxxMA, S-8358BxxUA


Remark The power supply for the IC chip is from the VOUT pin.
Figure 19
(2) S-8357BxxMC, S-8357FxxMC, S-8357HxxMC, S-8357NxxMC, S-8358BxxMC, S-8358FxxMC, S-8358HxxMC, S-8358NxxMC

$\mathrm{ON} / \overline{\mathrm{OFF}}$
Remark The power supply for the IC chip is from the VOUT pin.
Figure 20
(3) S-8357ExxMC, S-8357GxxMC, S-8357JxxMC, S-8357PxxMC, S-8358ExxMC, S-8358GxxMC, S-8358JxxMC, S-8358PxxMC


Remark The power supply for the IC chip is from the VDD pin.
Figure 21
(4) S-8357ExxMC, S-8357GxxMC, S-8357JxxMC, S-8358ExxMC, S-8358GxxMC, S-8358JxxMC


Remark The power supply for the IC chip is from the VDD pin.
Figure 22
(5) S-8355KxxMC, S-8355LxxMC, S-8355MxxMC, S-8355QxxMC, S-8356KxxMC, S-8356LxxMC, S-8356MxxMC, S-8356QxxMC


Remark The power supply for the IC chip is from the VDD pin.
Figure 23
(6) S-8355KxxMC, S-8355LxxMC, S-8355MxxMC, S-8356KxxMC, S-8356LxxMC, S-8356MxxMC


Remark The power supply for the IC chip is from the VDD pin.
Figure 24
Caution The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

## - Precautions

- Mount external capacitors, the diode, and the coil as close as possible to the IC.
- Characteristics ripple voltage and spike noise occur in IC containing switching regulators. Moreover rush current flows at the time of a power supply injection. Because these largely depend on the coil, the capacitor and impedance of power supply used, fully check them using an actually mounted model.
- Make sure that the dissipation of the switching transistor (especially at a high temperature) does not exceed the allowable power dissipation of the package.
- The performance of this IC varies depending on the design of the PCB patterns, peripheral circuits and external parts. Thoroughly test all settings with your device. Also, try to use the recommended external parts. If not, contact an ABLIC Inc. sales person.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- ABLIC Inc. claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.


## - Application Circuits

## 1. LCD Power Supply

The following example is an application power supply circuit ( $15 \mathrm{~V} / 20 \mathrm{~V}$ output) to drive an LCD panel, and its characteristics.


Figure 25 Power Supply Circuit Example for LCD
Table 25

| Condition | Output Voltage | IC | L Type Name | TR Type Name | SD Type Name | $\mathrm{C}_{\mathrm{L}}$ | $\mathrm{R}_{\mathrm{a}}$ | $\mathrm{R}_{\mathrm{b}}$ | Cc |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 15 V | S-8356M50 | CDRH5D18-220 | MCH3405 | MA2Z748 | F 93 <br> $(20 \mathrm{~V}, 10 \mu \mathrm{~F})$ | $580 \mathrm{k} \Omega$ | $300 \mathrm{k} \Omega$ | 15 pF |
| 2 | 20 V | S-8356M50 | CDRH5D18-220 | FDN337N | MA729 | F 93 <br> $(25 \mathrm{~V}, 10 \mu \mathrm{~F})$ | $575 \mathrm{k} \Omega$ | $200 \mathrm{k} \Omega$ | 15 pF |
| 3 | 10 V | S-8356Q50 | CDRH5D18-100 | MCH3405 | MA2Z748 | F 93 <br> $(20 \mathrm{~V}, 10 \mu \mathrm{~F})$ | $560 \mathrm{k} \Omega$ | $560 \mathrm{k} \Omega$ | 15 pF |

Caution The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

## 2. Output Characteristics of The Power Supply for LCD

The data of the step-up characteristics (a) Output current (lout) vs. Efficiency ( $\eta$ ) characteristics, (b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {OUT }}$ ) characteristics under conditions of 1 to 3 in Table $\mathbf{2 5}$ is shown below.

## Condition 1



## Condition 2

(a) Output current (lout) vs. Efficiency ( $\eta$ )


## Condition 3


(b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {Out }}$ )

(b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {Out }}$ )

(b) Output current (lout) vs. Output voltage (Vout)


## 3. Using Ceramic Capacitor Example

When using small $R_{\text {ESR }}$ parts such as ceramic capacitors for the output capacitance, mount a resistor $\left(R_{1}\right)$ corresponding to the $R_{\text {ESR }}$ in series with the ceramic capacitor $\left(C_{L}\right)$ as shown in Figure 26. $\mathrm{R}_{1}$ differs depending on " L " value, the capacitance, the wiring, and the application (output load). The following example shows a circuit using $\mathrm{R}_{1}=100 \mathrm{~m} \Omega$, output voltage $=3.3 \mathrm{~V}$, output load $=500 \mathrm{~mA}$ and its characteristics.


Figure 26 Using Ceramic Capacitor Circuit Example
Table 26

| Condition | IC | L Type Name | TR Type Name | SD Type Name | SD Type Name | R 11 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | S-8357F33 | CDRH6D28-220 | FDN335N | M1FH3 | $10 \mu \mathrm{~F} \times 2$ | $100 \mathrm{~m} \Omega$ |
| 2 | S-8358B50 | CDRH6D28-470 | FDN335N | M1FH3 | $10 \mu \mathrm{~F} \times 2$ | $100 \mathrm{~m} \Omega$ |
| 3 | S-8357N33 | CDRH6D28-100 | FDN335N | M1FH3 | $10 \mu \mathrm{~F} \times 2$ | $100 \mathrm{~m} \Omega$ |

Caution The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

## 4. Output Characteristics of The Using Ceramic Capacitor Circuit Example

The data of the step-up characteristics (a) Output current (lout) vs. Efficiency ( $\eta$ ) characteristics, (b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {OUT }}$ ) characteristics, (c) Output Current (lout) vs. Ripple voltage (Vr) under conditions of 1 to 3 in Table 26 is shown below.

## Condition 1


(c) Output Current (lout) vs. Ripple voltage ( $\mathrm{V}_{\mathrm{r}}$ )


## Condition 2


(c) Output Current (lout) vs. Ripple voltage ( $\mathrm{V}_{\mathrm{r}}$ )

(b) Output current (lout) vs. Output voltage (VOUT)

(b) Output current (lout) vs. Output voltage (VOUT)


## Condition 3



## ■ Characteristics (Typical Data)

1. Example of Major Temperature characteristics ( $\mathrm{Ta}=-40$ to $+85^{\circ} \mathrm{C}$ )
(1) Current Consumption 1 ( $\mathrm{Iss1}_{1}$ ) vs. Temperature ( Ta ) ( $\mathrm{V}_{\text {out }}=3.3 \mathrm{~V}$ )


(2) Current Consumption 2 ( $\mathrm{Iss}_{2}$ ) vs. Temperature ( Ta ) $\left(\mathrm{V}_{\text {OUT }}=3.3 \mathrm{~V}\right.$ )



(3) Current Consumption at Shutdown (lsss) vs. Temperature (Ta)

(4) EXT Pin Output Current "H" (Exth) vs. Temperature (Ta)

(6) Oscillation Frequency (fosc) vs. Temperature ( Ta ) $\left(\mathrm{V}_{\text {Out }}=3.3 \mathrm{~V}\right.$ )




(5) EXT Pin Output Current "L" (lextL) vs. Temperature (Ta)
(7) Maximum Duty Ratio (MaxDuty) vs. Temperature (Ta) (Vout $=3.3 \mathrm{~V}$ )

(8) PWM / PFM Switching Duty Ratio (PFMDuty) vs. Temperature (Ta) (S-8356/58 Series)

(10) ON / $\overline{\text { OFF }}$ Pin Input Voltage "L" $1\left(\mathrm{~V}_{\mathrm{SL} 1}\right)$ vs. Temperature (Ta)

(9) ON/ $\overline{\text { OFF }}$ Pin Input Voltage " H " $\left(\mathrm{V}_{\text {SH }}\right)$ vs. Temperature (Ta)

(11) ON/ $\overline{\text { OFF }}$ Pin Input Voltage "L" $2\left(\mathrm{~V}_{\mathrm{SL}}\right)$ vs. Temperatuer ( Ta )

(12) Soft Start Time ( $\mathrm{tss}^{\text {s }}$ ) vs. Temperature $(\mathrm{Ta})\left(\mathrm{V}_{\text {out }}=3.3 \mathrm{~V}\right)$



(13) Operation Start Voltage ( $\mathrm{V}_{\text {ST1 }}$ ) vs. Temperature ( Ta )

(14) Oscillation Start Voltage ( $\mathrm{V}_{\text {ST2 }}$ ) vs. Temperature ( Ta )

(15) Output Voltage ( $\mathrm{V}_{\text {OUT }}$ ) vs. Temperature $(\mathrm{Ta})\left(\mathrm{V}_{\text {out }}=3.3 \mathrm{~V}\right)$




## 2. Examples of Major Power Supply Dependence Characteristics ( $\mathrm{Ta}=\mathbf{2 5}^{\circ} \mathrm{C}$ )

(1) Current Consumption 1 ( Isss ) vs. Power Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ), Current Consumption 2 ( $\mathrm{I}_{\mathrm{s} 2}$ ) vs. Power Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ) ( $\mathrm{V}_{\text {out }}=3.3 \mathrm{~V}$ )

(2) Current Consumption at Shutdown (lsss) vs. Power Supply Voltage (VD)

(3) Oscillation Friquency (fosc) vs. Power Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ )



(4) Maximum Duty Ratio (MaxDuty) vs. Power Supply Voltage (VDD)



(6) EXT Pin Output Current "L" (lextL) vs.

Power Supply Voltage (VDD)

(7) Output Voltage ( $\mathrm{V}_{\mathrm{OUT}}$ ) vs. Power Supply Voltage $\left(\mathrm{V}_{\mathrm{DD}}\right)\left(\mathrm{V}_{\mathrm{DD}}\right.$ Separate Type, $\mathrm{V}_{\text {out }}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=1.98 \mathrm{~V}$, $\left.\mathrm{I}_{\mathrm{out}}=66 \mathrm{~mA}\right)$




## 3. Output Waveforms

(1) S-8358B33MC


(2) S-8358F33MC

$\mathrm{I}_{\text {OUT }}=100 \mathrm{~mA}$


(3) S-8358N33MC




## 4. Examples of Transient Response Characteristics

(1) Power-On ( $\mathrm{V}_{\text {IN }}: \mathbf{0} \mathbf{V} \rightarrow \mathbf{2}$ V)

$600 \mathrm{kHz}, \mathrm{l}_{\text {OUT }}=1 \mathrm{~mA}$

(2) $\mathrm{ON} / \overline{\mathrm{OFF}}$ Pin Response ( $\mathrm{V}_{\mathrm{ON} / \overline{\mathrm{OFF}}}: 0 \mathrm{~V} \rightarrow \mathbf{2 . 0} \mathrm{~V}$ )


$600 \mathrm{kHz}, \mathrm{l}_{\text {OUT }}=1 \mathrm{~mA}$



300 kHz , lout $=100 \mathrm{~mA}$

$600 \mathrm{kHz}, \mathrm{l}_{\text {out }}=100 \mathrm{~mA}$

(3) Load Fluctuations


300 kHz , lout : $100 \mu \mathrm{~A} \rightarrow 100 \mathrm{~mA}$


600 kHz , lout : $0.1 \mathrm{~mA} \rightarrow 100 \mathrm{~mA}$



300 kHz , lout : $100 \mathrm{~mA} \rightarrow 100 \mu \mathrm{~A}$


600 kHz , lout : $100 \mathrm{~mA} \rightarrow 0.1 \mathrm{~mA}$

(4) Input Voltage Fluctuations (lout $=\mathbf{1 0 0} \mathbf{m A}$ )

$300 \mathrm{kHz}, \mathrm{V}_{\mathrm{IN}}=1.98 \mathrm{~V} \rightarrow 2.64 \mathrm{~V}$

$600 \mathrm{kHz}, \mathrm{V}_{\mathrm{IN}}=1.98 \mathrm{~V} \rightarrow 2.64 \mathrm{~V}$


$300 \mathrm{kHz}, \mathrm{V}_{\mathrm{IN}}=2.64 \mathrm{~V} \rightarrow 1.98 \mathrm{~V}$

$600 \mathrm{kHz}, \mathrm{V}_{\mathrm{IN}}=2.64 \mathrm{~V} \rightarrow 1.98 \mathrm{~V}$


## ■ Reference Data

Use this reference data to choose the external parts. This reference data makes it possible to choose the recommended external part based on the application and characteristics data.

## 1. External Parts for Reference Data

Table 27 Efficiency vs. Output Current Characteristics and Output Voltage vs. Output Current Characteristics for External

| Condition | Product Name | Oscillation Frequency | Output Voltage | Control System | Inductor | Transistor | Diode | Output capacitor |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | S-8357F33 | 300 kHz | 3.3 V | PWM | CDRH104R-220 | FDN335N | M1FH3 | F93 (16 V, 47 $\mu \mathrm{F}) \times 2$ |
| 2 | S-8357F50 | 300 kHz | 5.0 V | PWM |  |  |  |  |
| 3 | S-8356M50 | 300 kHz | 5.0 V | PWM/PFM |  |  |  |  |
| 4 | S-8357B33 | 100 kHz | 3.3 V | PWM | CDRH104R-470 |  |  |  |
| 5 | S-8358B33 | 100 kHz | 3.3 V | PWM/PFM |  |  |  |  |
| 6 | S-8357B50 | 100 kHz | 5.0 V | PWM |  |  |  |  |
| 7 | S-8356M50 | 300 kHz | 5.0 V | PWM/PFM | CDRH8D28-220 |  |  | F93 (16 V, $47 \mu \mathrm{~F}$ ) |
| 8 | S-8357B33 | 100 kHz | 3.3 V | PWM | CDRH8D28-470 |  |  |  |
| 9 | S-8358B33 | 100 kHz | 3.3 V | PWM/PFM |  |  |  |  |
| 10 | S-8357B50 | 100 kHz | 5.0 V | PWM |  |  |  |  |
| 11 | S-8357F33 | 300 kHz | 3.3 V | PWM | CXLP120-220 | MCH3405 | MA2Z748 | F92 (6.3 V, $47 \mu \mathrm{~F})$ |
| 12 | S-8356M50 | 300 kHz | 5.0 V | PWM/PFM |  |  |  |  |
| 13 | S-8357N33 | 600 kHz | 3.3 V | PWM | CDRH8D28-100 | FDN335N | M1FH3 | F93 (16 V, $47 \mu \mathrm{~F})$ |
| 14 | S-8357N50 | 600 kHz | 5.0 V | PWM |  |  |  |  |
| 15 | S-8356Q33 | 600 kHz | 3.3 V | PWM/PFM |  |  |  |  |
| 16 | S-8356Q50 | 600 kHz | 5.0 V | PWM/PFM |  |  |  |  |

STEP-UP, SUPER-SMALL PACKAGE, 600 kHz , PWM CONTROL or PWMPFM SWITCHABLE SWITCHING REGULATOR CONTROLLER

The properties of the external parts are shown below.
Table 28 Properties of External Parts

| Compornent | Product Name | Manufacturer | Characteristics |
| :---: | :---: | :---: | :---: |
| Inductor | CDRH104R-220 | Sumida Corporation | $\begin{aligned} & 22 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=73 \mathrm{~m} \Omega, \mathrm{I}_{\mathrm{MAX}}{ }^{* 2}=2.5 \mathrm{~A}, \\ & \text { Component height }=4.0 \mathrm{~mm} \end{aligned}$ |
|  | CDRH104R-470 |  | $\begin{aligned} & 47 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=128 \mathrm{~m} \Omega, \mathrm{I}_{\mathrm{MAX}}{ }^{* 2}=1.9 \mathrm{~A}, \\ & \text { Component height }=4.0 \mathrm{~mm} \end{aligned}$ |
|  | CDRH8D28-100 |  | $10 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=47 \mathrm{~m} \Omega, \mathrm{I}_{\mathrm{MAX}}{ }^{*}=2.7 \mathrm{~A},$ <br> Component height $=3.0 \mathrm{~mm}$ |
|  | CDRH8D28-220 |  | $\begin{aligned} & 22 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=99 \mathrm{~m} \Omega, \mathrm{I}_{\mathrm{MAX}}{ }^{* 2}=1.8 \mathrm{~A}, \\ & \text { Component height }=3.0 \mathrm{~mm} \end{aligned}$ |
|  | CDRH8D28-470 |  | $47 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=195 \mathrm{~m} \Omega, \mathrm{I}_{\text {MAX }}{ }^{* 2}=1.25 \mathrm{~A},$ $\text { Component height }=3.0 \mathrm{~mm}$ |
|  | CXLP120-220 | Sumitomo Special Metals Co., Ltd. | $\begin{aligned} & 22 \mu \mathrm{H}, \mathrm{DCR}^{* 1}=590 \mathrm{~m} \Omega, \mathrm{I}_{\text {MAX }}{ }^{{ }^{*}{ }^{2}=}=0.55 \mathrm{~A}, \\ & \text { Component height }=1.2 \mathrm{~mm} \end{aligned}$ |
| Diode | M1FH3 | Shindengen Electric Manufacturing Co., Ltd. | $\mathrm{V}_{\mathrm{F}}{ }^{3}=0.3 \mathrm{~V}, \mathrm{IF}^{*}{ }^{4}=1.5 \mathrm{~A}$ |
|  | MA2Z748 | Matsushita Electric Industrial Co., Ltd. | $\mathrm{V}_{\mathrm{F}}{ }^{3}=0.4 \mathrm{~V}, \mathrm{IF}^{*}{ }^{4}=0.3 \mathrm{~A}$ |
| Capacitor <br> (Output Capacitance) | F93 | Nichicon Corporation | $16 \mathrm{~V}, 47 \mu \mathrm{~F}$ |
|  | F92 |  | $6.3 \mathrm{~V}, 47 \mu \mathrm{~F}$ |
| Transistor (Nch FET) | FDN335N | Fairchild Semiconductor Japan Ltd. | $\begin{aligned} & \mathrm{V}_{\mathrm{DSS}}{ }^{{ }^{55}}=20 \mathrm{~V} \text { max., } \mathrm{V}_{\mathrm{Gss}}{ }^{{ }^{6} 6}=8 \mathrm{~V} \text { max., } \\ & \mathrm{C}_{\text {ISS }}{ }^{{ }^{7}}=310 \mathrm{pF}, \mathrm{ID}^{{ }^{+8}}=1.5 \mathrm{~A} \\ & \left(\mathrm{~V}_{\mathrm{GS}}{ }^{* 6}=2.5 \mathrm{~V}\right) \end{aligned}$ |
|  | MCH3405 | Sanyo Electric Co., Ltd. |  |

*1. Direct current resistance
*2. Maximum allowable current
*3. Forward voltage
*4. Forward current
*5. Drain source voltage
*6. Gate source voltage
*7. Input capacitance
*8. Drain current

Caution The values shown in the characteristics column of Table 28 above are based on the materials provided by each manufacture. However, consider the characteristics of the original materials when using the above products.

## 2. Output Current (lout) vs. Efficiency ( $\eta$ ) Characteristics, Output Current (lout) vs. Output Voltage (Vout) Characteristics

The following shows the actual (a) Output current (lout) vs. Efficiency ( $\eta$ ) characteristics and (b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\mathrm{OUT}}$ ) characteristics under the conditions of No. 1 to 16 in Table 27.

## Condition 1 S-8357F33

(a) Output current (lout) vs. Efficiency ( $\eta$ )


Condition 2 S-8357F50
(a) Output current (lout) vs. Efficiency ( $\eta$ )


Condition 3 S-8356M50
(a) Output current (lout) vs. Efficiency ( $\eta$ )

(b) Output current (lout) vs. Output voltage (VOUT)

(b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {OUT }}$ )

(b) Output current (lout) vs. Output voltage ( $\left.\mathrm{V}_{\text {OUT }}\right)$


## Condition 4 S-8357B33



Condition 5 S-8358B33


## Condition 6 S-8357B50



## Condition 7 S-8357M50



(b) Output current (lout) vs. Output voltage (VOUT)

(b) Output current (lout) vs. Output voltage (VOut)

(b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {Out }}$ )


## Condition 8 S-8357B33



Condition 9 S-8358B33


Condition 10 S-8357B50


Condition 11 S-8357F33

(b) Output current (lout) vs. Output voltage (Vout)

(b) Output current (lout) vs. Output voltage (VOUT)


(b) Output current (lout) vs. Output voltage (VOUT)


Condition 12 S-8356M50


Condition 13 S-8357N33


Condition 14 S-8357N50


Condition 15 S-8356Q33

(b) Output current (lout) vs. Output voltage (VOUT)

(b) Output current (lout) vs. Output voltage (VOut)


(b) Output current (lout) vs. Output voltage (VOUT)


## Condition 16 S-8356Q50




## 3. Output Current (lout) vs. Ripple Voltage (Vr) Characteristics

The following shows the actual Output current (lout) vs. Ripple voltage (Vr) characteristics and (b) Output current (lout) vs. Output voltage ( $\mathrm{V}_{\text {OUT }}$ ) characteristics under the conditions of No. 1 to 16 in Table 27.

## Condition 1 S-8357F33



Condition 3 S-8356M50


Condition 5 S-8358B33


Condition 7 S-8356M50


Condition 2 S-8357F50


Condition 4 S-8357B33


Condition 6 S-8357B50


Condition 8 S-8357B33


## Condition 9 S-8358B33



Condition 11 S-8357F33


Condition 13 S-8357N33


Condition 15 S-8356Q33


Condition 10 S-8357B50


Condition 12 S-8356M50


Condition 14 S-8357N50


Condition 16 S-8356Q50



No. MP003-A-P-SD-1.2

| TITLE | SOT233-A-PKG Dimensions |
| :---: | :---: |
| No. | MP003-A-P-SD-1.2 |
| ANGLE | $\notin$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP003-A-C-SD-2.0

| TITLE | SOT233-A-Carrier Tape |
| :---: | :---: |
| No. | MP003-A-C-SD-2.0 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP003-A-R-SD-1.1

| TITLE | SOT233-A-Reel |  |
| :---: | :---: | :---: |
|  | MP003-A-R-SD-1.1 |  |
| ANGLE |  | QTY. |
| UNIT | mm |  |
|  |  |  |
|  |  |  |
| ABLIC Inc. |  |  |



| TITLE | SOT235-A-PKG Dimensions |
| :---: | :---: |
| No. | MP005-A-P-SD-1.3 |
| ANGLE | $\rightarrow$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP005-A-C-SD-2.1

| TITLE | SOT235-A-Carrier Tape |
| :---: | :---: |
| No. | MP005-A-C-SD-2.1 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. MP005-A-R-SD-1.1

| TITLE | SOT235-A-Reel |  |  |
| :---: | :---: | :---: | :---: |
|  | MP005-A-R-SD-1.1 |  |  |
| ANGLE |  | QTY. |  |
|  | 3,000 |  |  |
| UNIT | mm |  |  |
|  |  |  |  |
| ABLIC Inc. |  |  |  |



No. UP003-A-P-SD-2.0

| TITLE | SOT893-A-PKG Dimensions |
| :---: | :---: |
| No. | UP003-A-P-SD-2.0 |
| ANGLE | $\rightarrow$ |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



No. UP003-A-C-SD-2.0

| TITLE | SOT893-A-Carrier Tape |
| :---: | :---: |
| No. | UP003-A-C-SD-2.0 |
| ANGLE |  |
| UNIT | mm |
|  |  |
|  |  |
| ABLIC Inc. |  |



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NCP81203MNTXG NCP81206MNTXG NX2155HCUPTR UBA2051C MAX8778ETJ+ NTBV30N20T4G NCP1015ST65T3G
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