

Moisture Sensitivity Level (MSL)–This product is not Moisture Sensitive MSL = N/A: Not Applicable

> FEATURES:

- 5x7x2 mm SMT, RoHS Compliant reflow-able package
- Frequency translation to either 20MHz or 40MHz carrier @ +3.3V Supply Voltage
- Industrial operating temperature range (-40°C to +85°C)
- LVCMOS Output
- \bullet Internal absolute pull range $> \pm 100$ ppm allowing for long term drift correction

> APPLICATIONS:

- Frequency translation, clock smoothing and jitter attenuation of the input 10MHz reference
- Datacom DSLAM, DSLAR, Access Nodes
- Cable modem head end
- Base Station GSM, CDMA
- Telecom SONET/SDH/ATM

GENERAL DESCRIPTION

The ABFT series is an Ultra Low Jitter VCXO based frequency translator; ideally suited to improve Jitter characteristics of the input signal. This device is designed to provide input clock smoothing - while providing Phase and Frequency Locked higher frequency translated output.

Typical application will take a 10MHz reference frequency and phase & frequency lock it to either a 20MHz or a 40MHz Low Jitter VCXO. The implemented technology significantly attenuates the jitter content of the 10MHz reference signal; while keeping the higher frequency RF Output - Frequency and Phase Coherent with the input 10MHz reference signal.

> STANDARD SPECIFICATIONS:

Parameters	Minimum	Typical	Maximum	Units	Notes
Resonant Frequency		20.000 <i>Or</i> 40.00		MHz	See options
Operating Temperature	-40		+85	°C	
Storage Temperature	-40		+85	°C	
Supply Voltage (Vdd)	3.135	3.3	3.465	V	3.3V±5%
Input Signal Characteristics Frequency Signal level	9.999800 0.300	10.000000	10.000200	MHz Vp-p	Input signal must be with-in ±20.00 ppm from 10.00MHz carrier for the ABFT device to achieve lock
Lock Time	0.500	< 20	50	ms	device to demove rock
Frequency Stability Over Temperature (Note # 1) Internal Frequency Pull Range	-25.00 ±100.00		+25.00	ppb	Referenced to the stable input reference of 10.00MHz (such as a Stratum-III TCXO or an OCXO) This is the internal pull range of the ABFT device providing sufficient correction range to account for internal aging, stand-alone temperature variation, etc.
Supply Current (I _{DD})		< 14.0	20.00	mA	Under Lock
RF output Characteristics					
Output Load: Rise Time (Tr) Fall Time (Tf) Symmetry Output Voltage (V _{OH}) Output Voltage (V _{OL})	45 0.9*Vdd	853 526 48/52	15 10 1200 1200 55 0.1*Vdd	pF kΩ ps ps % V	@1/2Vdd
	-5.0		+5.0		@+25°C First year
Stand alone Aging (Note # 2)	-12.0		+12.0	ppm	@+25°C After 10 years

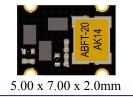
(Note # 1): The frequency stability over temperature of the ABFT device is greatly dependant on the short term perturbations of the input reference signal. (Note # 2): The Aging characteristics of the Quartz used inside the ABFT solution are such that, the stand-alone aging will not exceed ±12.00 ppm over a 10-year product life; referenced to the initial measured frequency post reflow in end application







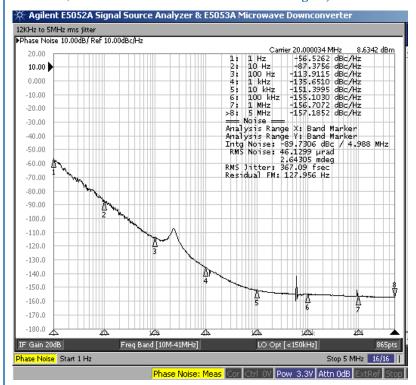




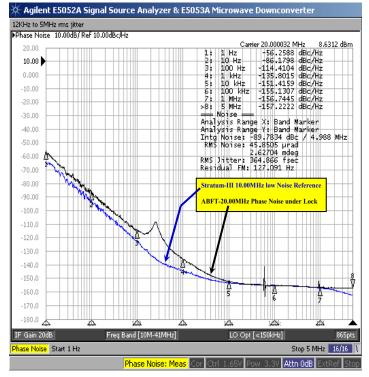
Parameters	Minimum	Typical	Maximum	Units	Notes
Phase Noise @ 20MHz carrier					Close to the carrier phase noise is dependent on the
1Hz offset from the carrier		-56			cleanliness of the
10Hz offset from the carrier		-87			input reference.
100Hz offset from the carrier		-113		dBc/Hz	However, at 1kHz
1,000Hz offset from the carrier		-135	-130		offset and beyond,
10,000Hz offset from the carrier		-151	-145		ABFT phase noise is
100,000Hz offset from the carrier		-155	-150		practically
1,000,000Hz offset from the carrier		-156	-150		independent of the
5,000,000Hz offset from the carrier		-157	-155		input reference noise

> PHASE NOISE

Phase Noise under lock (ABFT-20.00MHz with input connected to a low noise, stable 10.00MHz Stratum-IIIreference signal)



DetailedComparative Phase Noise Plot (Stratum-III 10.0MHz as Reference)

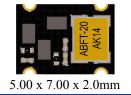




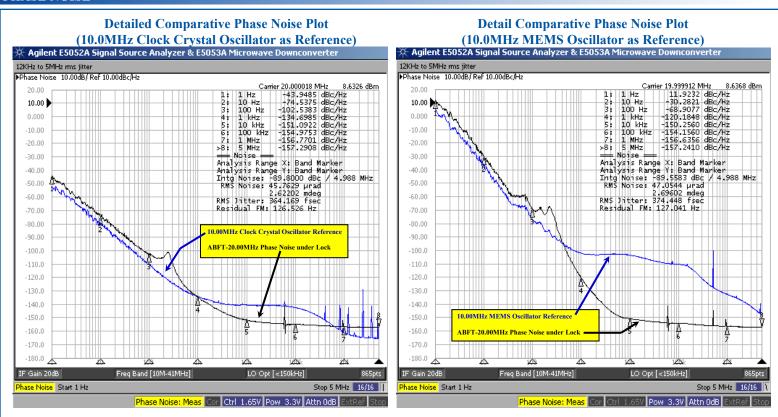




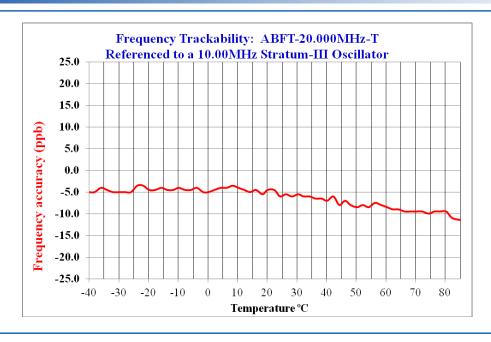




> PHASE NOISE



FREQUENCY TRACKING OVER TEMPERATURE

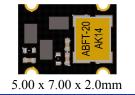




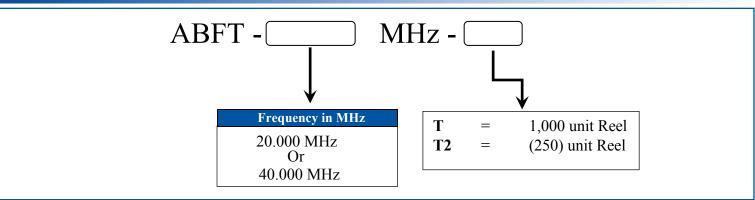




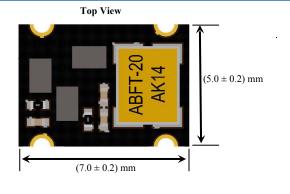


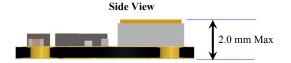




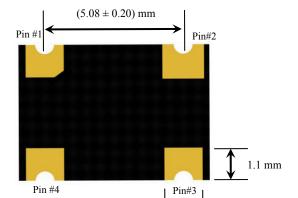


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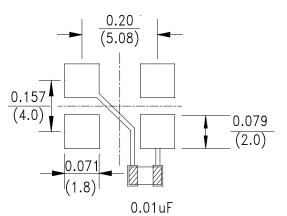


Bottom View



1.4 mm

Recommended land pattern



Dimension: <u>Inches</u> (mm)

PIN#	Name
1	10.0MHz Reference Input
2	GND
3	Locked - Output
4	Vdd

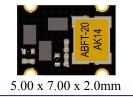
Note: Abracon recommends a 0.01 uF bypass capacitor between pin#2 and pin#4

ALL Dimensions are in (mm)

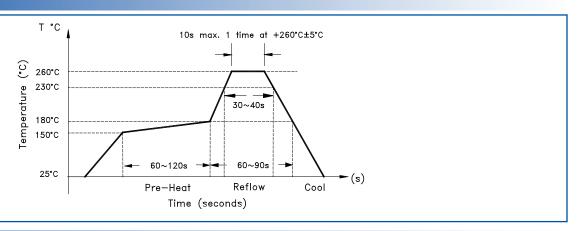


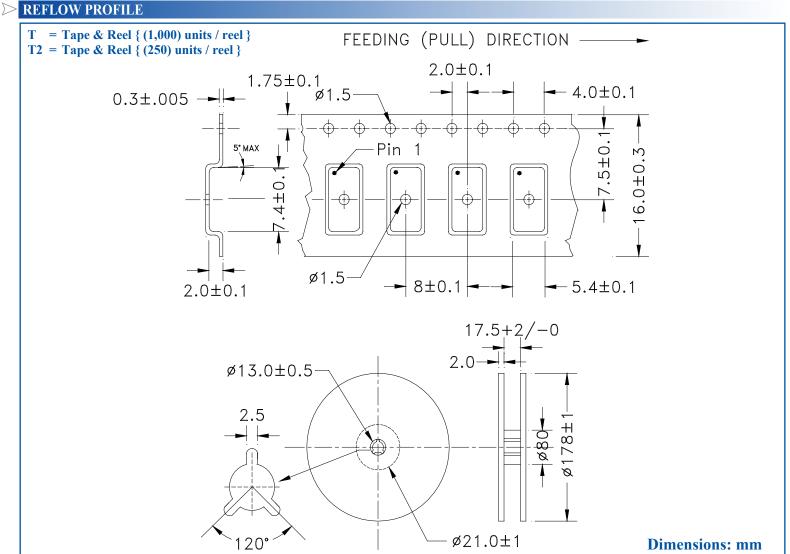
ABFT











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