Programmable - High Performance SMD Crystal Oscillator

ASG-C Series

Moisture Sensitivity Level (MSL) - This product is Hermetically Sealed and not Moisture Sensitive; therefore MSL = N/A (Not Applicable)

FEATURES:

- ASG series is a High Performance crystal based oscillator; available either as an XO or a VCXO
- \bullet Frequency range from 10MHz to 250MHz with LVCMOS output
- Available from 10MHz to 1.50GHz with LVDS or LVPECL output
- Offered with either 2.50V or 3.30V bias voltage
- Quick turn, 1~5 business days for small quantity orders

STANDARD SPECIFICATIONS:



Pb

• Networking, SONET/SDH

RoHS

Compliant

- WiMax / WLAN
- Computing
- Phase Locked Loops
- Direct Digital Synthesis (DDS)
- DSL/ADSL
- Base Terminal Stations

Parameters	5	Minimum	Typical	Maximum	Units	Notes
Erequerey Democr	$V_{dd} = 3.3V$	10		250	MHz	
Frequency Kange:	$V_{dd} = 2.5 V$	10		250	MHz	
Operating Temperature:		-40		+85	°C	
Storage Temperature:		-55		+125	°C	
Overall Frequency Stabili	ty:	-50		+50	ppm	<i>See Note # 1</i>
Initial Set Tolerance		-5.00	$\leq \pm 1.00$	+5.00	ppm	
Stability over operating temperature		-35.00	<u>≤</u> ±20.00	+35.00	ppm	
Aging @ 25°C over 10-years		-7.00		+7.00	ppm	
Frequency variation over supply voltage change (±5%)		-2.00		+2.00	ppm	
Frequency variation o variation (15pF ± 5%)	ver load	-1.00		+1.00	ppm	
	$V_{dd} = 3.3V$	3.135	3.300	3.465	V	
Supply voltage (vdd):	$V_{dd} = 2.5 V$	2.375	2.500	2.625	V	
Imput Cumonti	$V_{dd} = 3.3V$			45	mA	Frequency dependent
Input Current:	$V_{dd} = 2.5 V$			35	mA	Frequency dependent
Symmetry:	ymmetry:		50	52	%	@ 1/2Vdd
Rise and Fall Time (Tr/Tf	1 Time (Tr/Tf): ≤ 450		1000	ps		
Output Load:				15	pF	CMOS
Outrast Waltana	VOH	Vdd * 0.90			V	r
Output voltage.	VOL	VOL Vdd * 0.10 V				
Start-up Time:			<u>≤</u> 2.0	3.0	ms	
Enable/Disable Function :		"1" $(V_{IH} \ge 0.7*$ "0" $(V_{IL} < 0.3*$	0.7*Vdd) or Open: Oscillation 0.3*Vdd) : High Z			
Vcontrol Range		0.00		Vdd	Volts	
Frequency Pull		±50			ppm	
Control Port Bandwidth		10			kHz	
	Integer Mode		< 0.60	1.60	ps	12kHz to 20MHz
Phase jitter RMS [tjit(ϕ)] See Note #2	Fractional Mode		< 0.90	1.60	ps	12kHz to 20MHz

Note #1: Inclusive of initial tolerance at 25°C±3°C, operating temperature range, input voltage variation, load variation & aging.
 Note #2: The rms jitter over 12kHz to 20MHz Bandwidth is dependent on the carrier and whether or not the final frequency is achieved without engaging the Fractional Mode







7.0 x 5.0 x 2.0mm

ASG-C Series

OVERALL SYSTEM BLOCK DIAGRAM



> PHASE NOISE & JITTER CHARACTERISTICS

100MHz Carrier 🔆 Agilent E5052A Signal Source Analyzer & E5053A Microwave Downconverte 12KHz to 20MHz rms jitte Phase Noise 10.00dB/ Ref 0.000dBc/Hz 10.00 0.000 -10.00 -20,00 -30.00 -40,00 -50.00 -60,00 -70,00 -80,00 -90.00 -100.0 -110.0 -120.0 -130.0 -140.0 -150.0 -160.0 -170.0 -180.0 -190.0

156.25MHz Carrier

RoHS

Compliant

Pb







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7.0 x 5.0 x 2.0mm

Programmable - High Performance SMD Crystal Oscillator ASG-C Series Frequency Stability Vs. Temperature Prequency Stability Vs. Temperature





PART IDENTIFICATION:



MARKING:







Programmable - High Performance SMD Crystal Oscillator

ASG-C Series





7.0 x 5.0 x 2.0mm

OUTLINE DIMENSIONS:

		ASG GYWW 5.0
Pin #	Pin Description	
	FOT VCAO configuration	Pin # 1 Bottom View
1	Voltage Control for VCXO	
2	Output Enable (OE)	5.08
3	GND	
4	RF Output	
5	N/C	
6	Vdd	R0.71-1
	<u> </u>	<u>+</u>
Pin #	Pin Description For XO configuration	
1	Output Enable (OE)	
2	N/C for XO	
3	GND	Side View
4	RF Output	
	N/C	
5	IN/C	190 Max

REFLOW PROFILE:







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ASG-C Series



ASG

7.0 x 5.0 x 2.0mm

> TAPE & REEL:



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 VG-4231CE 27.0000M-PSCM3
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 ECXV-L37C2N-148.500
 LFVCX0067507Bulk
 VS-501-0074

 1000M0
 LFVCX0067538Bulk
 515CDA5M00000AAG
 S15CDA5M00000AAG
 S15CDA5M00000AAG