EMS43 Series



REGULATORY COMPLIANCE

Lead Free	EU RoHS	China RoHS	REACH
\bigotimes	2011/65 + 2015/863	e	SVHC
COMPLIANT	COMPLIANT	COMPLIANT	COMPLIANT



ITEM DESCRIPTION

Spread Spectrum MEMS Clock Oscillators LVCMOS (CMOS) 3.3Vdc 4 Pad 2.0mm x 2.5mm Plastic Surface Mount (SMD)

ELECTRICAL SPECIFICATIONS			
Nominal Frequency	1MHz to 175MHz		
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, 260°C Reflow, Shock, and Vibration ±100ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C ±100ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C		
Aging at 25°C	±1ppm Maximum First Year		
Supply Voltage	3.3Vdc ±10%		
Maximum Supply Voltage	-0.5Vdc to +3.65Vdc		
Input Current	Unloaded; Nominal Vdd 30mA Maximum over Nominal Frequency of 1MHz to 25MHz 40mA Maximum over Nominal Frequency of 25.000001MHz to 175MHz		
Output Voltage Logic High (V _{он})	IOH=-8mA 90% of Vdd Minimum		
Output Voltage Logic Low (V₀∟)	IOL=+8mA 10% of Vdd Maximum		
Rise/Fall Time	Measured from 20% to 80% of waveform 2nSe <mark>c Maxim</mark> um		
Duty Cycle	Measured at 50% of waveform 50 ±5(%) over Nominal Frequency of 1MHz to 75MHz 50 ±10(%) over Nominal Frequency of 75.000001MHz to 175MHz		
Load Drive Capability	15pF Maximum		
Output Logic Type	CMOS		
Output Control Function	Tri-State (Disabled Output High Impedance) Power Down (Disabled Output Logic Low) Spread Disable (Spread Spectrum On Output Disabled)		
Power Down Input Voltage (Vih and Vil)	70% of Vdd Minimum or No Connection to Enable Output, 30% of Vdd Maximum to Disable Output (Disabled Output Logic Low)		
Tri-State Inpu <mark>t Voltage</mark> (Vih and Vil)	70% of Vdd Minimum or No Connection to Enable Output, 30% of Vdd Maximum to Disable Output (Disabled Output High Impedance)		
Disable Current	Pad 1=Ground 20mA Maximum (Disabled Output: High Impedance)		
Standby Current	Pad 1=Ground 50µA Maximum (Disabled Output: Logic Low)		
Spread Spectrum Input Voltage	70% of Vdd Minimum or No Connection to Enable Spread Spectrum-On Output,		
(Vih and Vil) Spread Spectrum	30% of Vdd Maximum to Disable Spread Spectrum-On Output (Spread Spectrum On Output Disabled)		
Spread Spectrum	 ±0.25% Center Spread (Not available with Output Control Function of Spread Disable) ±0.50% Center Spread (Not available with Output Control Function of Spread Disable) ±1.00% Center Spread (Not available with Output Control Function of Spread Disable) -0.50% Down Spread -1.00% Down Spread -2.00% Down Spread 		
Modulation Frequency	30kHz Minimum, 32kHz Typical, 35kHz Maximum		
Period Jitter	Cycle to Cycle; Spread Spectrum-On; Fo=133.333M, Vdd=3.3Vdc 30pSec Maximum		
Start Up Time	10mSec Maximum		

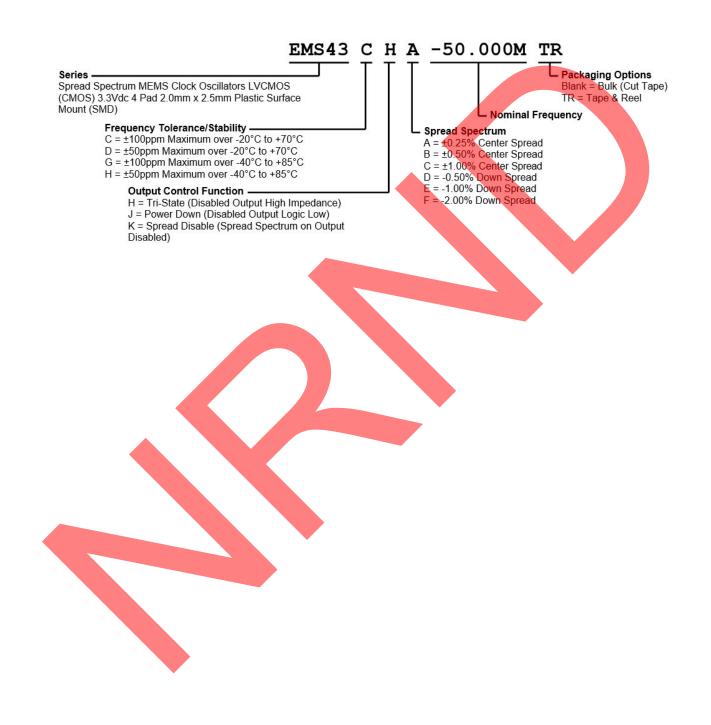
EMS43 Series



Storage Temperature Range

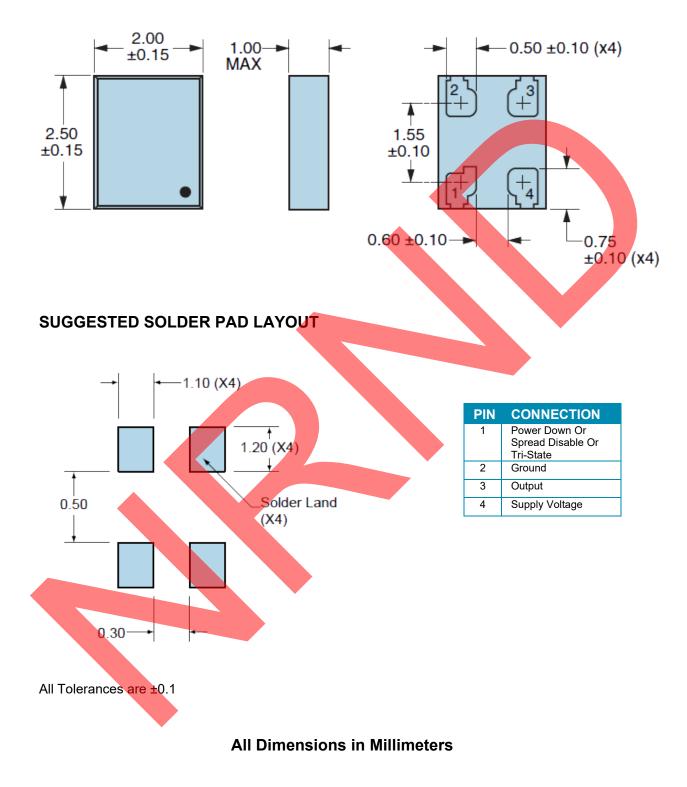
-55°C to +125°C

PART NUMBERING GUIDE



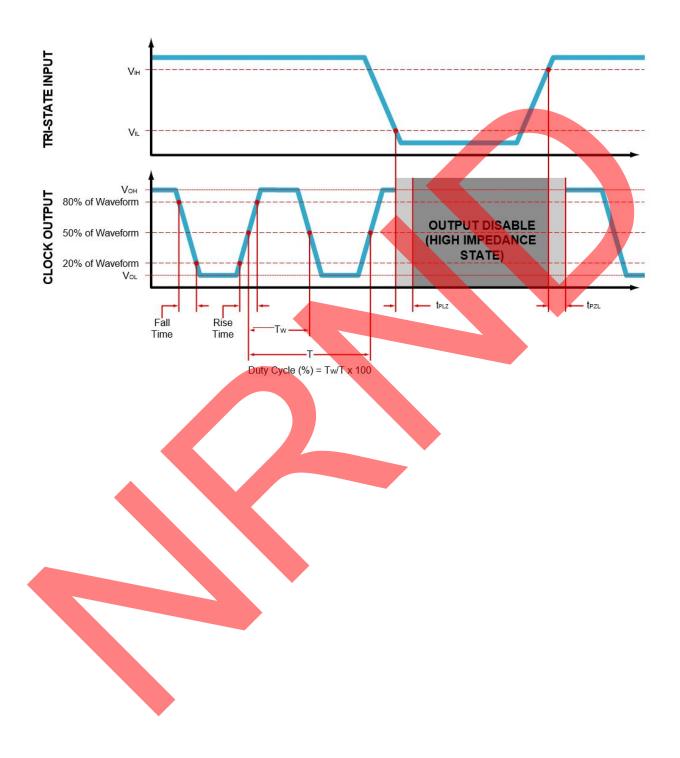


MECHANICAL DIMENSIONS



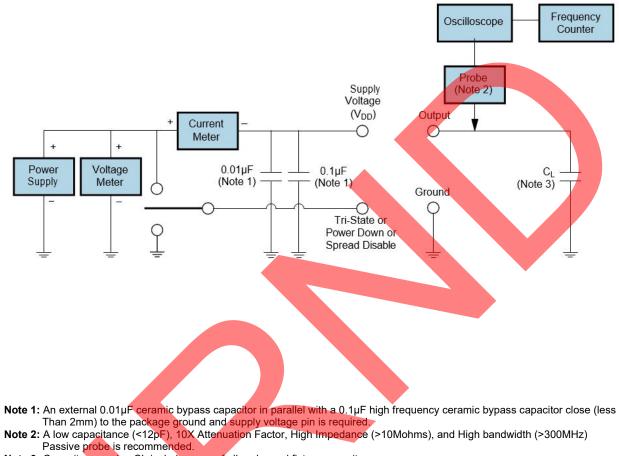


OUTPUT WAVEFORM & TIMING DIAGRAM

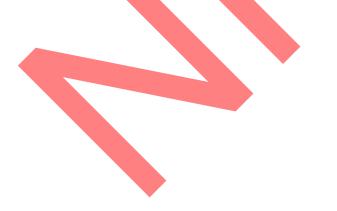




TEST CIRCUIT FOR CMOS OUTPUT



Note 3: Capacitance value CL includes sum of all probe and fixture capacitance.



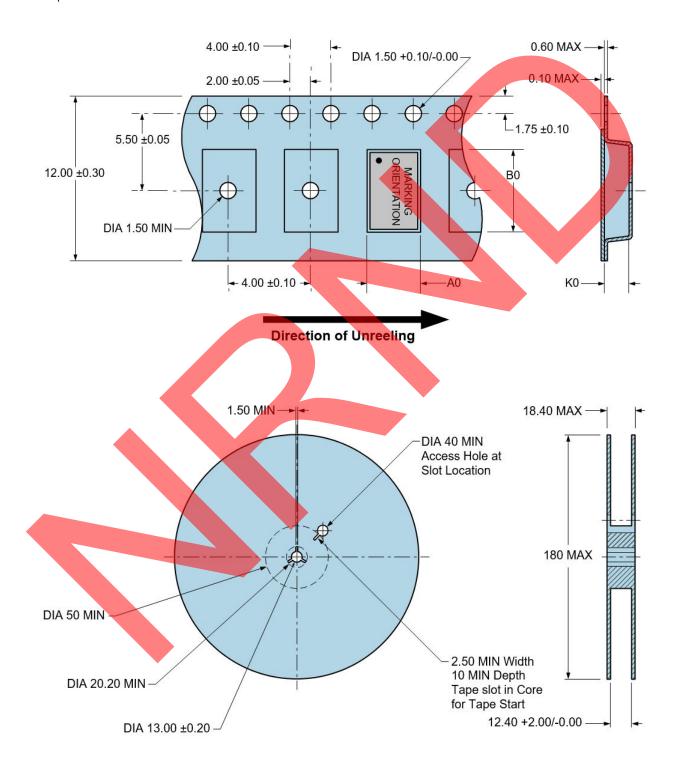
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TAPE & REEL DIMENSIONS

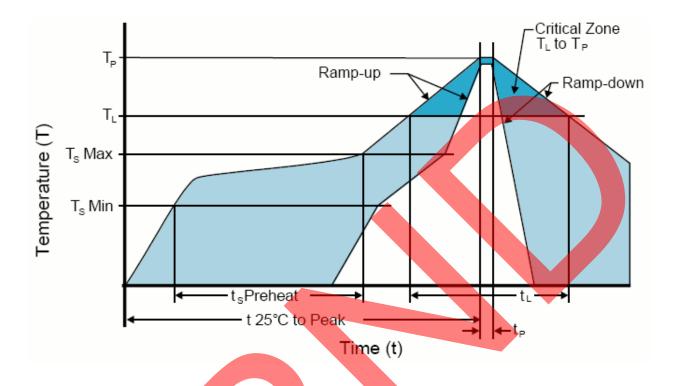
Quantity per Reel: 1,000 Units All Dimensions in Millimeters

Compliant to EIA-481





RECOMMENDED SOLDER REFLOW METHOD



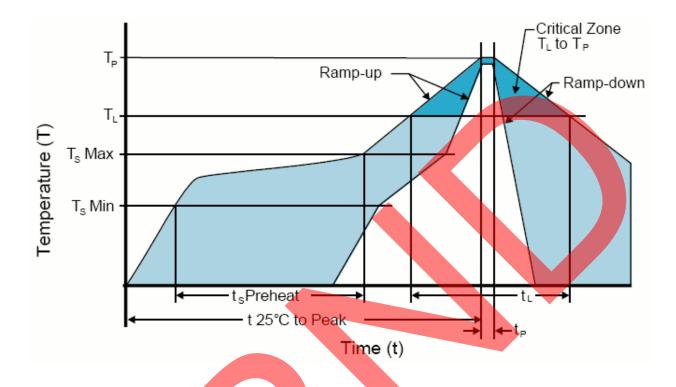
HIGH TEMPERATURE INFRARE	D/CONVECTION	
T_s MAX to T_L (Ramp-up Rate)	3°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	150°C	
- Temperature Typical (T _s TYP)	175°C	
- Temperature Maximum(T _s MAX)		
- Time (t _s)	60 - 180 Seconds	
Ramp-up Rate (T_L to T_P)	3°C/Second Maximum	
Time Maintained Above:		
- Temperature (T _L)	217°C	
- Time (t _L)	60 - 150 Seconds	
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum	
Target Peak Temperature(T _P Target)	250°C +0/-5°C	
Time within 5°C of actual peak (t _p)	20 - 40 Seconds	
Ramp-down Rate	6°C/Second Maximum	
Time 25°C to Peak Temperature (t)	8 Minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARE	D/CONVECTION	
T_s MAX to T_L (Ramp-up Rate)	5°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (T _s TYP)	150°C	
- Temperature Maximum(T _s MAX)		
- Time (t _s)	60 - 120 Seconds	
Ramp-up Rate (T_L to T_P)	5°C/Second Maximum	
Time Maintained Above:		
- Temperature (T _L)	150°C	
- Time (t _L)	200 Seconds Maximum	
Peak Temperature (T _P)	240°C Maximum	
Target Peak Temperature(T _P Target)	240°C Maximum 2 Times/230°C Maximum 1Time	
Time within 5°C of actual peak (t _P)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time	
Ramp-down Rate	5°C/Second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

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