

### ● General Description

The AGM25N15C combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### ● Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

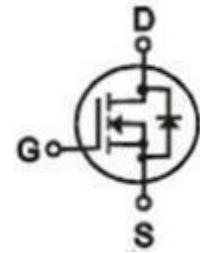
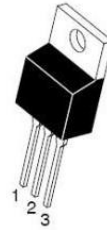
### ● Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### Product Summary

BVDSS	RDSON	ID
150V	20.5mΩ	52A

### TO-220C Pin Configuration



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM25N15C	AGM25N15C	TO-220C	----	----	1000

**Table 1. Absolute Maximum Ratings (TA=25°C)**

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	150	V
VGS	Gate-Source Voltage (VDS=0V)	±25	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	52	A
	Drain Current-Continuous(Tc=100°C)	27	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	180	A
PD	Maximum Power Dissipation(Tc=25°C)	125	w
	Maximum Power Dissipation(Tc=100°C)	50	w
EAS	Avalanche energy <b>(Note 3)</b>	361	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

**Table 2. Thermal Characteristic**

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	60	°C/W
RθJC	Thermal Resistance Junction-Case <sup>1</sup>	---	1.0	°C/W

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	150	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=150V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±25V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	2.5	3.0	4.0	V
gFS	Forward Transconductance	VDS=5V,ID=20A	--	--	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=30A	--	20.5	30	mΩ
		VGS=4.5V, ID=30A	--	--	--	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=75V,VGS=0V, F=1MHZ	--	850	--	pF
Coss	Output Capacitance		--	75	--	pF
Crss	Reverse Transfer Capacitance		--	15	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	1.0	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V,VDS=75V, ID=30A,RGEN=6Ω	--	8.3	--	nS
tr	Turn-on Rise Time		--	6.9	--	nS
td(off)	Turn-Off Delay Time		--	14	--	nS
tf	Turn-Off Fall Time		--	7.0	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=75V, ID=9A	--	8.3	--	nC
Qgs	Gate-Source Charge		--	7.1	--	nC
Qgd	Gate-Drain Charge		--	14	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	52	A
VSD	Forward on Voltage	VGS=0V,IS=9A	--	--	1.2	V
trr	Reverse Recovery Time	IF=9A , dI/dt=500A/μs , TJ=25°C	--	150	--	ns
Qrr	Reverse Recovery Charge		--	150	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Typical Characteristics

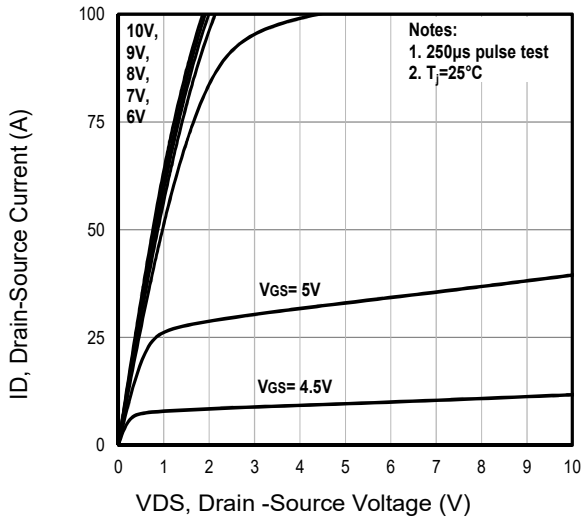


Fig1. Typical Output Characteristics

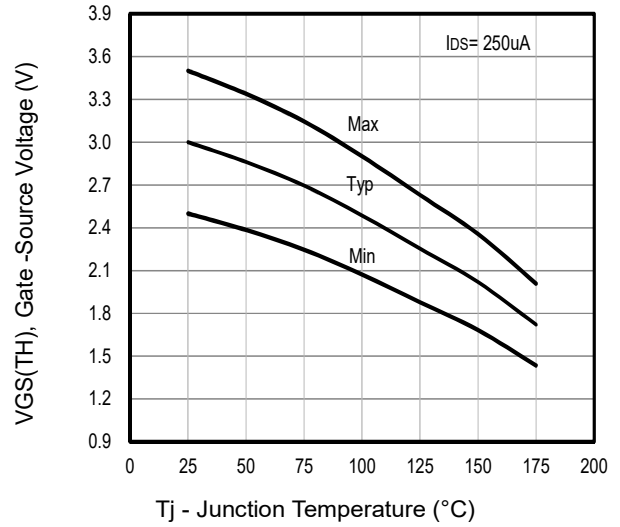


Fig2. Typical  $V_{GS(TH)}$  Gate-Source Voltage Vs.  $T_j$

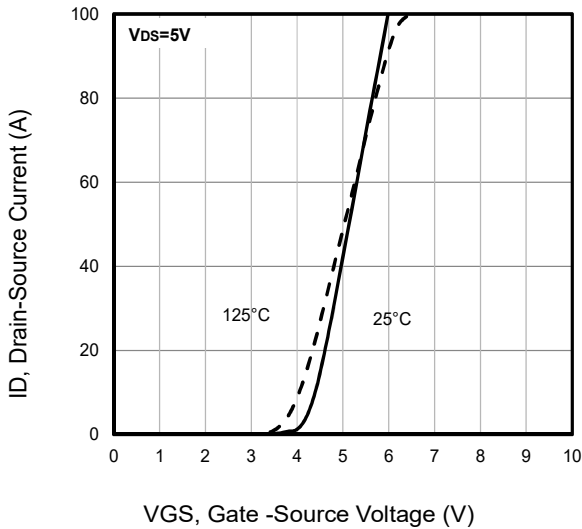


Fig3. Typical Transfer Characteristics

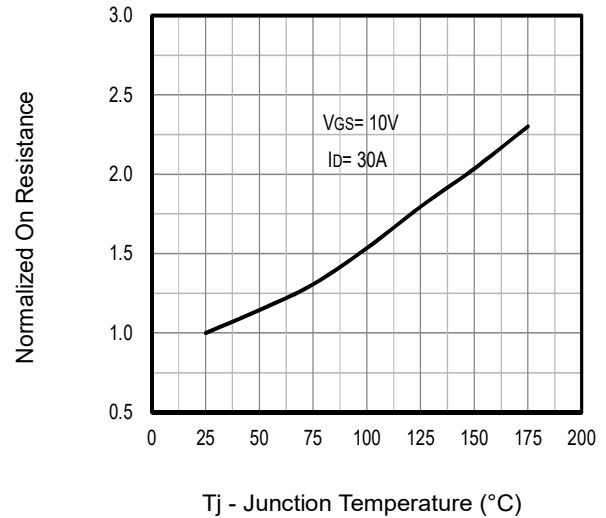


Fig4. Typical Normalized On-Resistance Vs.  $T_j$

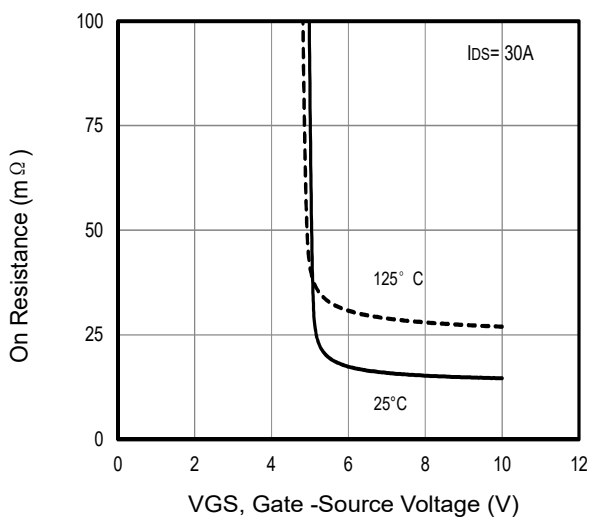


Fig5. Typical On Resistance Vs Gate-Source Voltage

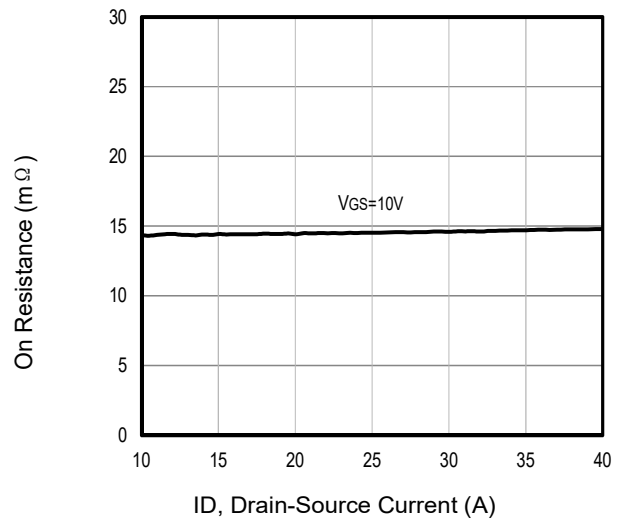


Fig6. Typical On Resistance Vs Drain Current

## Typical Characteristics

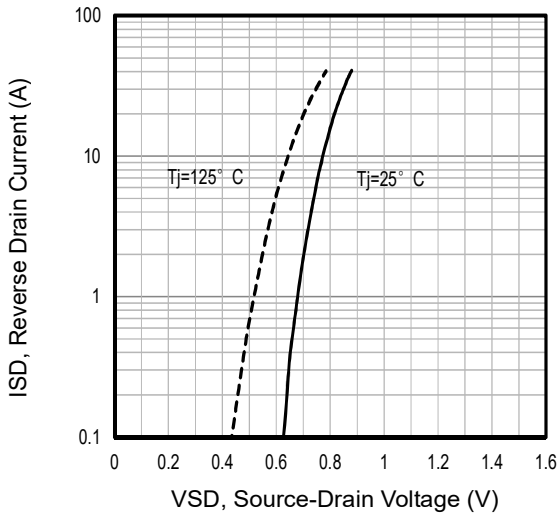


Fig7. Typical Source-Drain Diode Forward Voltage

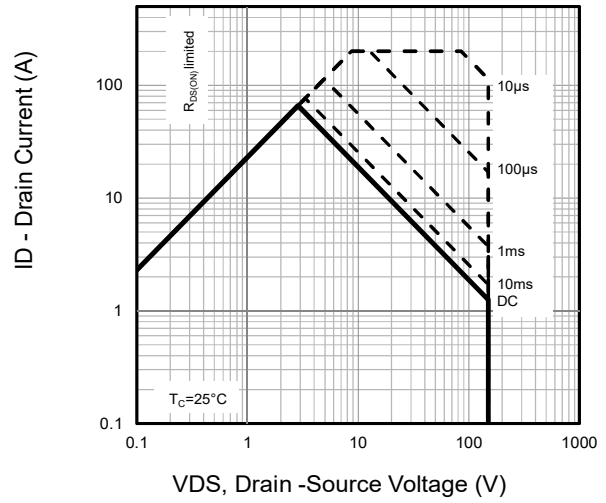


Fig8. Maximum Safe Operating Area

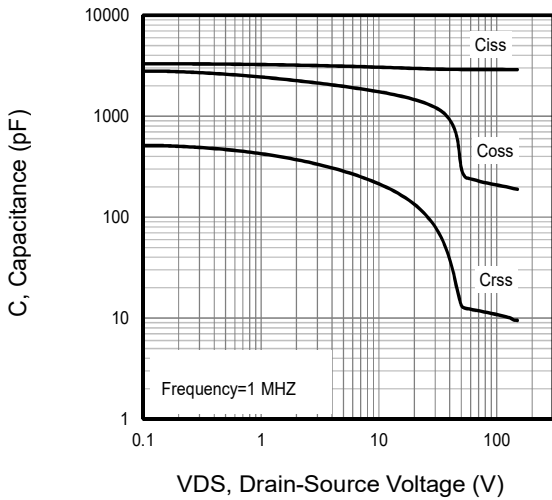


Fig9. Typical Capacitance Vs. Drain-Source Voltage

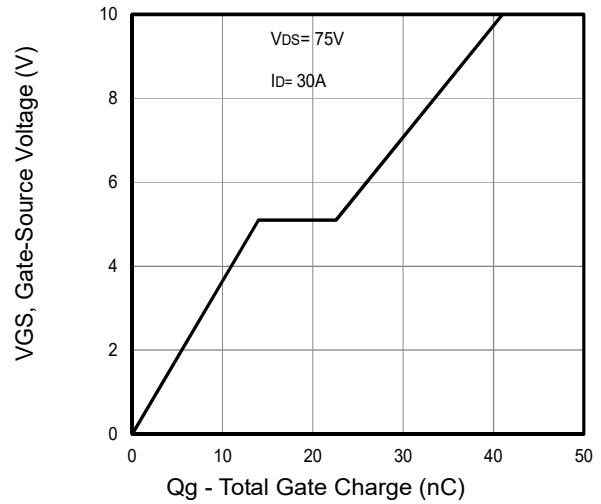


Fig10. Typical Gate Charge Vs. Gate-Source Voltage

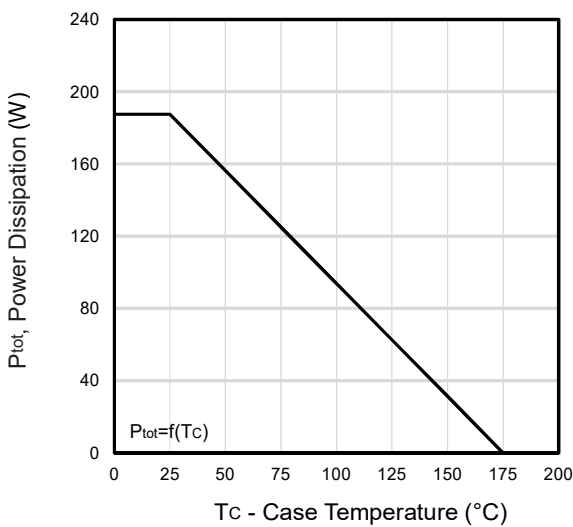


Fig11. Power Dissipation Vs. Case Temperature

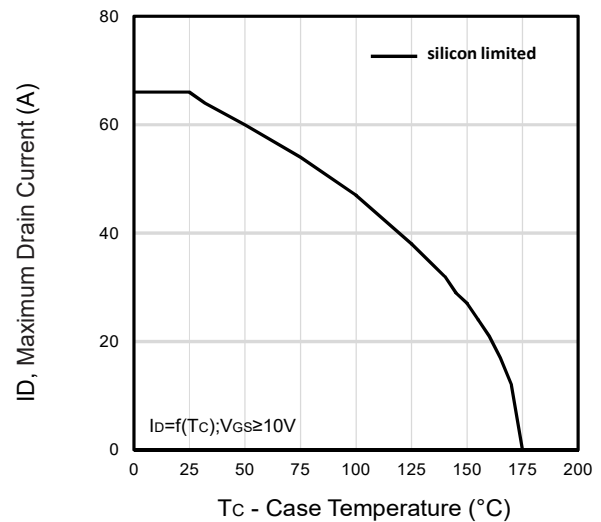


Fig12. Maximum Drain Current Vs. Case Temperature

Typical Characteristics

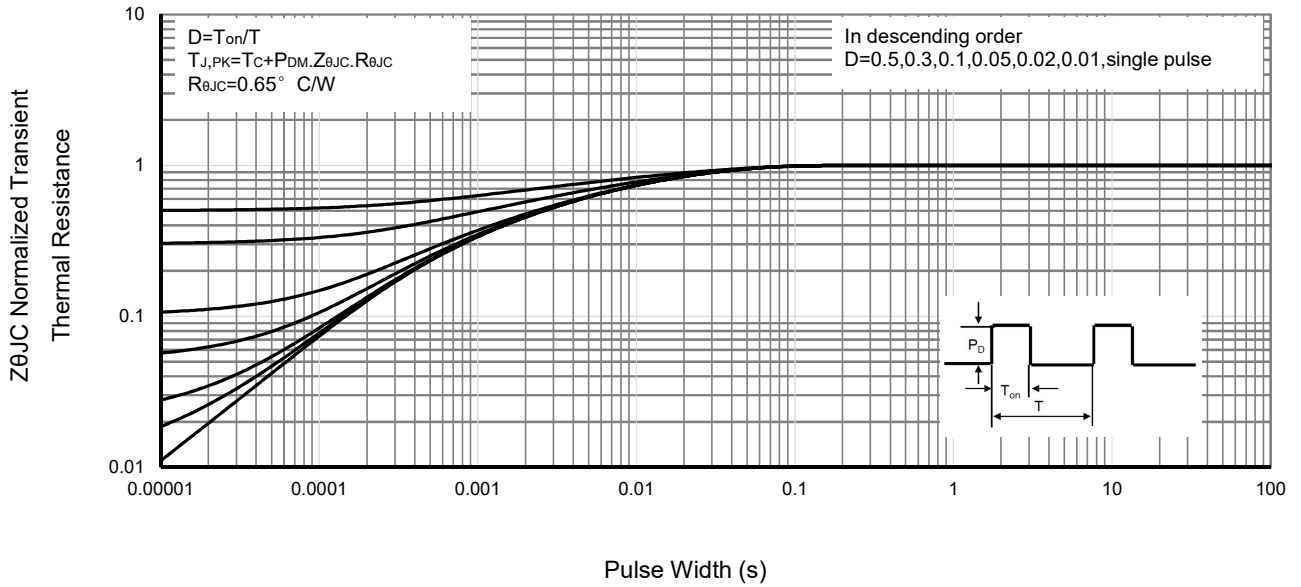


Fig13 . Normalized Maximum Transient Thermal Impedance

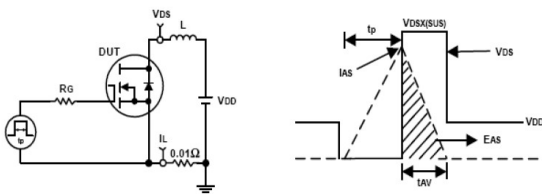


Fig14. Unclamped Inductive Test Circuit and waveforms

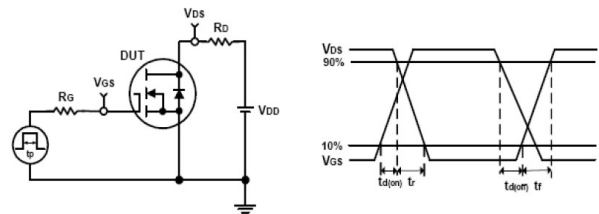
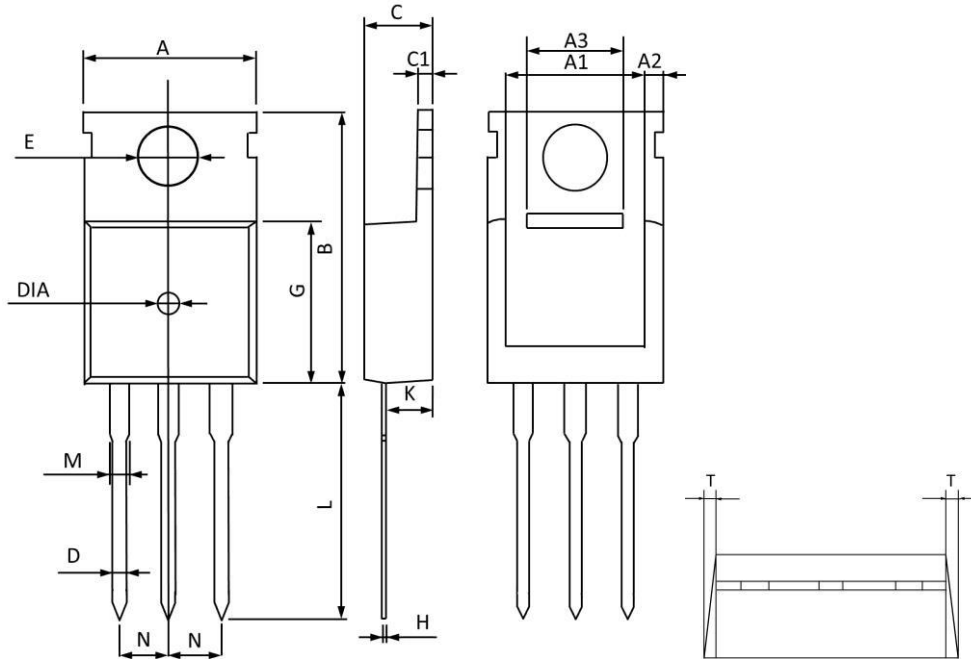


Fig15. Switching Time Test Circuit and waveforms

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.300	9.700	0.406	0.382
A1	8.840	8.440	0.348	0.332
A2	1.250	1.050	0.049	0.041
A3	5.300	5.100	0.209	0.201
B	16.200	15.400	0.638	0.606
C	4.680	4.280	0.184	0.169
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	3.800	3.400	0.150	0.134
G	9.300	8.700	0.366	0.343
H	0.600	0.400	0.024	0.016
K	2.700	2.100	0.106	0.083
L	13.600	12.800	0.535	0.504
M	1.500	1.100	0.059	0.043
N	2.590	2.490	0.102	0.098
T	W0.35		W0.014	
DIA	Φ1.5 TYP.	deep0.2 TYP.	Φ0.059 TYP.	deep0.008 TYP.


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