

● General Description

The AGM408MN combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

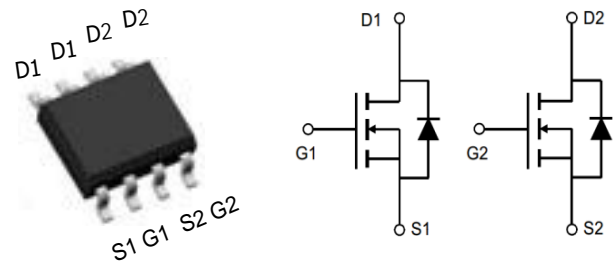
● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	R _{DS(ON)}	ID
40V	8.5mΩ	11A

SOP-8 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM408MN	AGM408MN	SOP8	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Ta=25°C) (Note 1)	11	A
	Drain Current-Continuous(Ta=100°C)	5	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	44	A
PD	Maximum Power Dissipation(Ta=25°C)	2.5	w
	Maximum Power Dissipation(Ta=100°C)	1.0	w
EAS	Avalanche energy (Note 3)	99	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	50	°C/W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=40V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	1.7	2.3	V
gFS	Forward Transconductance	VDS=5V,ID=10A	--	5.2	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=15A	--	8.5	11	mΩ
		VGS=4.5V, ID=10A	--	16	20	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=20V,VGS=0V, F=1MHZ	--	870	--	pF
Coss	Output Capacitance		--	265	--	pF
Crss	Reverse Transfer Capacitance		--	20	--	pF
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=20V, ID=10A,RGEN=6Ω	--	5.6	--	nS
tr	Turn-on Rise Time		--	29	--	nS
td(off)	Turn-Off Delay Time		--	15	--	nS
tf	Turn-Off Fall Time		--	4.6	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=20V, ID=10A	--	13	--	nC
Qgs	Gate-Source Charge		--	2.7	--	nC
Qgd	Gate-Drain Charge		--	1.6	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	11	A
VSD	Forward on Voltage	VGS=0V,IS=15A	--	0.8	1.2	V
trr	Reverse Recovery Time	IF=15A , dI/dt=100A/μs , TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Typical Characteristics

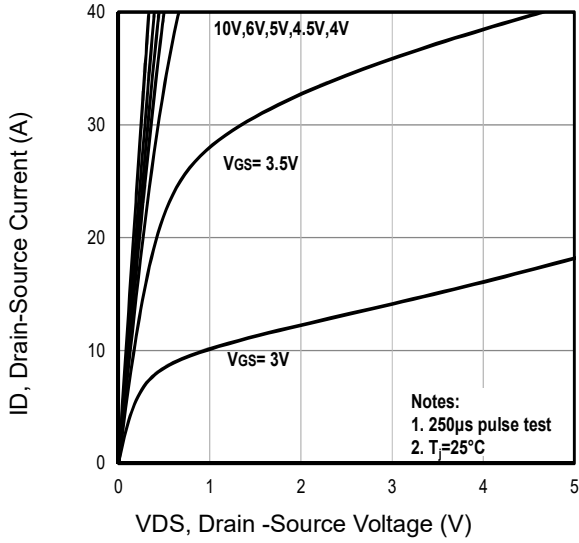


Fig1. Typical Output Characteristics

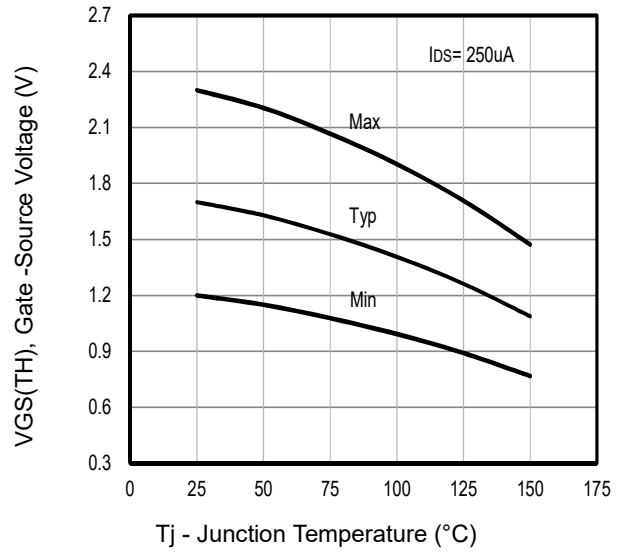


Fig2. Typical $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

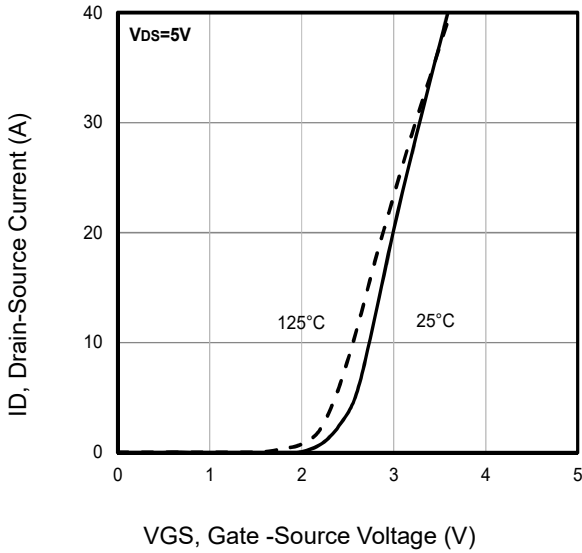


Fig3. Typical Transfer Characteristics

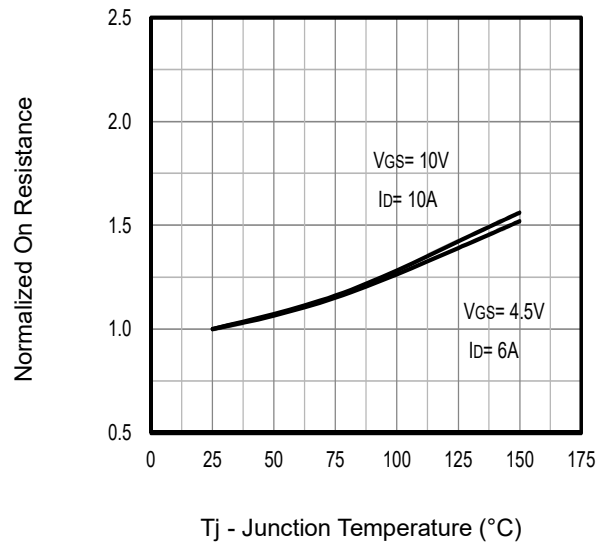


Fig4. Typical Normalized On-Resistance Vs. T_j

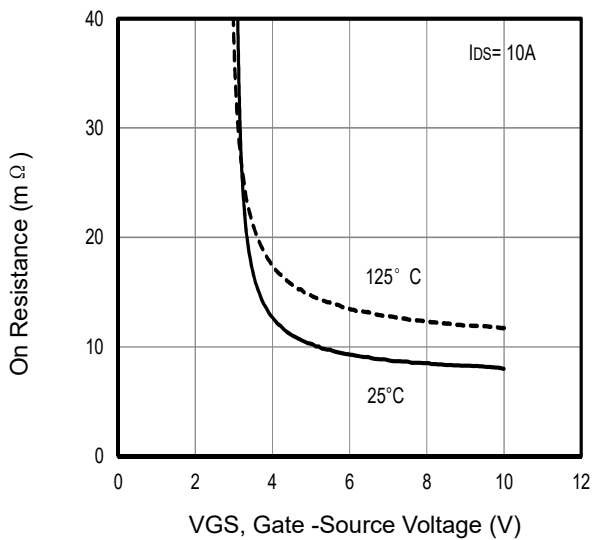


Fig5. Typical On Resistance Vs Gate -Source Voltage

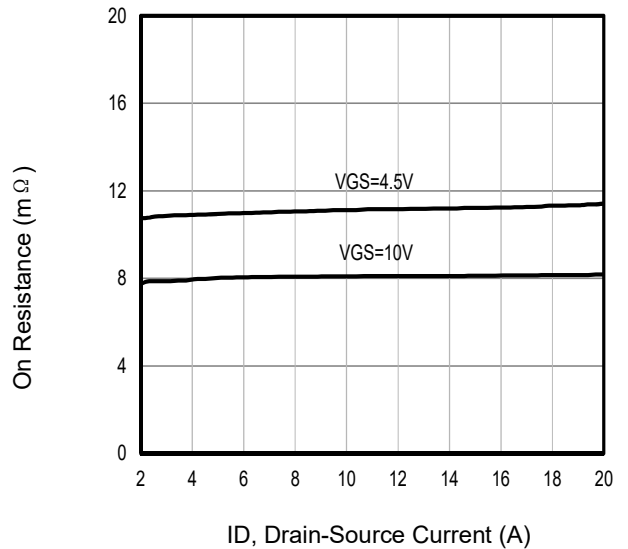


Fig6. Typical On Resistance Vs Drain Current

Typical Characteristics

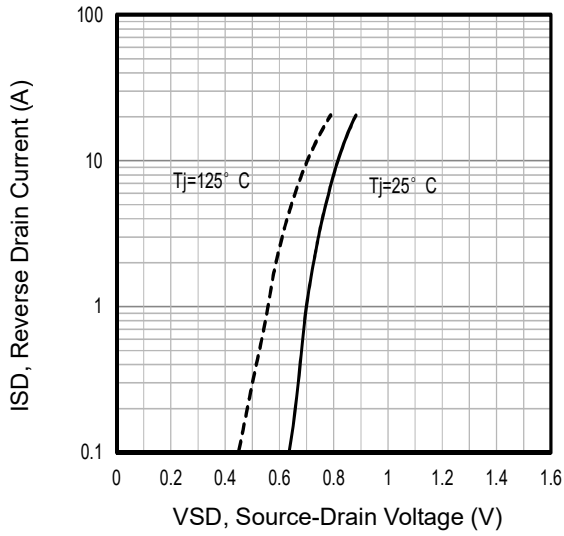


Fig7. Typical Source-Drain Diode Forward Voltage

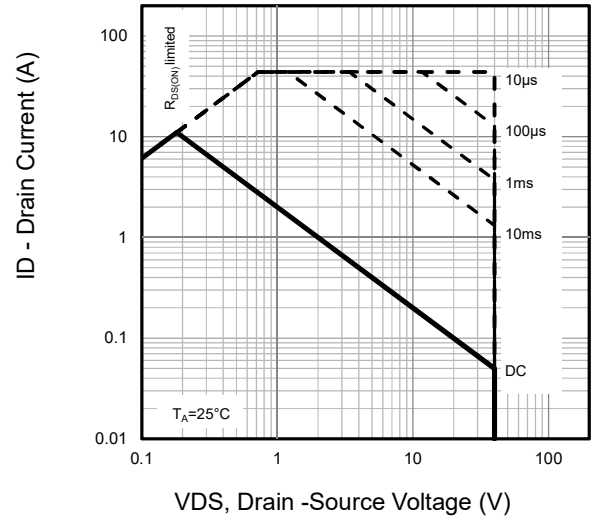


Fig8. Maximum Safe Operating Area

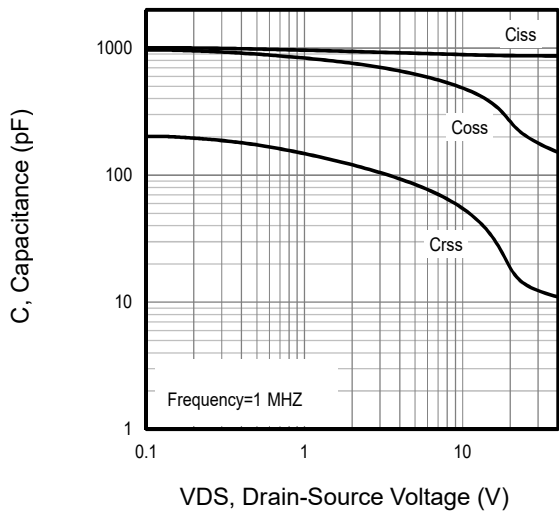


Fig9. Typical Capacitance Vs. Drain-Source Voltage

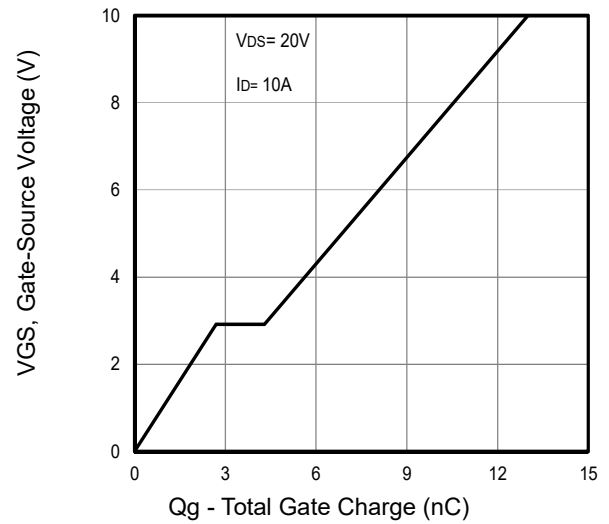


Fig10. Typical Gate Charge Vs. Gate-Source Voltage

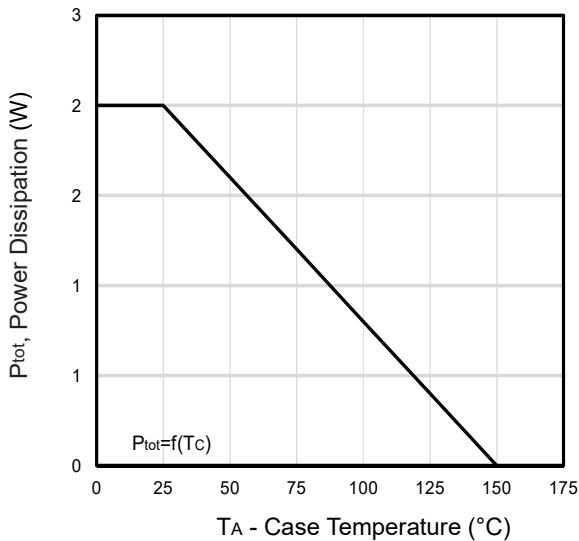


Fig11. Power Dissipation Vs. Case Temperature

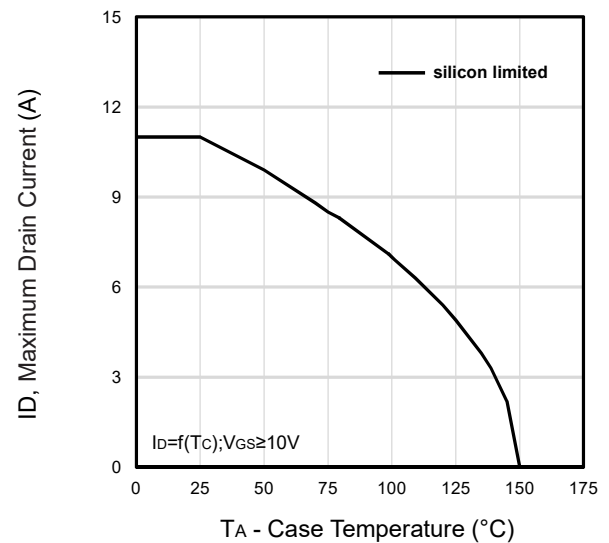


Fig12. Maximum Drain Current Vs. Case Temperature

Typical Characteristics

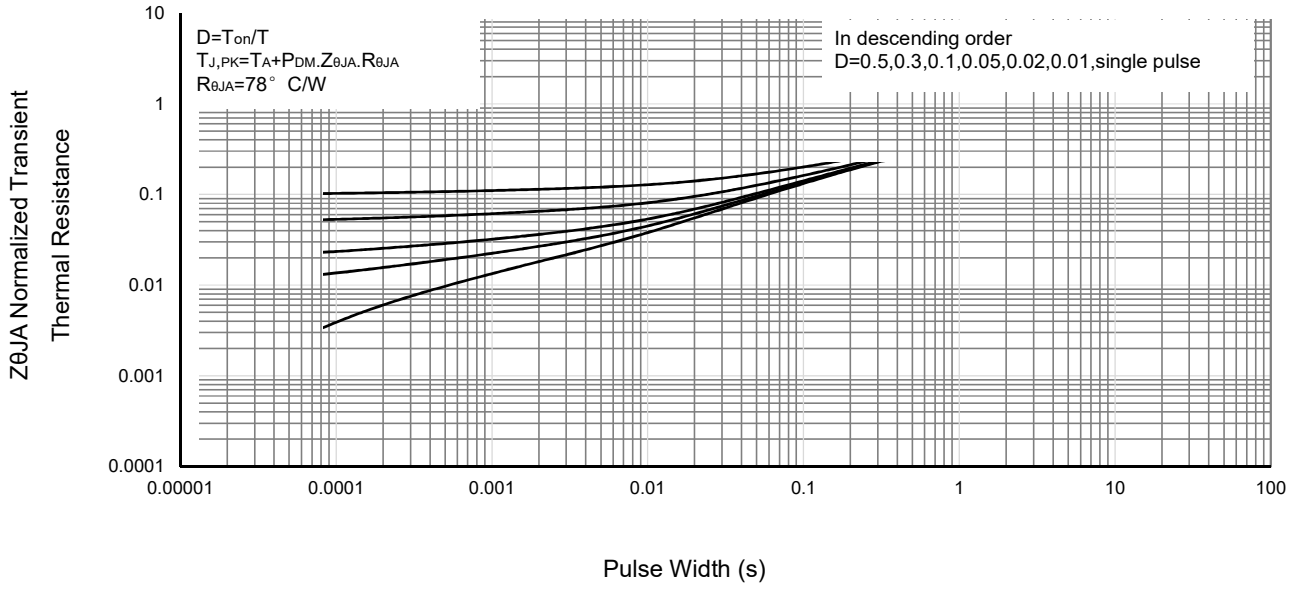
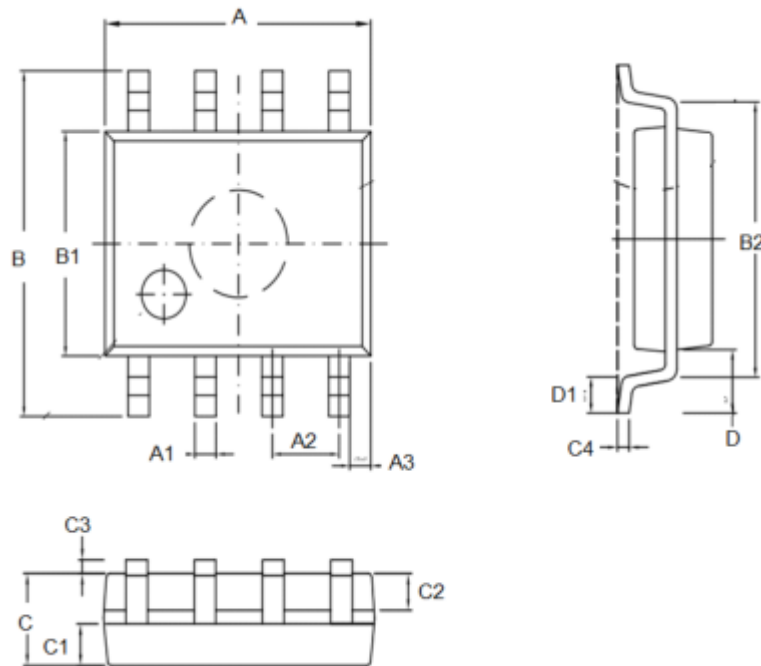


Fig13 . Normalized Maximum Transient Thermal Impedance

•Dimensions(SOP8)

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.80		5.00	C	1.30		1.50
A1	0.37		0.47	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.20
B	5.80		6.20	C4	0.19	0.20	0.23
B1	3.80		4.00	D		1.05	
B2		5.00		D1	0.40		0.62




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