

• General Description

The AGM420MA combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is ideal for load switch and battery protection applications.

• Features

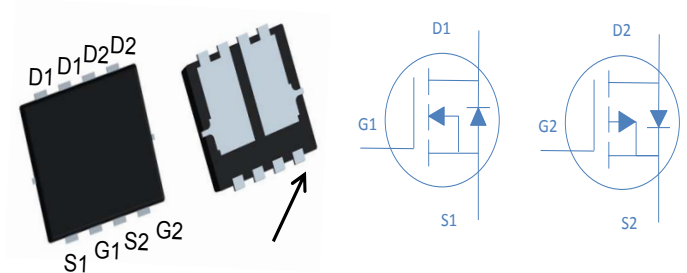
- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
40V	18mΩ	20A
-40V	26mΩ	-18A

PDFN5*6 Pin Configuration

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM420MA	AGM420MA	DFN5*6	325mm	16mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	40	-40	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	±20	±20	V
I_D	Drain Current-Continuous($T_c=25^\circ C$) (Note 1)	18	-20	A
	Drain Current-Continuous($T_c=100^\circ C$)	15	-16	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	50	-40	A
P_D	Total Power Dissipation($T_c=25^\circ C$)	25	31	W
	Total Power Dissipation($T_A=100^\circ C$)	4	5	W
EAS	Avalanche energy (Note 3)	31	58	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) ¹	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	5	°C/W

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_j = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(T _j =25°C)	V _{DS} =40V, V _{GS} =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T _j =125°C)	V _{DS} =40V, V _{GS} =0V	--	--	100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.7	2.5	V
R _{DS(ON)}	Drain-Source On-State Resistance ④	V _{GS} =10V, I _D =25A	--	18	23	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance ④	V _{GS} =4.5V, I _D =20A	--	25	36	mΩ
Dynamic Electrical Characteristics @ T_j= 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	970	1130	1230	pF
C _{oss}	Output Capacitance		95	100	120	pF
C _{rss}	Reverse Transfer Capacitance		80	90	105	pF
R _g	Gate Resistance	f=1MHz	--	2.2	--	Ω
Q _g	Total Gate Charge	V _{DS} =20V, I _D =20A, V _{GS} =10V	--	20.5	--	nC
Q _{gs}	Gate-Source Charge		--	4.9	--	nC
Q _{gd}	Gate-Drain Charge		--	4.1	--	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =20V, I _D =20A, R _G =3Ω, V _{GS} =10V				
t _r	Turn-on Rise Time		--	44.5	--	ns
t _{d(off)}	Turn-Off Delay Time		--	19	--	ns
t _f	Turn-Off Fall Time		--	9.2	--	ns
Source- Drain Diode Characteristics @ T_j = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	I _{SD} =25A, V _{GS} =0V	--	0.9	1.2	V
t _{rr}	Reverse Recovery Time	T _j =25°C, I _{SD} =20A, V _{GS} =0V di/dt=100A/μs	--	6.8	--	ns
Q _{rr}	Reverse Recovery Charge		--	1.6	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max junction temperature.
- ② Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH, R_G = 25Ω, I_{AS} = 6A, V_{GS} = 10V. Part not recommended for use above this value
- ③ The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C.
- ④ Pulse width ≤ 300μs; duty cycle ≤ 2%.

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_j = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-40	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-40V, V _{GS} =0V	--	--	-1	μA
	Zero Gate Voltage Drain Current(T _j =125°C)	V _{DS} =-40V, V _{GS} =0V	--	--	-100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.0	-1.7	-2.5	V
R _{DS(ON)}	Drain-Source On-State Resistance ②	V _{GS} =-10V, I _D =-15A	--	26	34	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance ②	V _{GS} =-4.5V, I _D =-5A	--	34	46	mΩ
Dynamic Electrical Characteristics @ T_j = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =-30V, V _{GS} =0V, f=1MHz	--	1112	--	pF
C _{OSS}	Output Capacitance		--	135	--	pF
C _{rss}	Reverse Transfer Capacitance		--	95	--	pF
Q _g	Total Gate Charge	V _{DS} =-20V, I _D =-10A, V _{GS} =-10V	--	27	--	nC
Q _{gs}	Gate-Source Charge		--	7.3	--	nC
Q _{gd}	Gate-Drain Charge		--	5.6	--	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =-20V, I _D =-10A, R _G =6.8Ω, V _{GS} =-10V	--	13	--	nS
t _r	Turn-on Rise Time		--	18	--	nS
t _{d(off)}	Turn-Off Delay Time		--	36	--	nS
t _f	Turn-Off Fall Time		--	25	--	nS
Source- Drain Diode Characteristics @ T_j = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	I _{SD} =-15A, V _{GS} =0V	--	-0.89	-1.2	V
t _{rr}	Reverse Recovery Time	T _j =25°C, I _{sd} =-10A, V _{GS} =0V	--	34	--	nS
Q _{rr}	Reverse Recovery Charge	di/dt=-100A/μs		30		nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width ≤ 300μs; duty cycle ≤ 2%.

③ Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH, R_G = 25Ω, I_{AS} = -34A, V_{GS} = -10V. Part not recommended for use above this value

P-Channel Typical Characteristics

Typical Characteristics

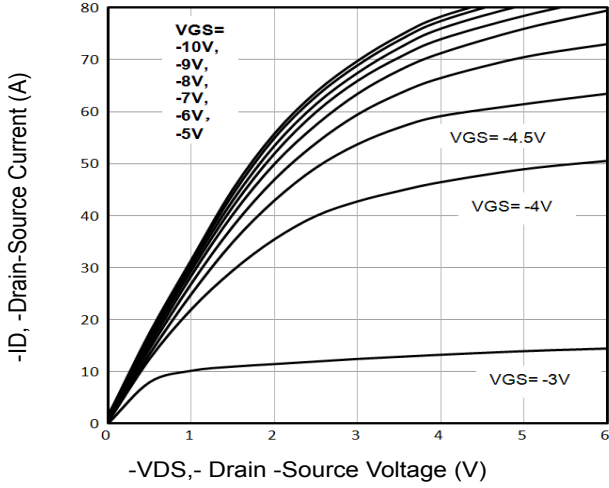


Fig1. Typical Output Characteristics

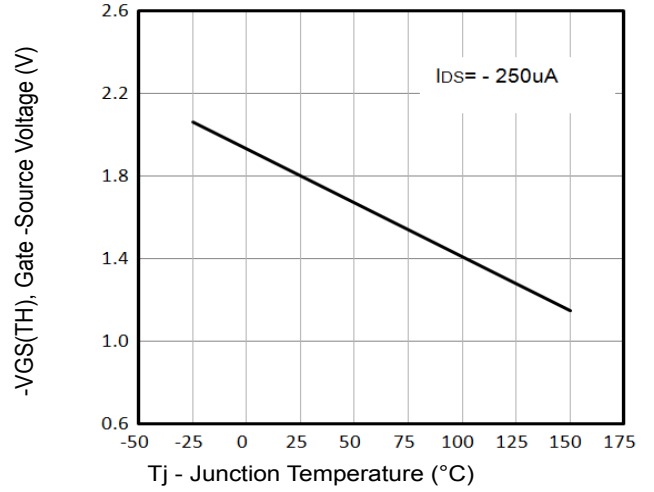


Fig2. $-V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

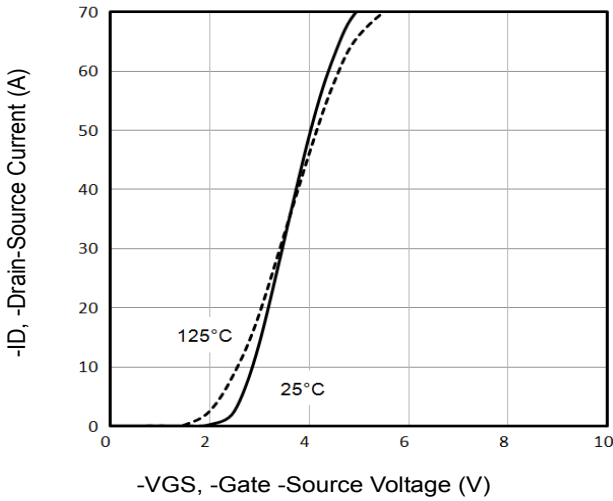


Fig3. Typical Transfer Characteristics

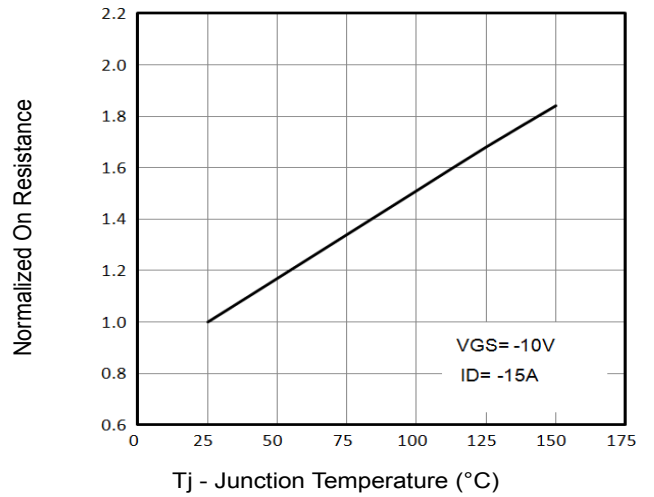


Fig4. Normalized On-Resistance Vs. T_j

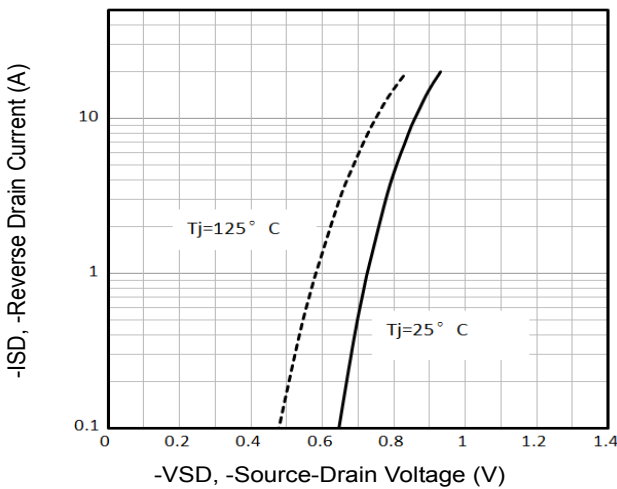


Fig5. Typical Source-Drain Diode Forward Voltage

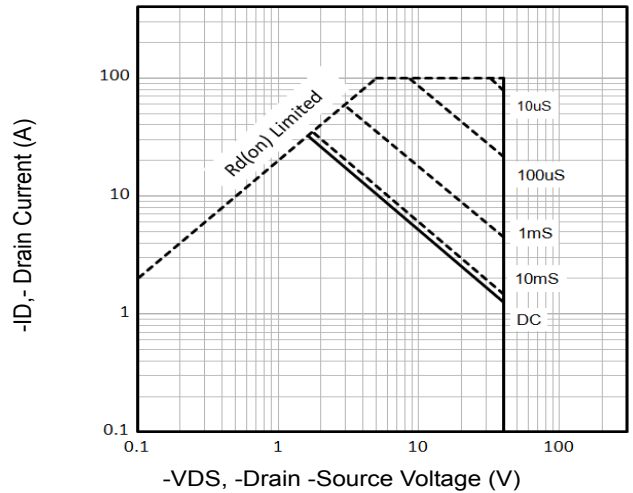


Fig6. Maximum Safe Operating Area

Typical Characteristics

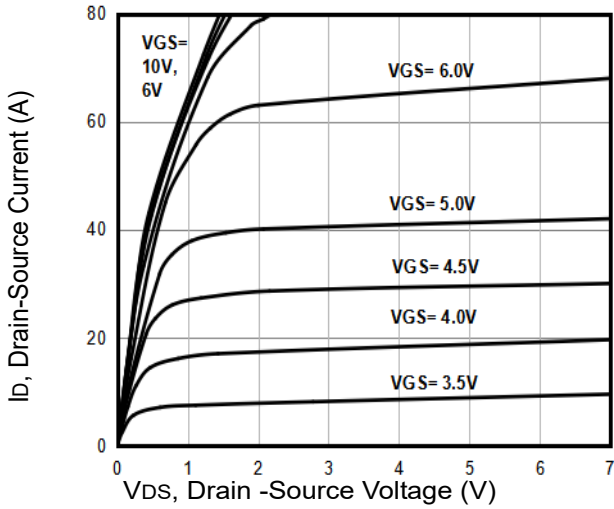


Fig1. Typical Output Characteristics

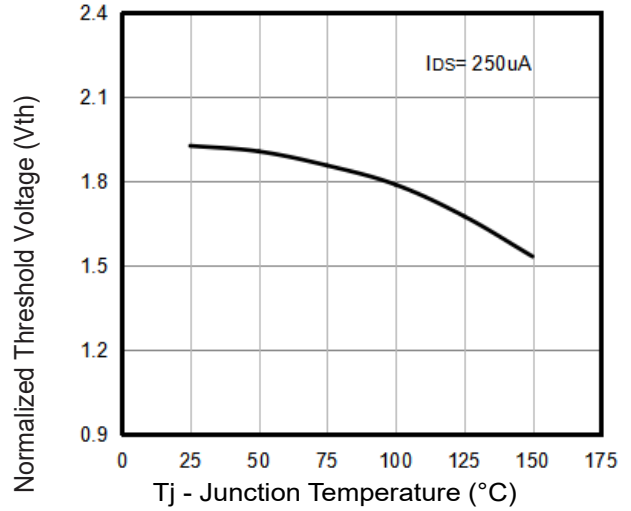


Fig2. Normalized Threshold Voltage Vs. Temperature

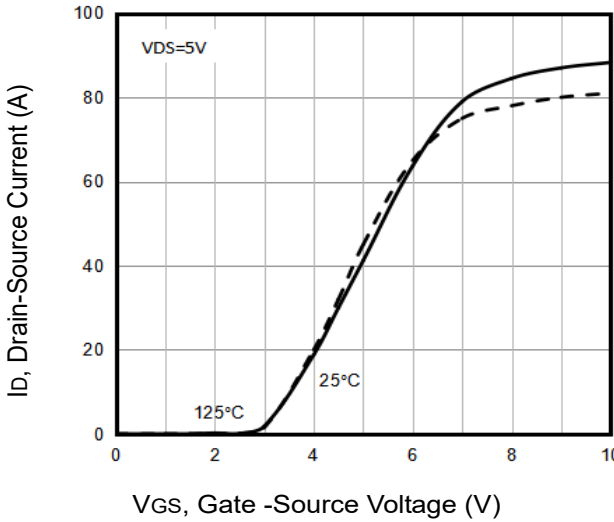


Fig3. Typical Transfer Characteristics

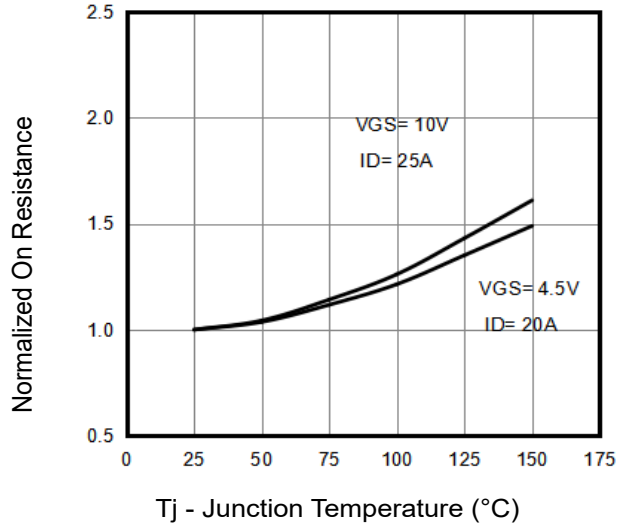


Fig4. Normalized On-Resistance Vs. Temperature

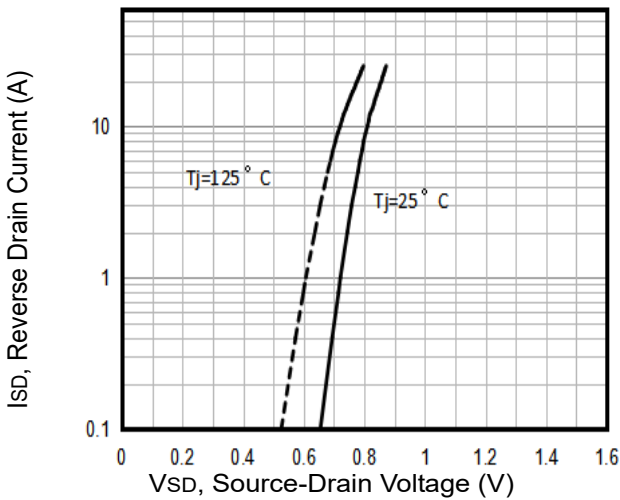


Fig5. Typical Source-Drain Diode Forward Voltage

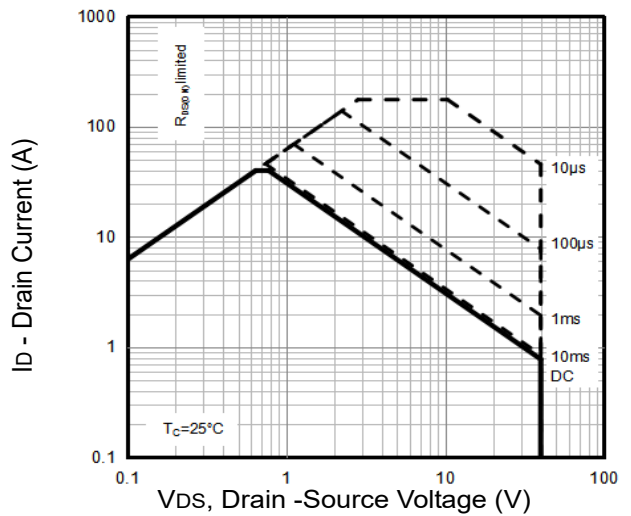


Fig6. Maximum Safe Operating Area

N-Channel Typical Characteristics

Typical Characteristics

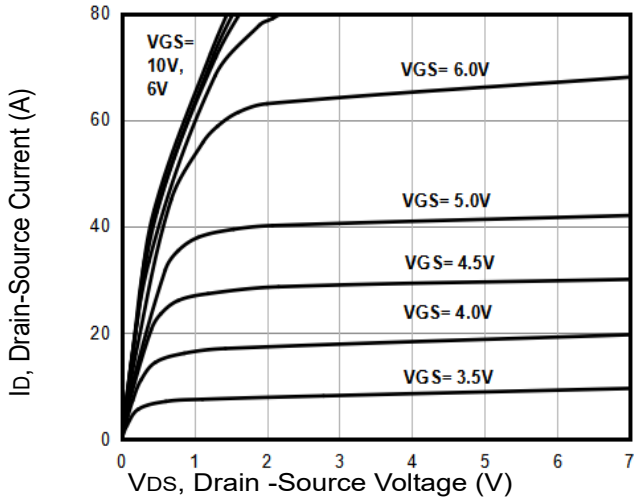


Fig1. Typical Output Characteristics

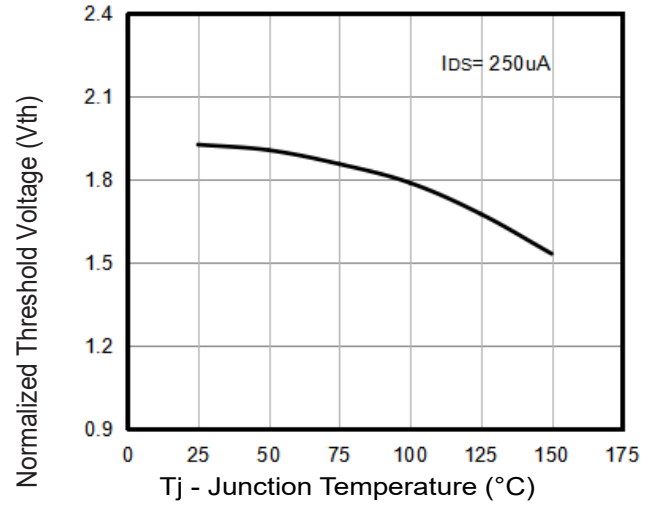


Fig2. Normalized Threshold Voltage Vs. Temperature

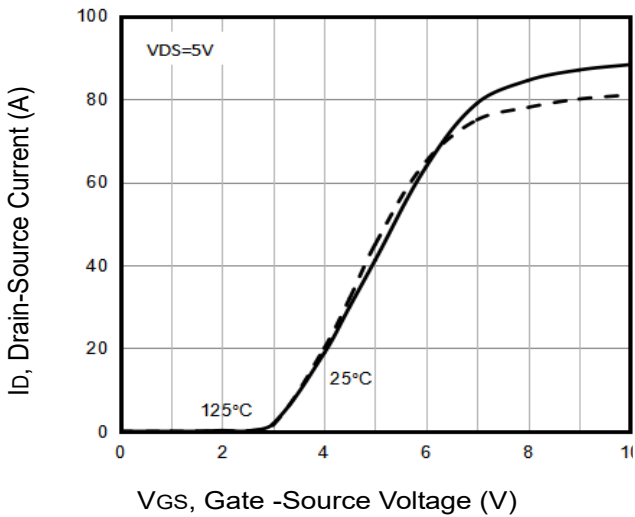


Fig3. Typical Transfer Characteristics

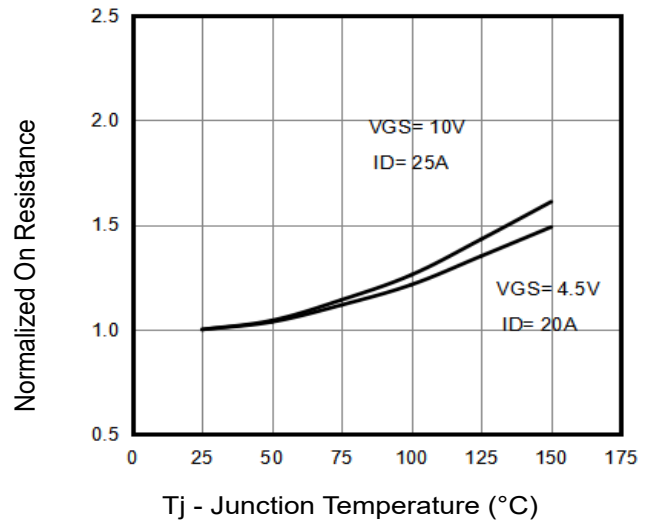


Fig4. Normalized On-Resistance Vs. Temperature

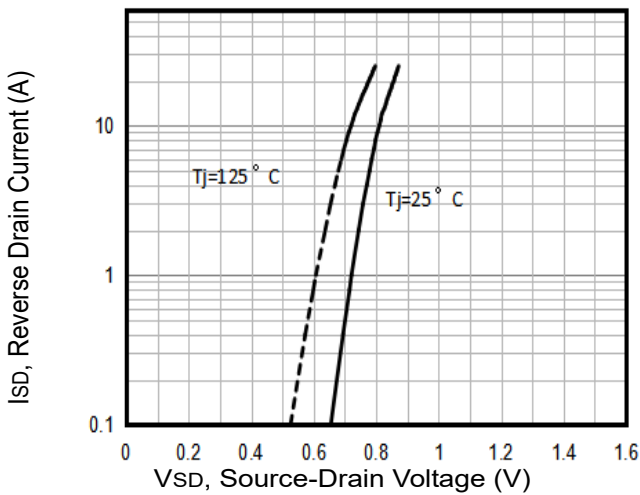


Fig5. Typical Source-Drain Diode Forward Voltage

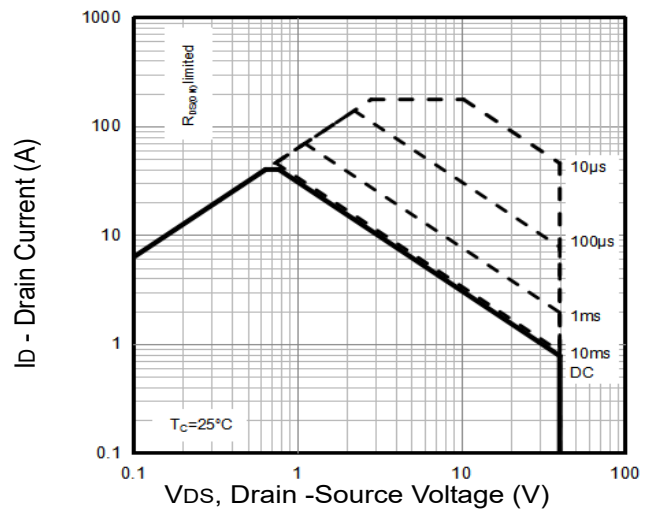


Fig6. Maximum Safe Operating Area

Typical Characteristics

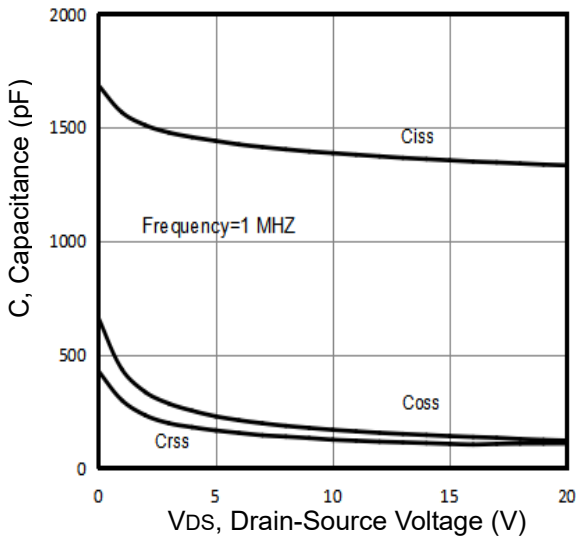


Fig7. Typical Capacitance Vs. Drain-Source Voltage

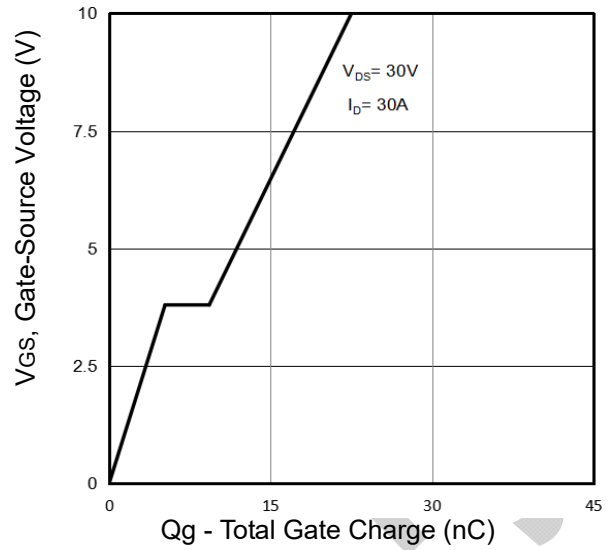


Fig8. Typical Gate Charge Vs. Gate-Source

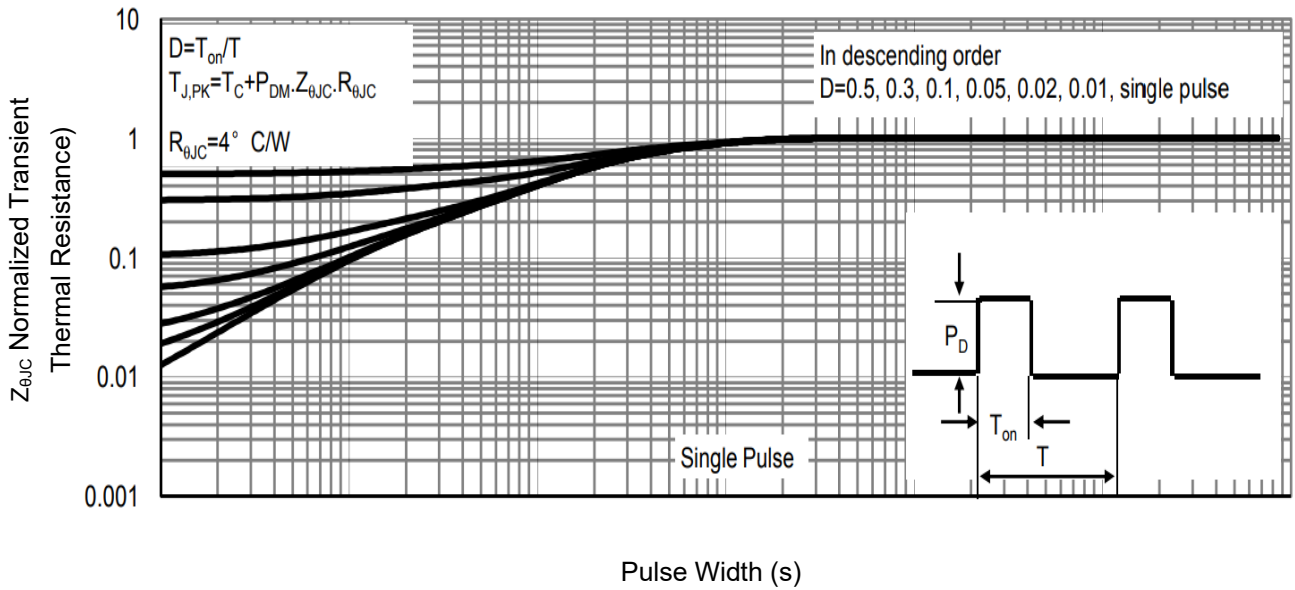


Fig9. Normalized Maximum Transient Thermal Impedance

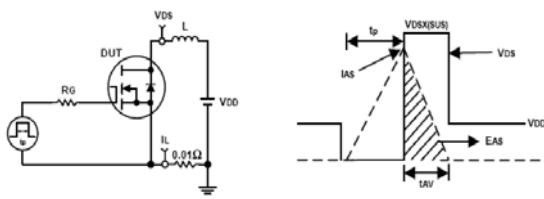


Fig10. Unclamped Inductive Test Circuit and waveforms

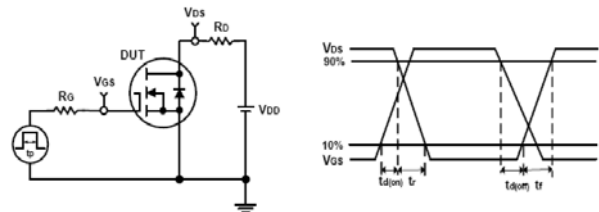
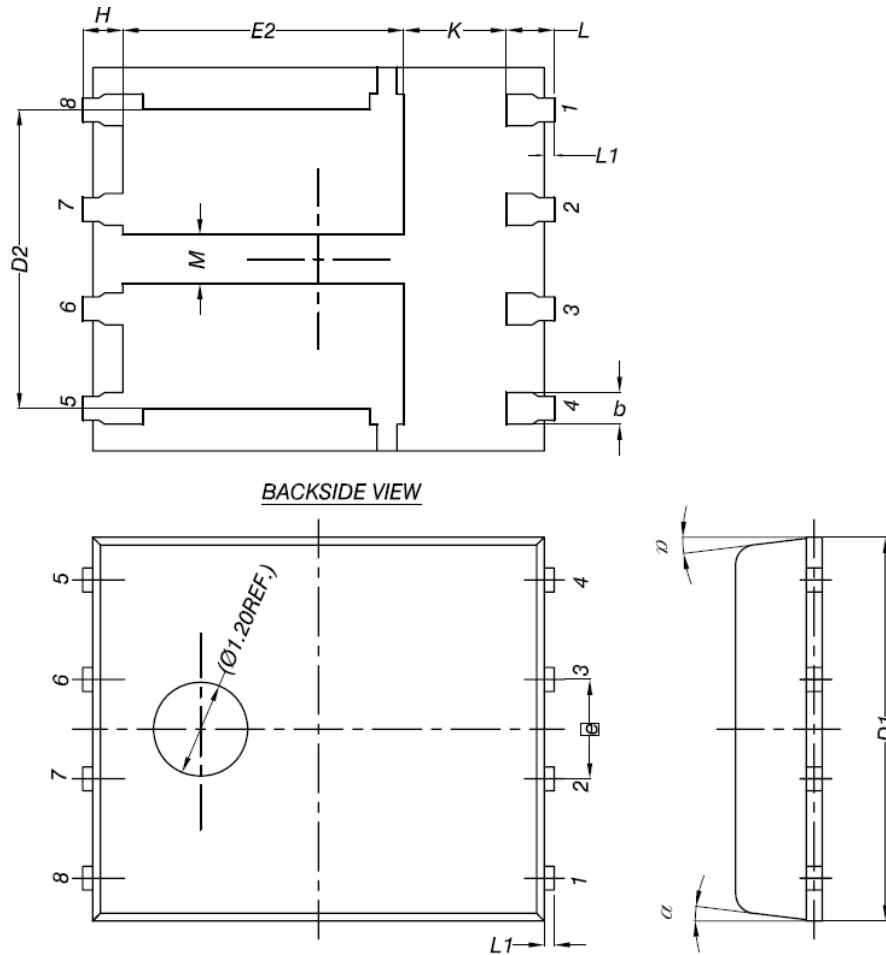


Fig11. Switching Time Test Circuit and waveforms

PDFN5x6 Package Outline Data

DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.00	1.10	e	1.27 BSC		
b	0.33	0.41	0.51	H	0.41	0.51	0.61
C	0.20	0.25	0.30	K	1.10	--	--
D1	4.80	4.90	5.00	L	0.51	0.61	0.71
D2	3.61	3.81	3.96	L1	0.06	0.13	0.20
E	5.90	6.00	6.10	M	0.50	--	--
E1	5.70	5.75	5.80	α	0°	--	12°
E2	3.38	3.58	3.78				


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