

• General Description

The AGM425MD combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

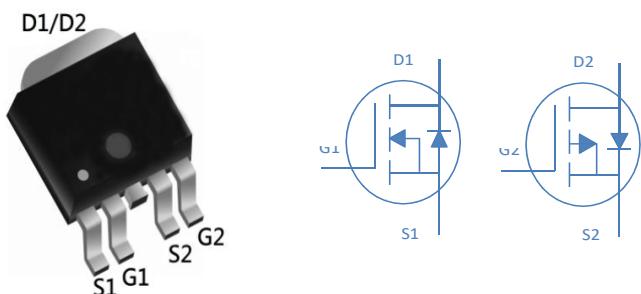
• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
40V	18mΩ	23A
-40V	32mΩ	-20A

TO-254-4 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM425MD	AGM425MD	TO-252-4	--mm	--mm	2500

Table 1. Absolute Maximum Ratings ($T_A=25^\circ C$)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	40	-40	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ C$) ^(Note 1)	23	-20	A
	Drain Current-Continuous($T_c=100^\circ C$)	18	-12.6	A
IDM (pulse)	Drain Current-Continuous@ Current-Pulsed ^(Note 2)	46	-80	A
P_D	Total Power Dissipation($T_c=25^\circ C$)	25	27.8	W
	Total Power Dissipation($T_c=100^\circ C$)	10	11	W
EAS	Avalanche energy ^(Note 3)	28	18	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) ¹	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	5	°C/W

Table 3. N- Channel Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=40V, VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	1.0	1.4	2.0	V
gFS	Forward Transconductance	VDS=5V, ID=10A	--	12	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=15A	--	18	25	mΩ
		VGS=4.5V, ID=10A	--	25	33	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=15V, VGS=0V, F=1MHZ	--	593	--	pF
Coss	Output Capacitance		--	76	--	pF
Crss	Reverse Transfer Capacitance		--	56	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	2.6	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V, VDS=20V, ID=1A, RGEN=3.3Ω	--	8.9	--	nS
tr	Turn-on Rise Time		--	2.2	--	nS
td(off)	Turn-Off Delay Time		--	41	--	nS
tf	Turn-Off Fall Time		--	2.7	--	nS
Qg	Total Gate Charge	VGS=4.5V, VDS=12V, ID=20A	--	5.5	--	nC
Qgs	Gate-Source Charge		--	1.25	--	nC
Qgd	Gate-Drain Charge		--	2.5	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	23	A
VSD	Forward on Voltage	VGS=0V, IS=1A	--	--	1.2	V
trr	Reverse Recovery Time	IF=1A , dI/dt=100A/μs , TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Table 3. P-Channel Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-40V, VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250μA	-1.0	-1.5	-2.5	V
gFS	Forward Transconductance	VDS=-5V, ID=-10A	--	18	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-15A	--	32	45	mΩ
		VGS=-4.5V, ID=-10A	--	41	52	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-20V, VGS=0V, F=1MHZ	--	1080	--	pF
Coss	Output Capacitance		--	87	--	pF
Crss	Reverse Transfer Capacitance		--	77	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	11	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V, VDS=-20V, ID=-6A, RGEN=3Ω	--	5.9	--	ns
tr	Turn-on Rise Time		--	7.1	--	ns
td(off)	Turn-Off Delay Time		--	25	--	ns
tf	Turn-Off Fall Time		--	8.2	--	ns
Qg	Total Gate Charge	VGS=-10V, VDS=-20V, ID=-6A	--	17	--	nC
Qgs	Gate-Source Charge		--	4.2	--	nC
Qgd	Gate-Drain Charge		--	3.7	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-20	A
VSD	Forward on Voltage	VGS=0V, IS=-6A	--	--	-1.2	V
trr	Reverse Recovery Time	IF=-6A, dl/dt=100A/μs, TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulsewidth limited by maximum junction temperature Notes

3.EAS condition: TJ=25°C

N-Channel Typical Characteristics

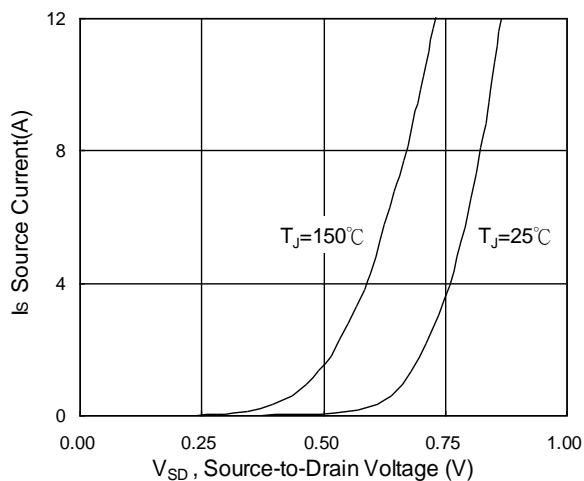
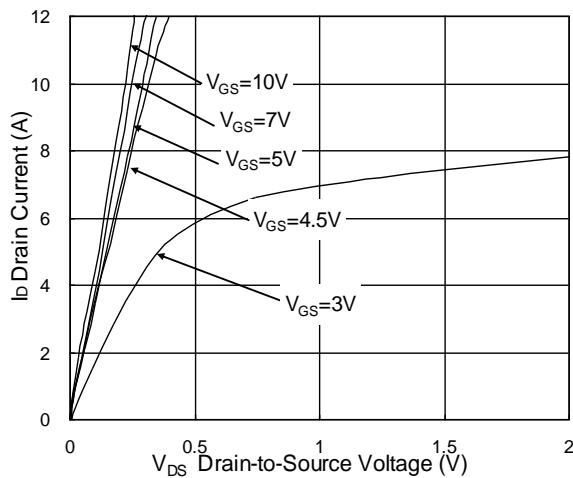


Fig.3 Forward Characteristics of Reverse

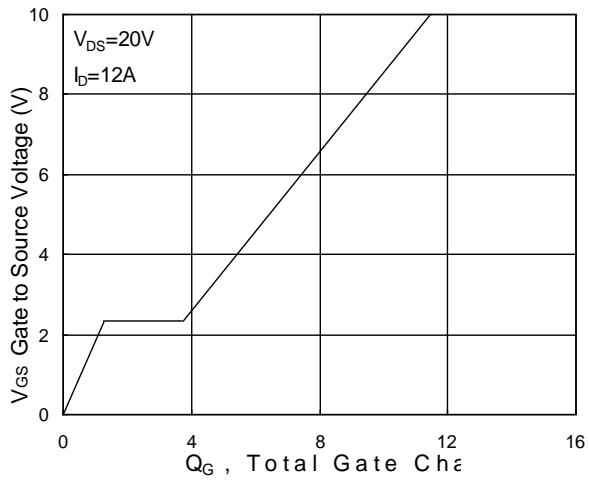
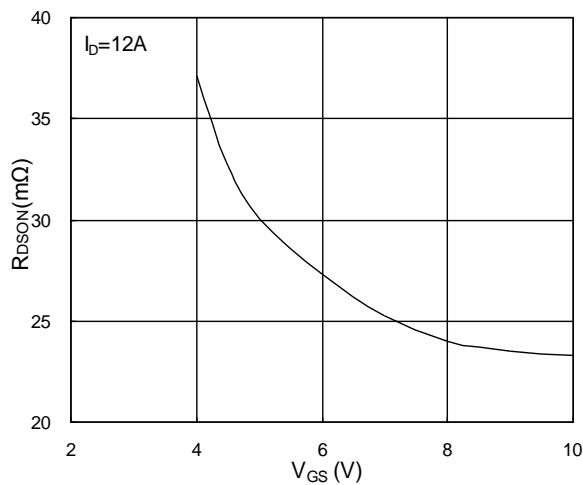


Fig.4 Gate-Charge Characteristics

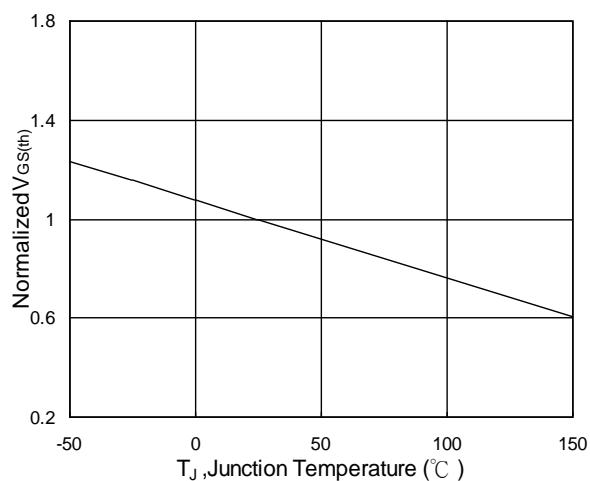


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

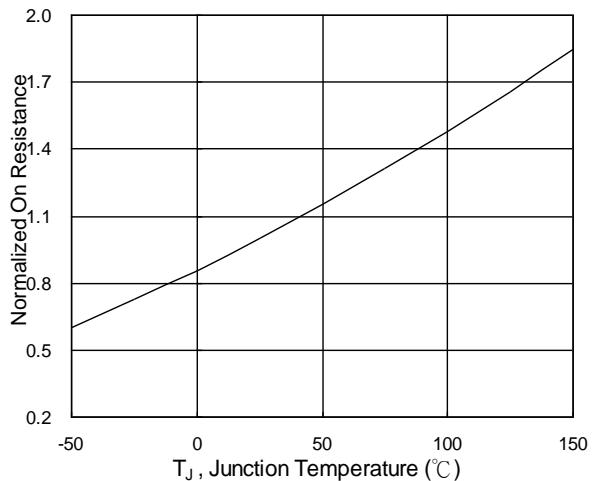
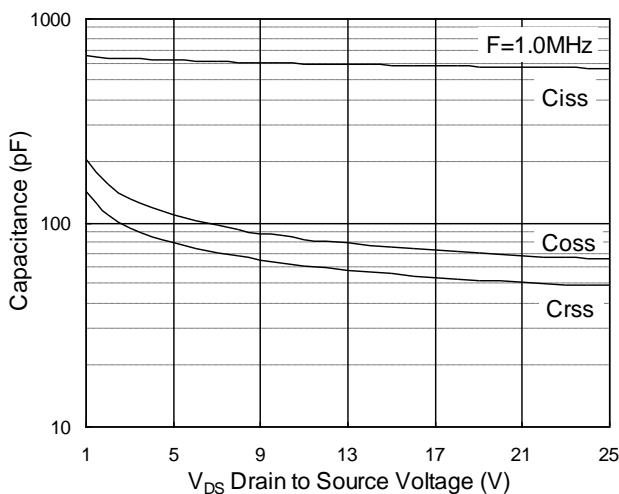
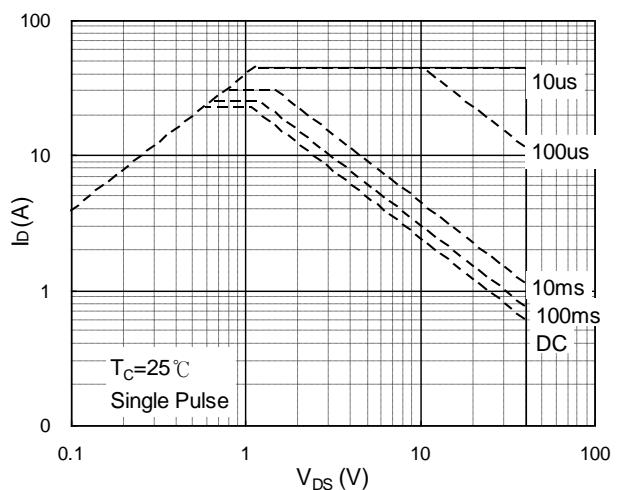
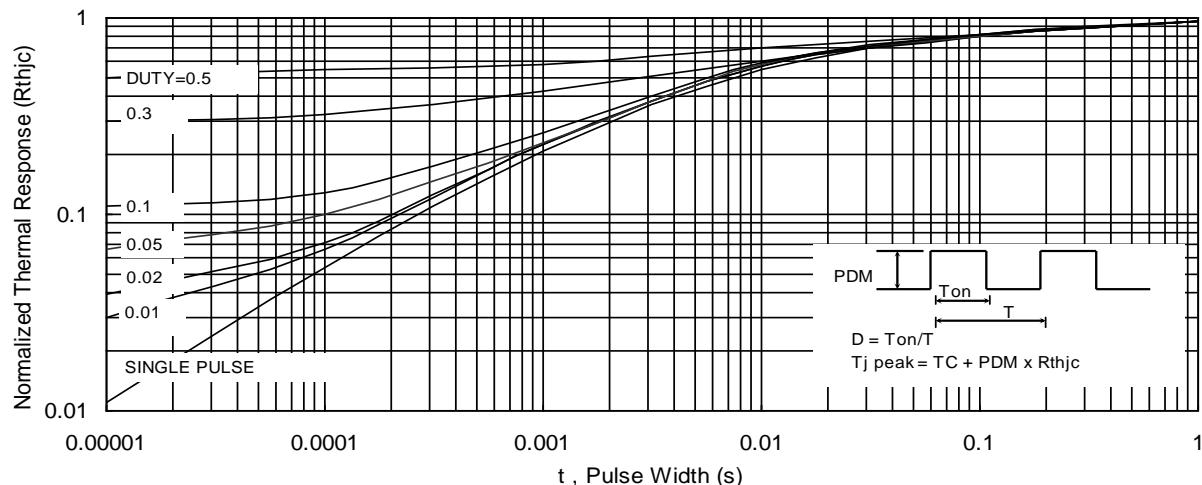
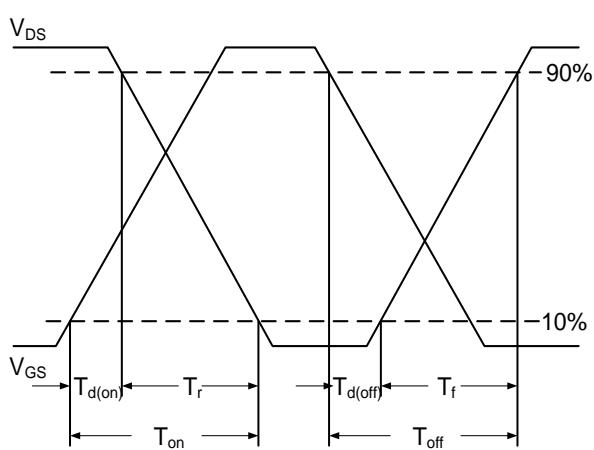
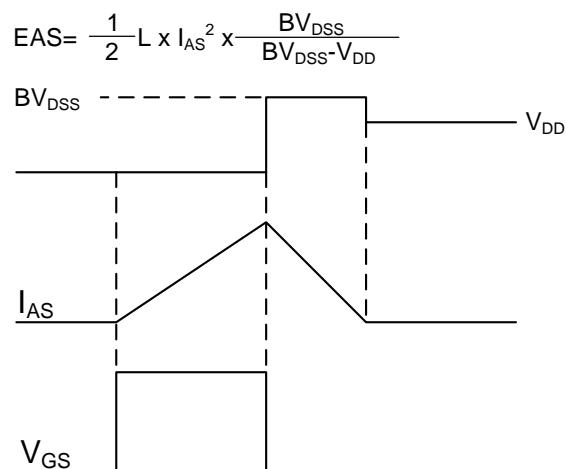
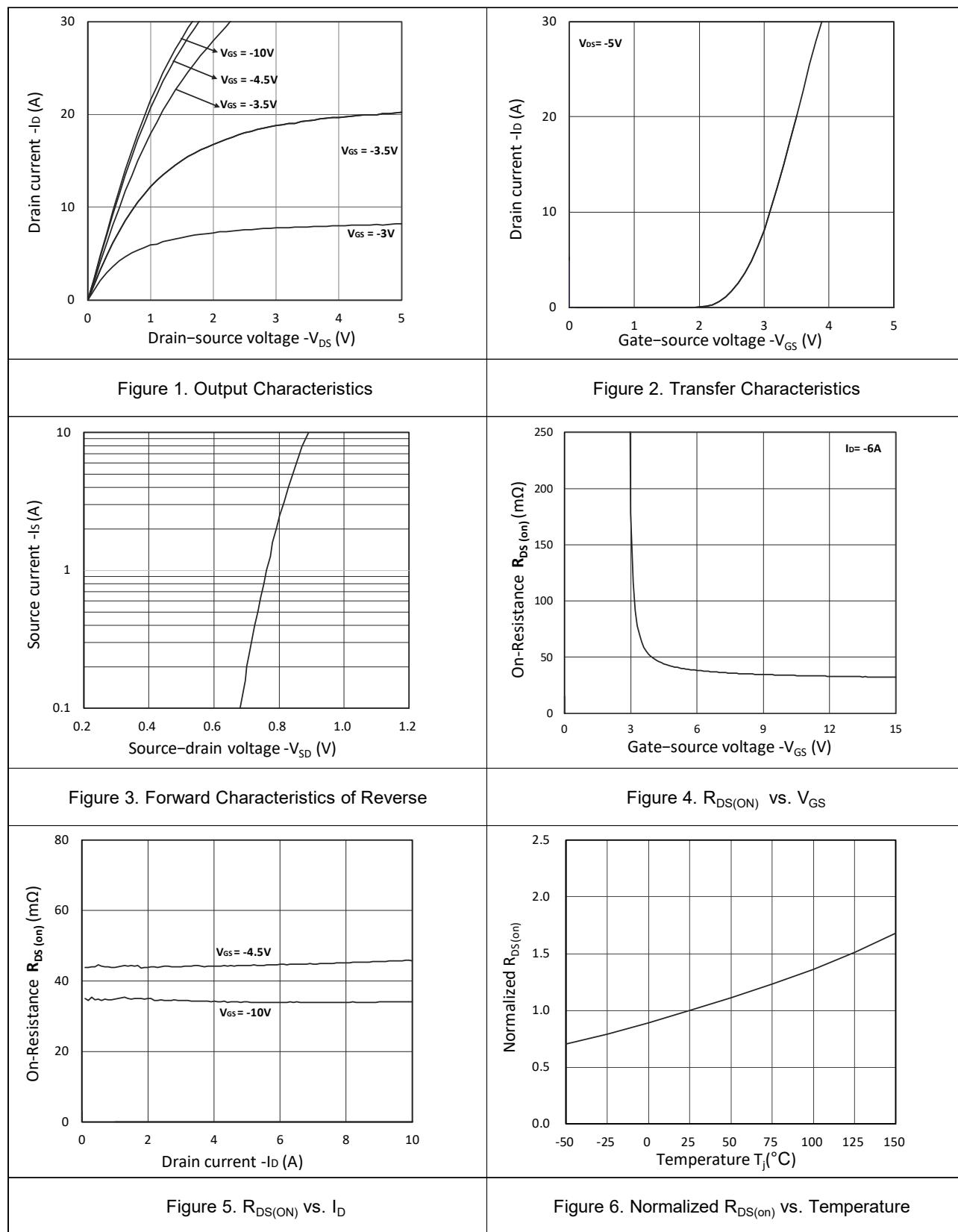
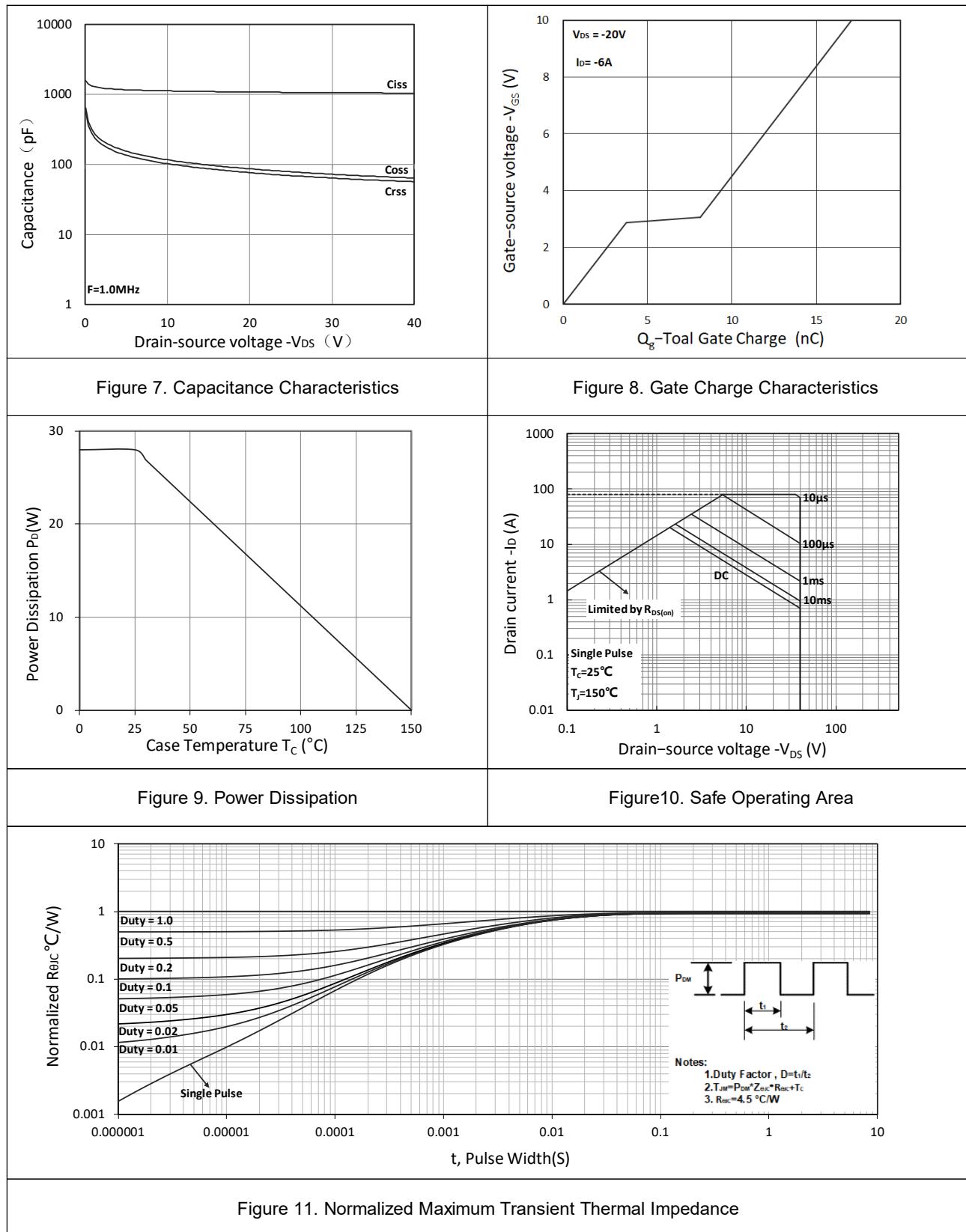


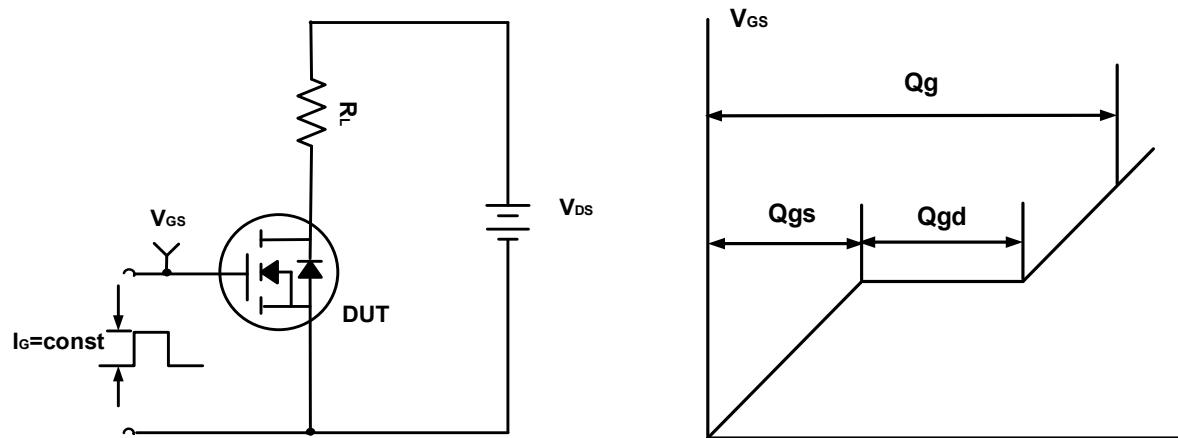
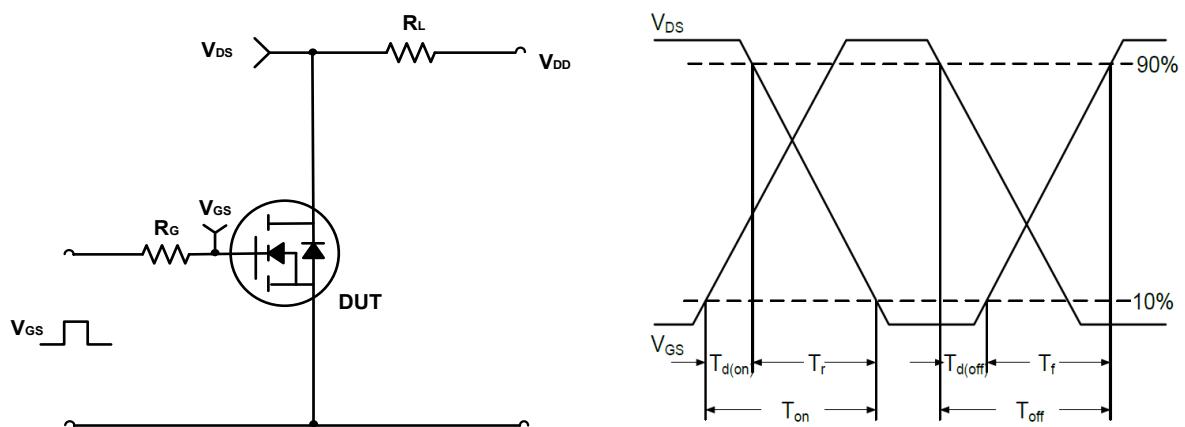
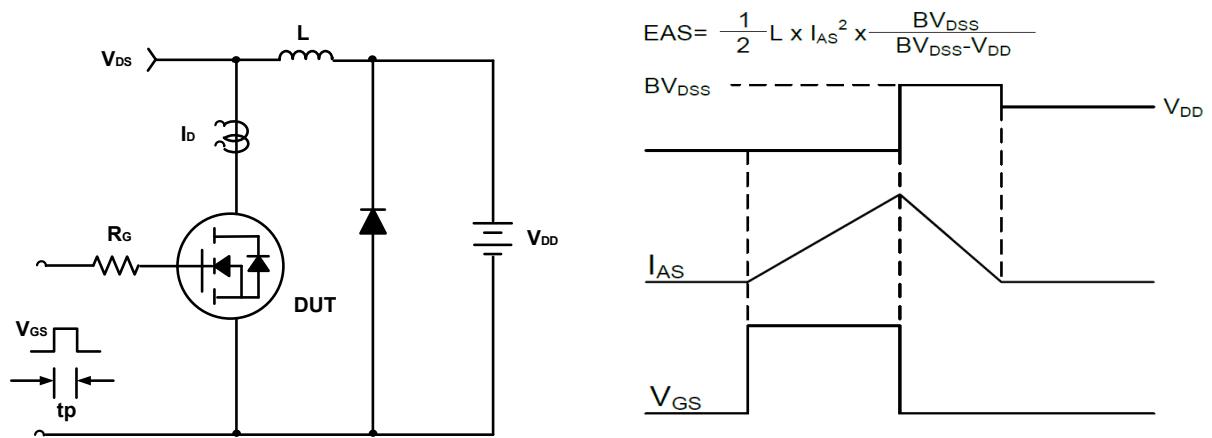
Fig.6 Normalized R_{DSON} vs. T_J

**Fig.7 Capacitance****Fig.8 Safe Operating Area****Fig.9 Normalized Maximum Transient Thermal Impedance****Fig.10 Switching Time Waveform****Fig.11 Unclamped Inductive Switching Wave**

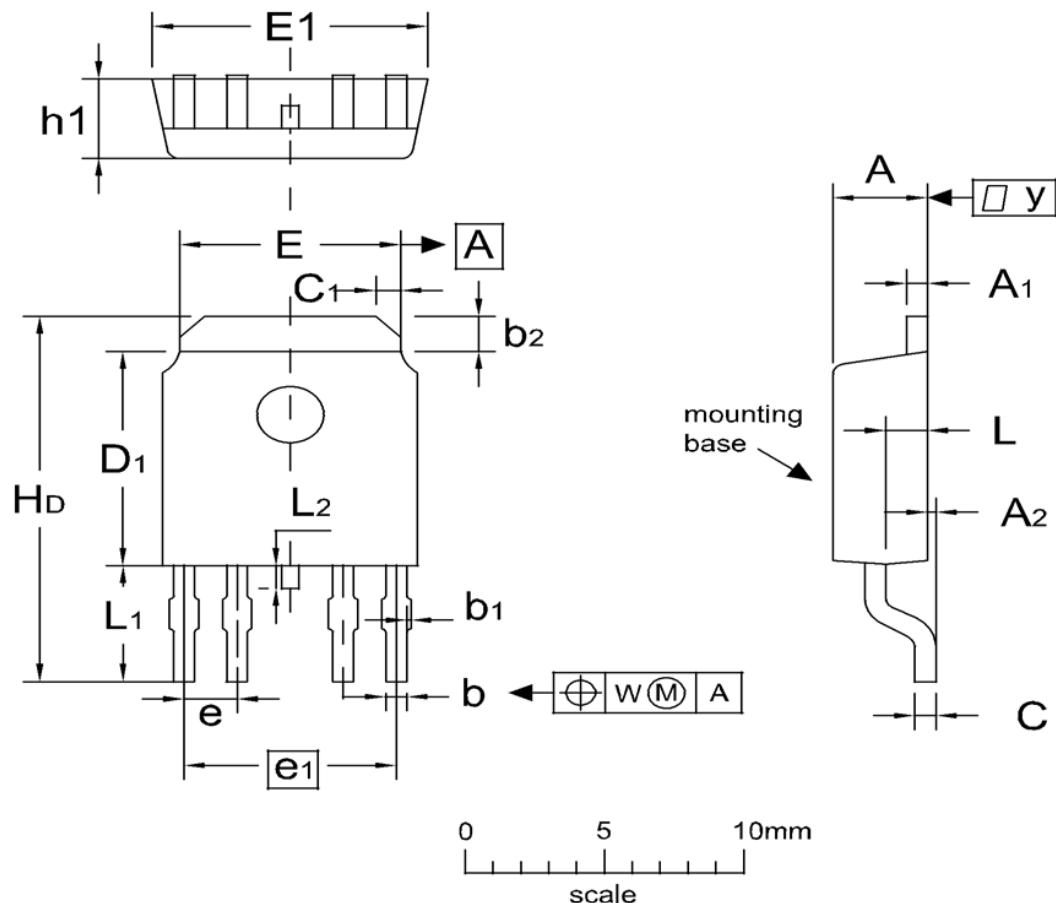
P-Channel Typical Characteristics





Test Circuit**Figure A. Gate Charge Test Circuit & Waveforms****Figure B. Switching Test Circuit & Waveforms****Figure C. Unclamped Inductive Switching Circuit & Waveforms**

TO-252-4L Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.1	2.3	2.5	A₁	0.4	0.5	0.6
A₂	--	--	0.3	b	0.4	0.5	0.6
b₁	--	--	0.1	b₂	0.8	1.0	1.2
C	0.4	0.5	0.6	C₁	0.4	0.6	0.8
D₁	5.7	6.1	6.5	E	5.0	5.3	5.6
E₁	6.3	6.6	6.9	e	--	1.27	--
e₁	--	5.08	--	H_D	9.6	10.0	10.4
h₁	2.1	2.3	2.5	L	0.80	1.0	1.2
L₁	2.6	2.9	3.2	L₂	0.35	0.65	0.95

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