

### ● General Description

The AGMH056N08A combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### ● Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

### ● Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGMH056N08A	AGMH056N08A	PDFN5*6	330mm	12mm	3000

**Table 1. Absolute Maximum Ratings (TC=25°C)**

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	85	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	142	A
	Drain Current-Continuous(Tc=100°C)	85	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	568	A
PD	Maximum Power Dissipation(Tc=25°C)	240	w
	Maximum Power Dissipation(Tc=100°C)	96	w
EAS	Avalanche energy <b>(Note 3)</b>	552	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

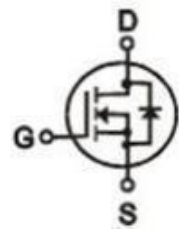
**Table 2. Thermal Characteristic**

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	50	°C/W
RθJC	Thermal Resistance Junction-Case <sup>1</sup>	---	0.52	°C/W

### Product Summary

BVDSS	RDSON	ID
85V	4.8mΩ	142A

### PDFN5\*6 Pin Configuration



**Table 3. Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	85	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=85V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	2.1	2.6	3.3	V
gFS	Forward Transconductance	VDS=5V,ID=20A	--	24	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A	--	4.8	6.1	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=40V, VGS=0V, F=1MHZ	--	3895	--	pF
Coss	Output Capacitance		--	665	--	pF
Crss	Reverse Transfer Capacitance		--	15	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	2.5	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V,VDS=40V, ID=40A,RGEN=3Ω	--	15	--	nS
tr	Turn-on Rise Time		--	52	--	nS
td(off)	Turn-Off Delay Time		--	38	--	nS
tf	Turn-Off Fall Time		--	24	--	nS
Qg	Total Gate Charge	VGS=40V, VDS=10V, ID=40A	--	57	--	nC
Qgs	Gate-Source Charge		--	19	--	nC
Qgd	Gate-Drain Charge		--	14	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	142	A
VSD	Forward on Voltage	VGS=0V,IS=10A	--	--	1.2	V
trr	Reverse Recovery Time	Is=10A , dI/dt=100A/μs , TJ=25°C	--	52	--	ns
Qrr	Reverse Recovery Charge		--	65	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: T<sub>J</sub>=25°C

Typical Characteristics

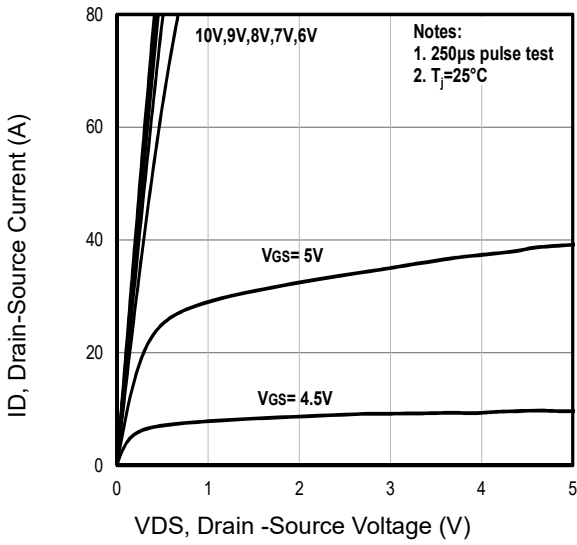


Fig1. Typical Output Characteristics

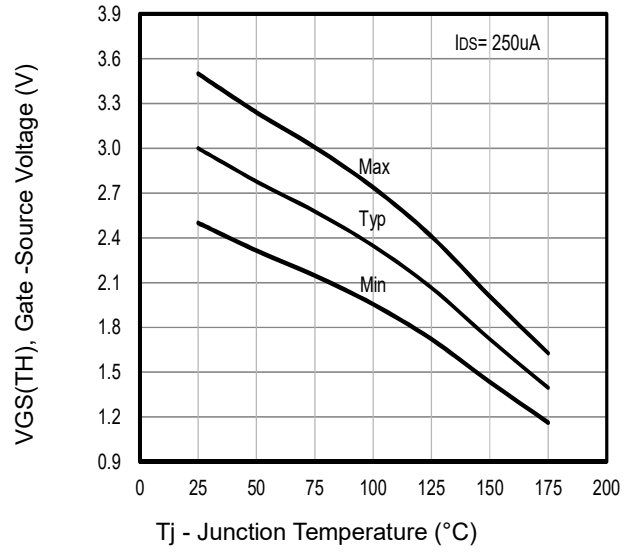


Fig2. Typical V<sub>GS(TH)</sub> Gate-Source Voltage Vs. T<sub>j</sub>

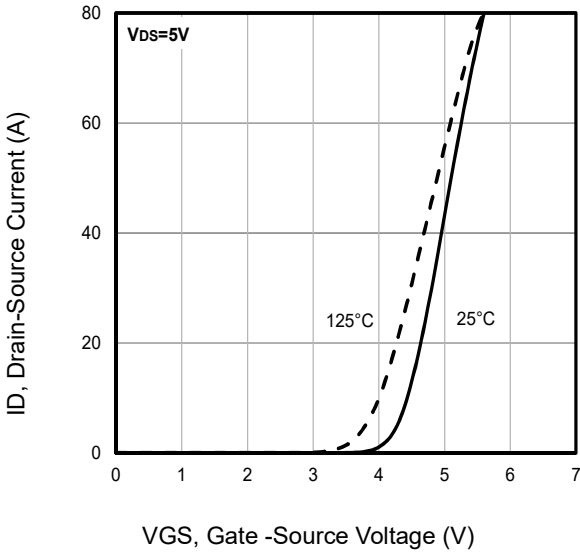


Fig3. Typical Transfer Characteristics

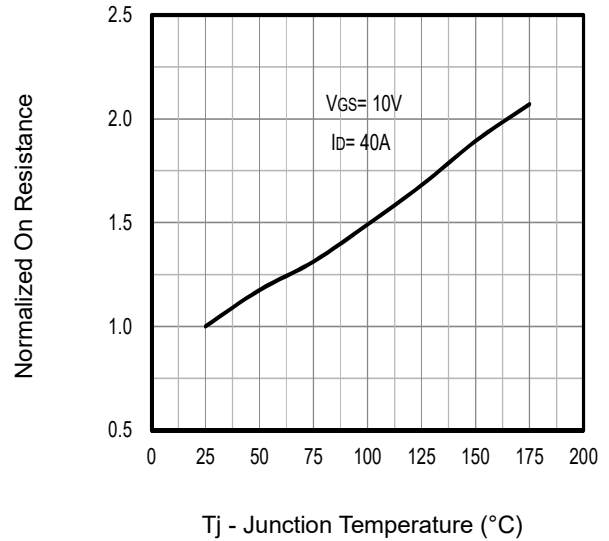


Fig4. Typical Normalized On-Resistance Vs. T<sub>j</sub>

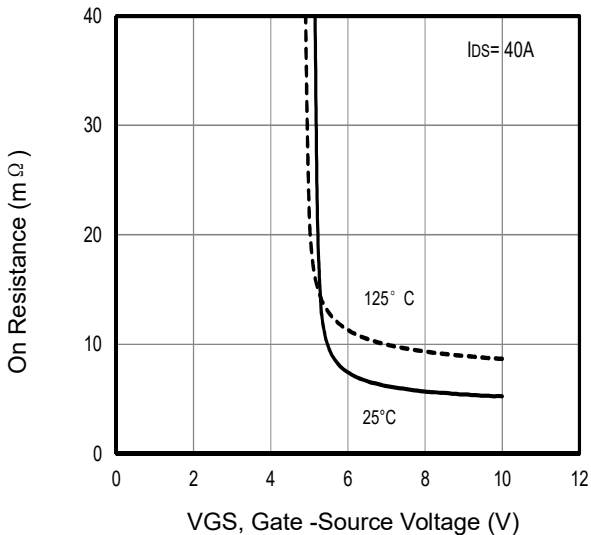


Fig5. Typical On Resistance Vs Gate-Source Voltage

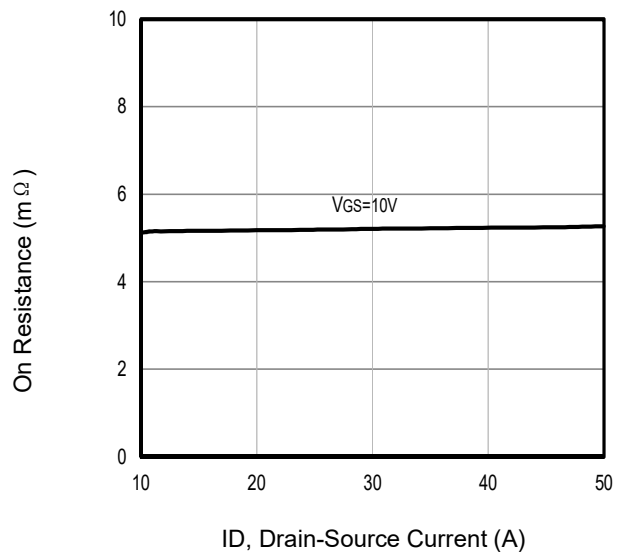
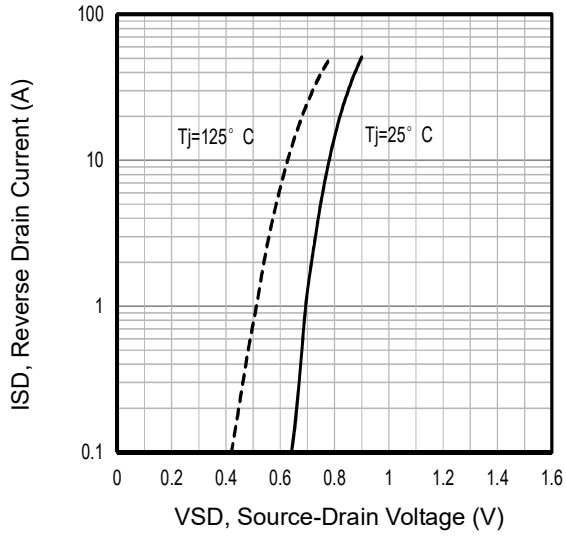
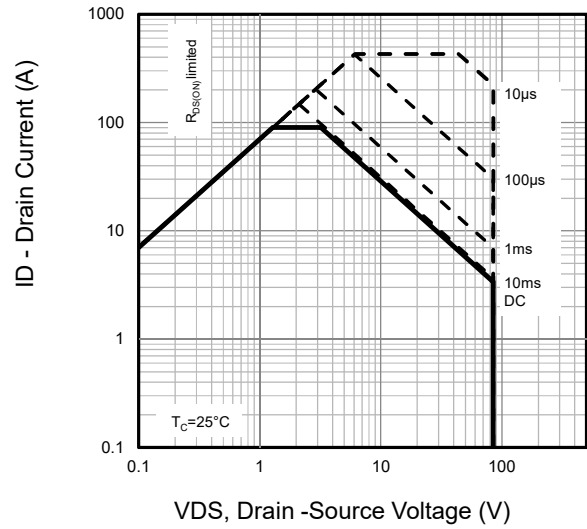


Fig6. Typical On Resistance Vs Drain Current

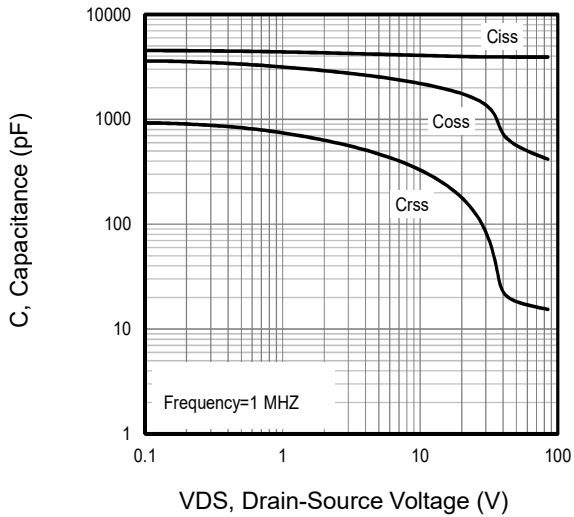
## Typical Characteristics



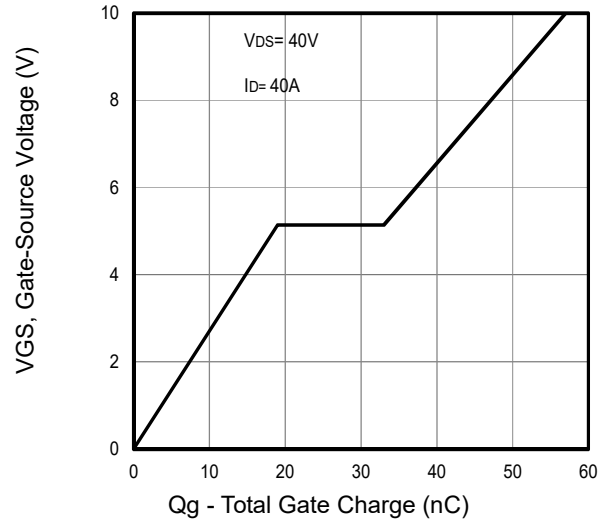
**Fig7.** Typical Source-Drain Diode Forward Voltage



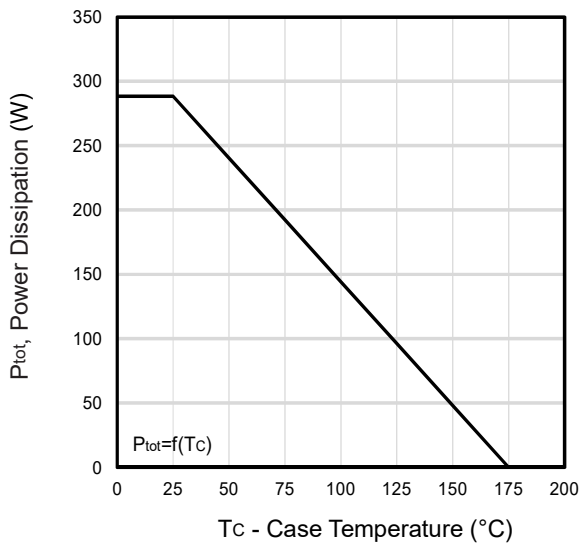
**Fig8.** Maximum Safe Operating Area



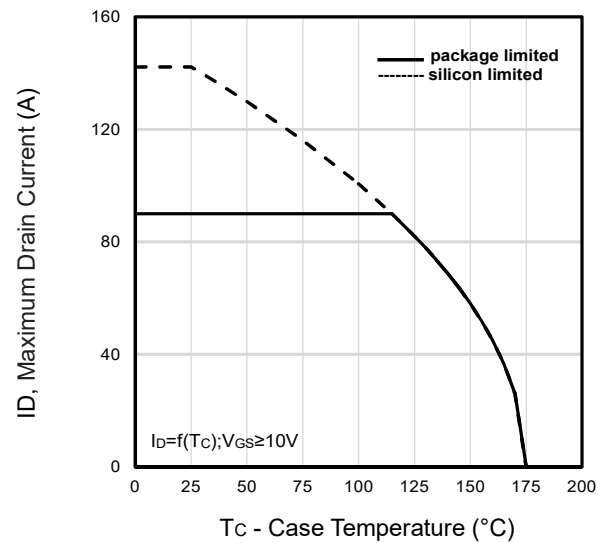
**Fig9.** Typical Capacitance Vs. Drain-Source Voltage



**Fig10.** Typical Gate Charge Vs. Gate-Source Voltage



**Fig11.** Power Dissipation Vs. Case Temperature



**Fig12.** Maximum Drain Current Vs. Case Temperature

Typical Characteristics

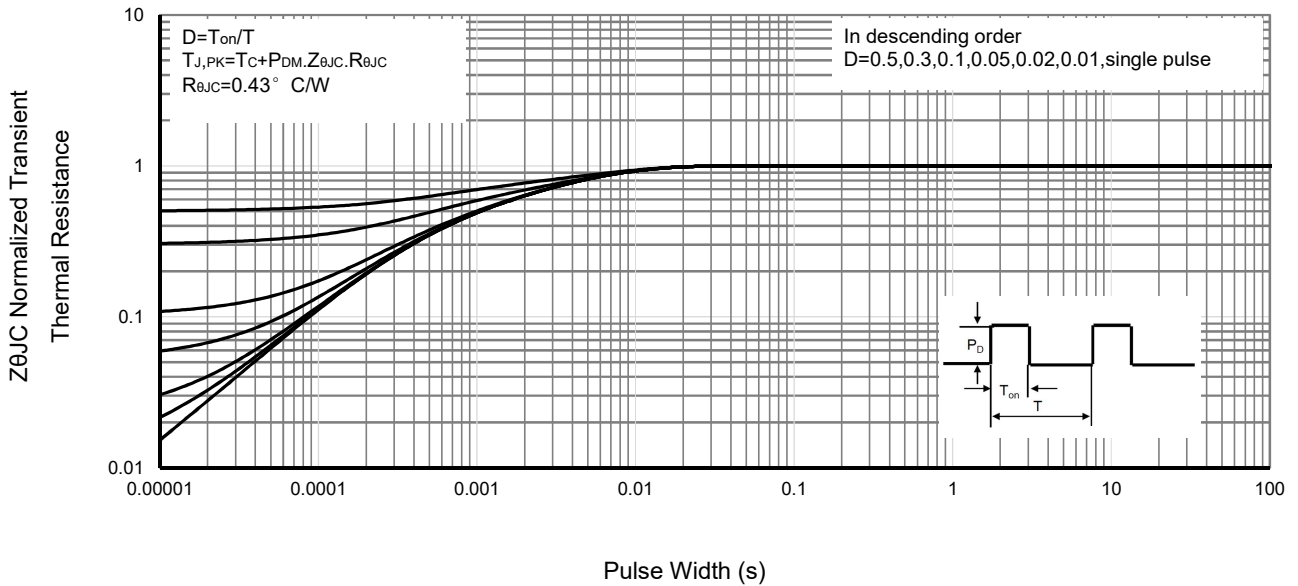


Fig13 . Normalized Maximum Transient Thermal Impedance

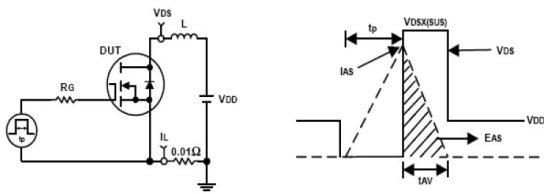


Fig14. Unclamped Inductive Test Circuit and waveforms

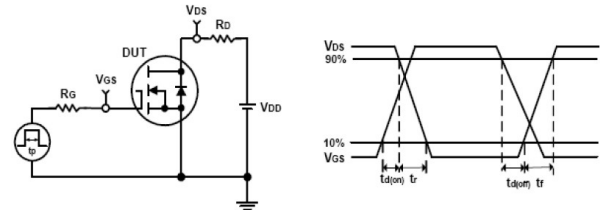
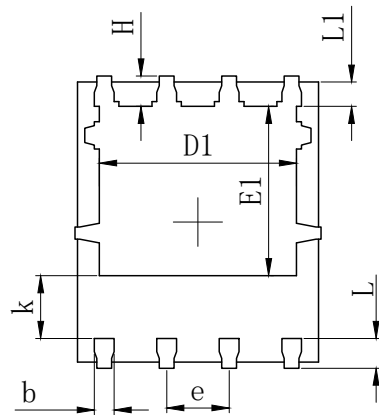
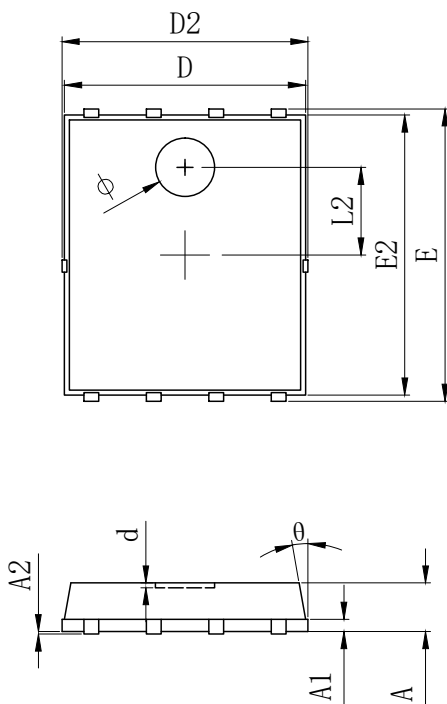


Fig15. Switching Time Test Circuit and waveforms

Dimensions:PDFN5\*6



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0~0.05		
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
theta	8°	10°	12°
phi	1.100	1.200	1.300
d			0.100


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