

### ● General Description

The AGMH606C combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### ● Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

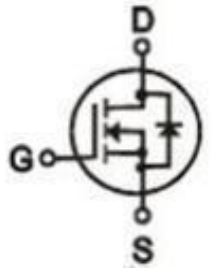
### ● Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### Product Summary

BVDSS	RDSON	ID
60V	5.3mΩ	80A

### TO-220 Pin Configuration



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGMH606C	AGMH606C	TO-220	----	----	1000

**Table 1. Absolute Maximum Ratings (TA=25°C)**

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	68	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	80	A
	Drain Current-Continuous(Tc=100°C)	52	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	320	A
PD	Maximum Power Dissipation(Tc=25°C)	108	w
	Maximum Power Dissipation(Tc=100°C)	53.5	w
EAS	Avalanche energy <b>(Note 3)</b>	169	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	°C

**Table 2. Thermal Characteristic**

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	50	°C/W
RθJC	Thermal Resistance Junction-Case <sup>1</sup>	---	1.4	°C/W

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	60	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=60V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	2.0	--	4.0	V
gFS	Forward Transconductance	VDS=5V,ID=20A	--	5	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=30A	--	5.3	7.0	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=30V,VGS=0V, F=1MHZ	--	4136	--	pF
Coss	Output Capacitance		--	286	--	pF
Crss	Reverse Transfer Capacitance		--	257	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V,VDS=30V RGEN=1.8Ω,ID=30A	--	9.0	--	nS
tr	Turn-on Rise Time		--	7.0	--	nS
td(off)	Turn-Off Delay Time		--	40	--	nS
tf	Turn-Off Fall Time		--	15	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=30V, ID=30A	--	90	--	nC
Qgs	Gate-Source Charge		--	9.0	--	nC
Qgd	Gate-Drain Charge		--	18	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	80	A
VSD	Forward on Voltage	VGS=0V,IS=30A	--	--	1.2	V
trr	Reverse Recovery Time	IS=30A , dI/dt=100A/μs , TJ=25°C	--	33	--	ns
Qrr	Reverse Recovery Charge		--	46	--	nc

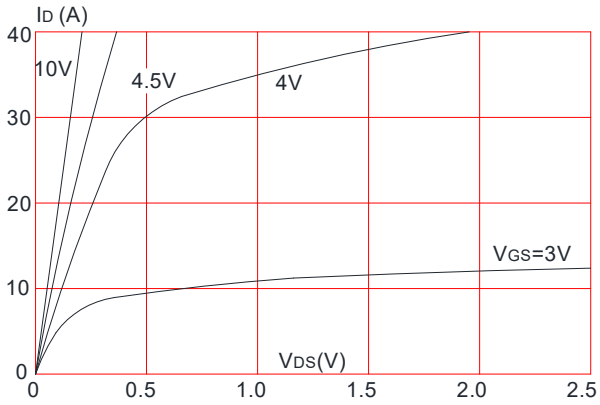
Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

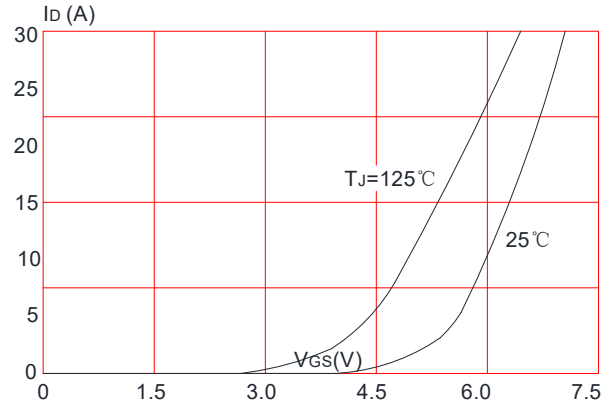
Notes 3.EAS condition: TJ=25°C

## Typical Performance Characteristics

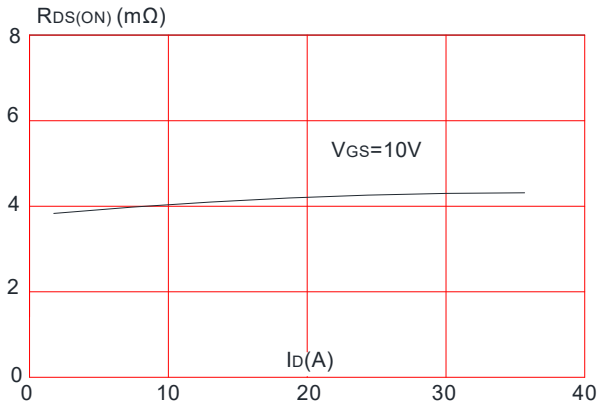
**Figure 1: Output Characteristics**



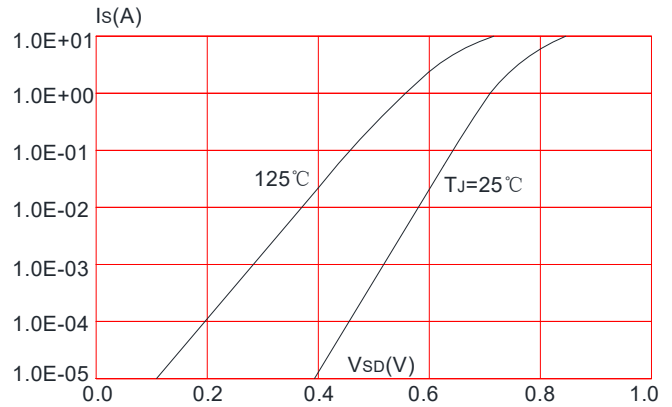
**Figure 2: Typical Transfer Characteristics**



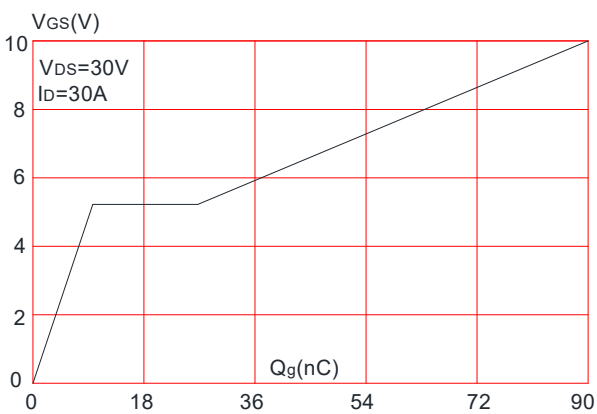
**Figure 3: On-resistance vs. Drain Current**



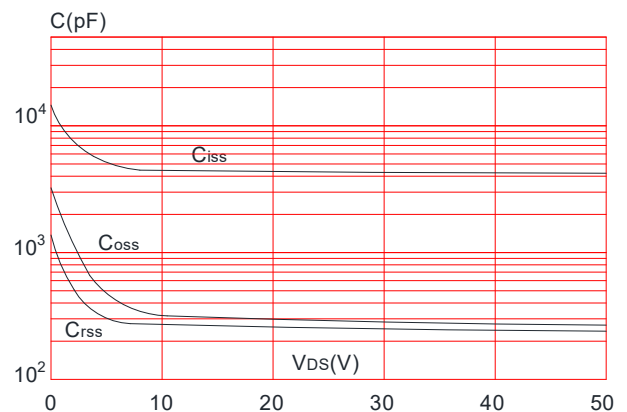
**Figure 4: Body Diode Characteristics**

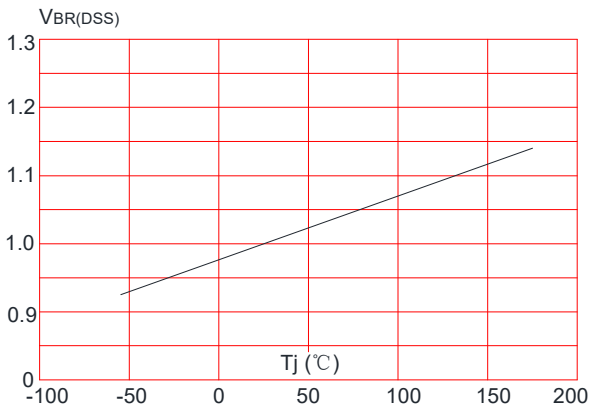
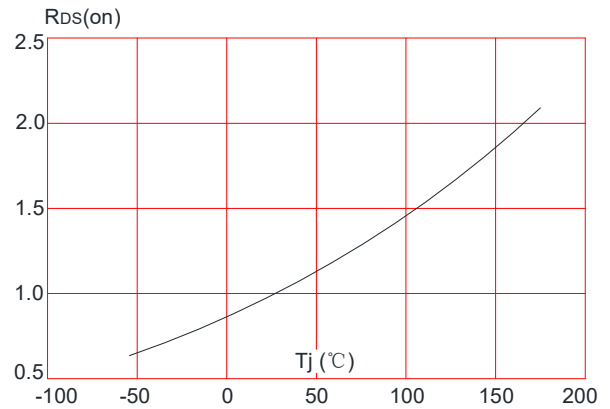
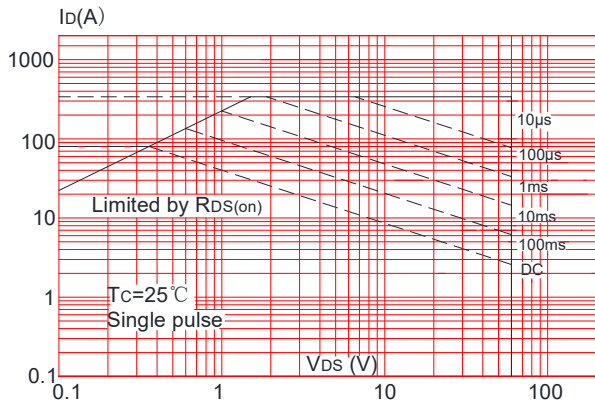
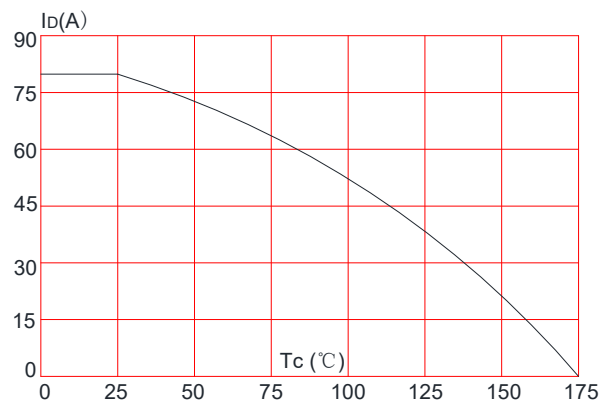
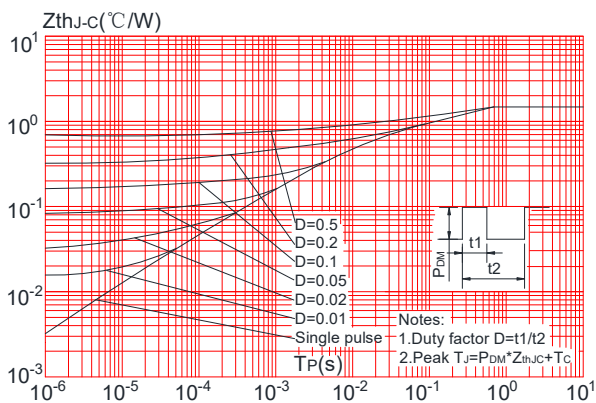


**Figure 5: Gate Charge Characteristics**



**Figure 6: Capacitance Characteristics**



**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**

**Figure 8: Normalized on Resistance vs. Junction Temperature**

**Figure 9: Maximum Safe Operating Area**

**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**

**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case**


Test Circuit

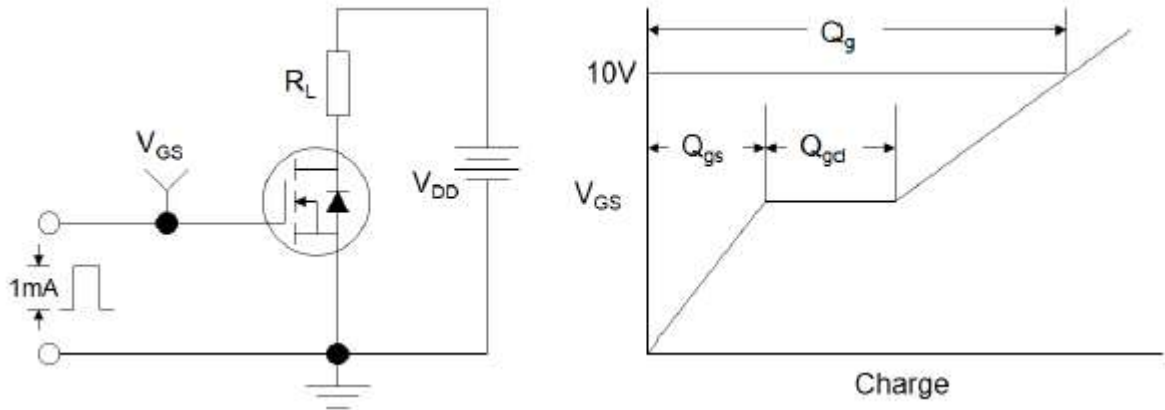


Figure1:Gate Charge Test Circuit & Waveform

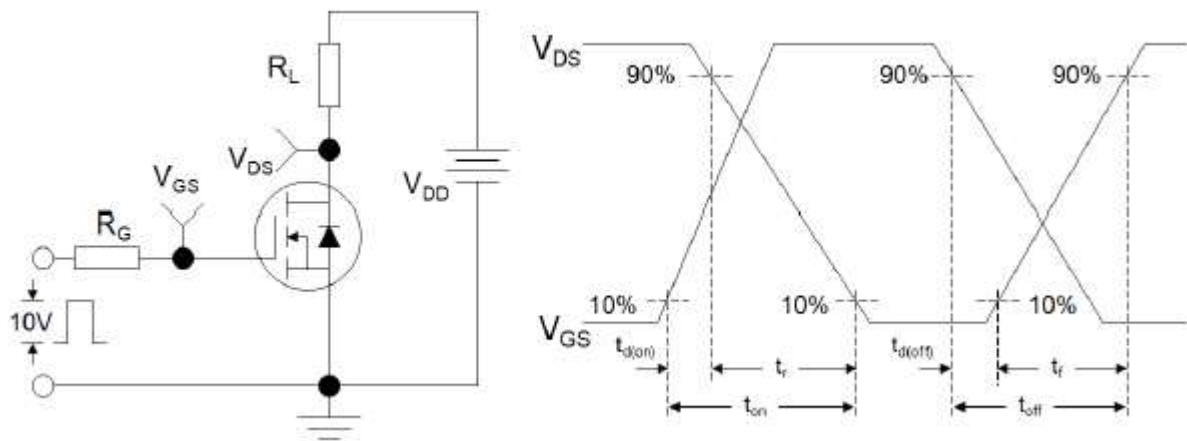


Figure 2: Resistive Switching Test Circuit & Waveforms

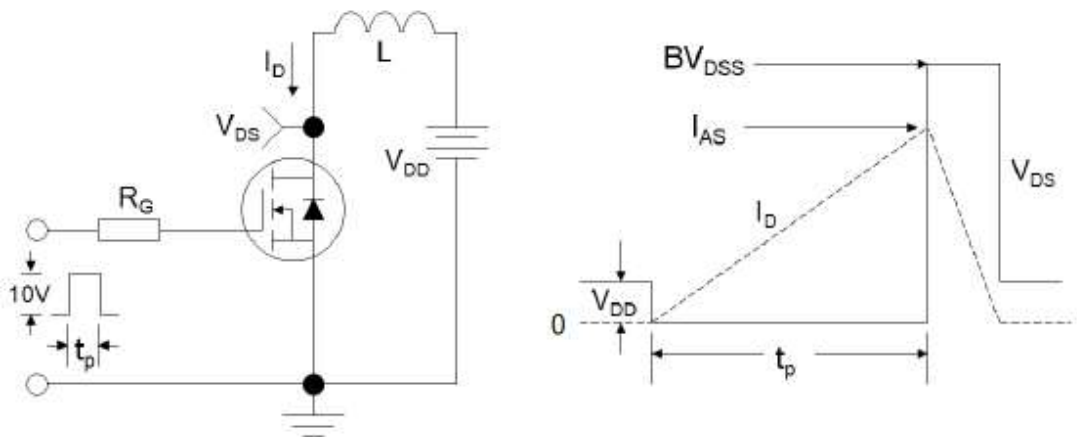
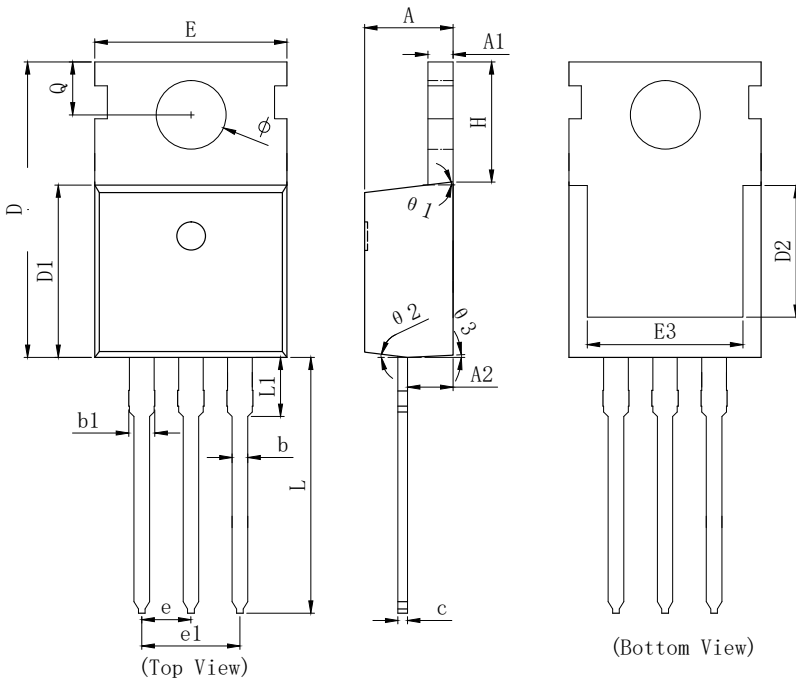


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

# TO-220 PACKAGE INFORMATION



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	4.370	4.570	4.700
A1	1.250	1.300	1.400
A2	2.150	2.350	2.550
b	0.700	0.800	0.950
b1	1.170	1.270	1.470
c	0.450	0.500	0.600
D	15.100	15.600	16.100
D1	8.800	9.100	9.400
D2	5.500	6.300 REF	
E	9.700	10.000	10.300
E3	7.000	7.600 REF	
e	2.540 BSC		
e1	5.080 BSC		
L	13.200	13.500	13.800
L1		3.100	3.400
H	6.250	6.500	6.750
$\phi$	3.400	3.600	3.800
Q	2.600	2.800	3.000
$\theta 1$	7° TYP		
$\theta 2$	7° TYP		
$\theta 3$	3° TYP		


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