

### AIC1734

### 300mA Low Dropout Linear Regulator

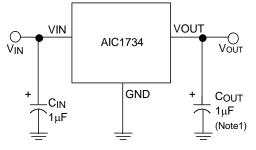
### **FEATURES**

- Low Dropout Voltage of 470mV at 300mA Output Current (3.0V Output Version).
- Guaranteed 300mA Output Current.
- Low Ground Current at 55μA.
- 2% Accuracy Output Voltage of 1.8V/ 2.0V /2.5V /2.7V/ 3.0V/ 3.3V/ 3.5V/ 3.7V/ 3.8V/ 5.0V/ 5.2V.
- Only needs 1µF Output Capacitor for Stability.
- Current and Thermal Limiting.

### APPLICATIONS

- CD-ROM Drivers.
- · LAN Cards.
- Microprocessor.
- RAM Module.
- Wireless Communication Systems.
- Battery Powered Systems.

### **TYPICAL APPLICATION CIRCUIT**



Low Dropout Linear Regulator

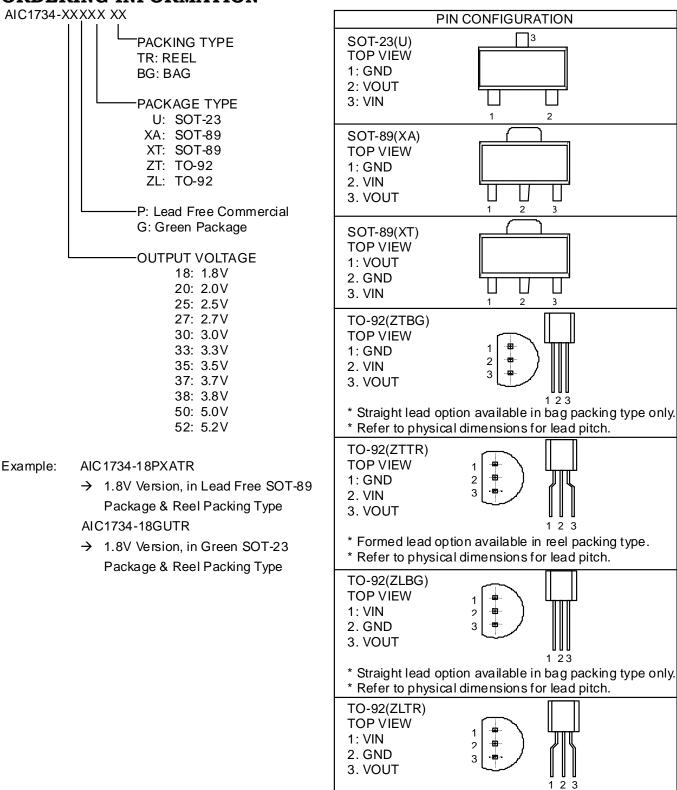
### DESCRIPTION

The AIC1734 is a 3-pin low dropout linear regulator. The superior characteristics of the AIC1734 include zero base current loss, very low dropout voltage, and 2% accuracy output voltage. Typical ground current remains approximately 55µA, for loading ranging from zero to maximum. Dropout voltage at 300mA output current is exceptionally low. Built-in output current limiting and thermal limiting provide maximal protection to the AIC1734 against fault conditions.

The AIC1734 is available in popular SOT-23, SOT-89 and TO-92 packages.

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\* Formed lead option available in reel packing type.\* Refer to physical dimensions for lead pitch.



### SOT-23 MARKING

Part No.	PU	GU	Part No.	PU	GU
AIC1734-18XU	CD18P	CD18G	AIC1734-35XU	CD35P	CD35G
AIC1734-20XU	CD20P	CD20G	AIC1734-37XU	CD37P	CD37G
AIC1734-25XU	CD25P	CD25G	AIC1734-38XU	CD38P	CD38G
AIC1734-27XU	CD27P	CD27G	AIC1734-50XU	CD50P	CD50G
AIC1734-30XU	CD30P	CD30G	AIC1734-52XU	CD52P	CD52G
AIC1734-33XU	CD33P	CD33G			

### • SOT-89 MARKING

SOT-89 MARKING						
ΡΧΑ	GXA	Part No.	РХТ	GXT		
CA18P	CA18G	AIC1734-18XXT	CB18P	CB18G		
CA20P	CA20G	AIC1734-20XXT	CB20P	CB20G		
CA25P	CA25G	AIC1734-25XXT	CB25P	CB25G		
CA27P	CA27G	AIC1734-27XXT	CB27P	CB27G		
CA30P	CA30G	AIC1734-30XXT	CB30P	CB30G		
CA33P	CA33G	AIC1734-33XXT	CB33P	CB33G		
CA35P	CA35G	AIC1734-35XXT	CB35P	CB35G		
CA37P	CA37G	AIC1734-37XXT	CB37P	CB37G		
CA38P	CA38G	AIC1734-38XXT	CB38P	CB38G		
CA50P	CA50G	AIC1734-50XXT	CB50P	CB50G		
CA52P	CA52G	AIC1734-52XXT	CB52P	CB52G		
	PXA   CA18P   CA20P   CA25P   CA27P   CA30P   CA33P   CA35P   CA37P   CA38P   CA38P   CA30P	PXA GXA   CA18P CA18G   CA20P CA20G   CA25P CA25G   CA27P CA27G   CA30P CA30G   CA33P CA33G   CA35P CA35G   CA37P CA35G   CA38P CA38G   CA38P CA50G	PXA GXA Part No.   CA18P CA18G AIC1734-18XXT   CA20P CA20G AIC1734-20XXT   CA25P CA25G AIC1734-25XXT   CA27P CA27G AIC1734-27XXT   CA30P CA30G AIC1734-30XXT   CA33P CA33G AIC1734-33XXT   CA35P CA35G AIC1734-35XXT   CA37P CA35G AIC1734-35XXT   CA38P CA38G AIC1734-35XXT   CA38P CA38G AIC1734-35XXT   CA36D CA37G AIC1734-35XXT	PXA GXA Part No. PXT   CA18P CA18G AIC1734-18XXT CB18P   CA20P CA20G AIC1734-20XXT CB20P   CA25P CA25G AIC1734-25XXT CB25P   CA27P CA27G AIC1734-27XXT CB27P   CA30P CA30G AIC1734-30XXT CB30P   CA33P CA33G AIC1734-33XXT CB33P   CA35P CA35G AIC1734-35XXT CB35P   CA37P CA35G AIC1734-35XXT CB35P   CA37P CA37G AIC1734-35XXT CB35P   CA38P CA38G AIC1734-35XXT CB35P   CA36 AIC1734-35XXT CB35P CA36		

### ABSOLUTE MAXIMUM RATINGS

Input Supply Voltage		-0.3 ~12V
Operating Temperature Range		-40°C~ 85°C
Storage Temperature Range		
Lead Temperature (Soldering 10 sec.)		
Thermal Resistance Junction to Case	SOT-89 Package	100°C/W
	TO-92 Package	120°C/W
	SOT-23 Package	130°C/W
Thermal Resistance Junction to Ambient	SOT-89 Package	160°C/W
(Assume no Ambient Airflow, no Heatsink)	TO-92 Package	150°C/W
	SOT-23 Package	180°C/W

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

### **TEST CIRCUIT**

Refer to the TYPICAL APPLICATION CIRCUIT

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### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, C<sub>IN</sub>=1µF, C<sub>OUT</sub>=1µF, unless otherwise specified.) (Note2)

PARAMETER	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT	
	No Load						
	AIC1734-52	V <sub>IN</sub> =5.5~12V	5.100	5.200	5.300		
	AIC1734-50	V <sub>IN</sub> =5.5~12V	4.900	5.000	5.100		
	AIC1734-38	V <sub>IN</sub> =4.1~12V	3.725	3.800	3.875		
	AIC1734-37	V <sub>IN</sub> =4.0~12V	3.625	3.700	3.775		
	AIC1734-35	V <sub>IN</sub> =4.0~12V	3.430	3.500	3.570		
Output Voltage	AIC1734-33	V <sub>IN</sub> =4.0~12V	3.235	3.300	3.365	V	
	AIC1734-30	V <sub>IN</sub> =4.0~12V	2.940	3.000	3.060		
	AIC1734-27	V <sub>IN</sub> =4.0~12V	2.646	2.700	2.754		
	AIC1734-25	V <sub>IN</sub> =4.0~12V	2.450	2.500	2.550		
	AIC1734-20	V <sub>IN</sub> =4.0~12V	1.960	2.000	2.040		
	AIC1734-18	V <sub>IN</sub> =4.0~12V	1.764	1.800	1.836		
Output Voltage							
Temperature	(Note 3)			50		PPM/°C	
Coefficiency							
	I <sub>L</sub> =1mA,						
Line Regulation	1.4V≤V <sub>OUT</sub> ≤3.2V	V <sub>IN</sub> =4V~12V		3	10	mV	
	3.3V≤V <sub>OUT</sub> ≤5.2V	V <sub>IN</sub> =5.5V~12V		3	10		
Load Degulation	I <sub>L</sub> =0.1~300mA						
Load Regulation	1.4V≤V <sub>OUT</sub> ≤3.9V			7	20	mV	
(Note 4)	4.0V≤V <sub>OUT</sub> ≤5.2V	V <sub>IN</sub> =7V		15	40		
Current Limit	V <sub>IN</sub> =7V, V <sub>OUT</sub> =0\	/	300			mA	
(Note 5)							
		4.0V≤V <sub>OUT</sub> ≤5.2V		400	500		
Dropout Voltage (Note 6)		$3.0V \le V_{OUT} \le 3.9V$		470	570		
	I <sub>L</sub> =300mA	$2.5V \le V_{OUT} \le 2.9V$		570	670	mV	
		$2.0V \le V_{OUT} \le 2.4V$		800	900		
		1.4V≤V <sub>OUT</sub> ≤1.9V		1260	1360		
	I <sub>O</sub> =0.1mA~I <sub>MAX</sub>						
Ground Current	$1.4V \le V_{OUT} \le 3.9V$	V <sub>IN</sub> =5~12V		55	80	μA	
	$4.0V \le V_{OUT} \le 5.2V$	V <sub>IN</sub> =7~12V		55	80	, p., i	

Note 1: To avoid output oscillation, aluminum electrolytic output capacitor is recommended and ceramic capacitor is not suggested.

Note 2: Specifications are production tested at T<sub>A</sub>=25°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).

Note 3: Guaranteed by design.

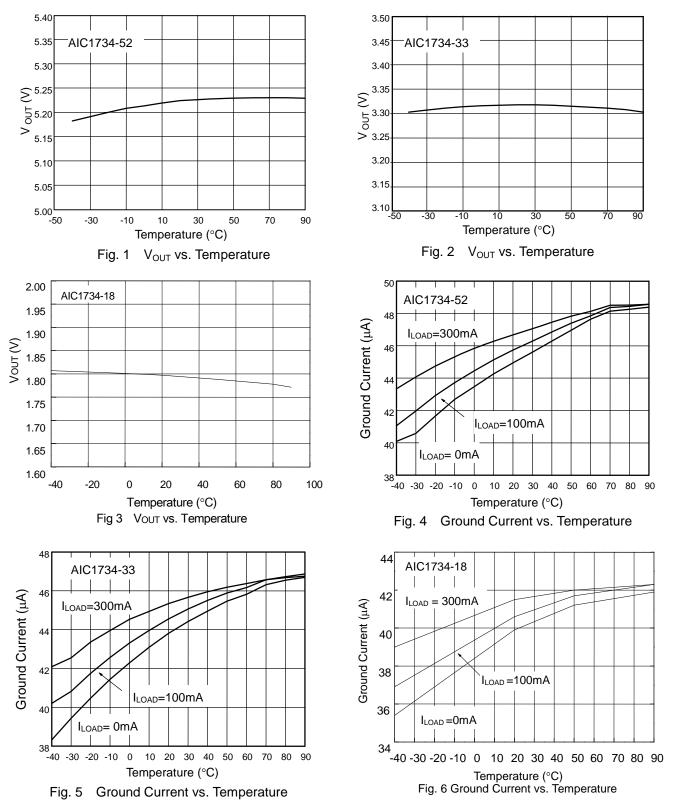
Note 4: Regulation is measured at constant junction temperature, using pulse testing with a low ON time.

Note 5: Current limit is measured by pulsing a short time.

Note 6: Dropout voltage is defined as the input to output differential at which the output voltage drops 100mV.

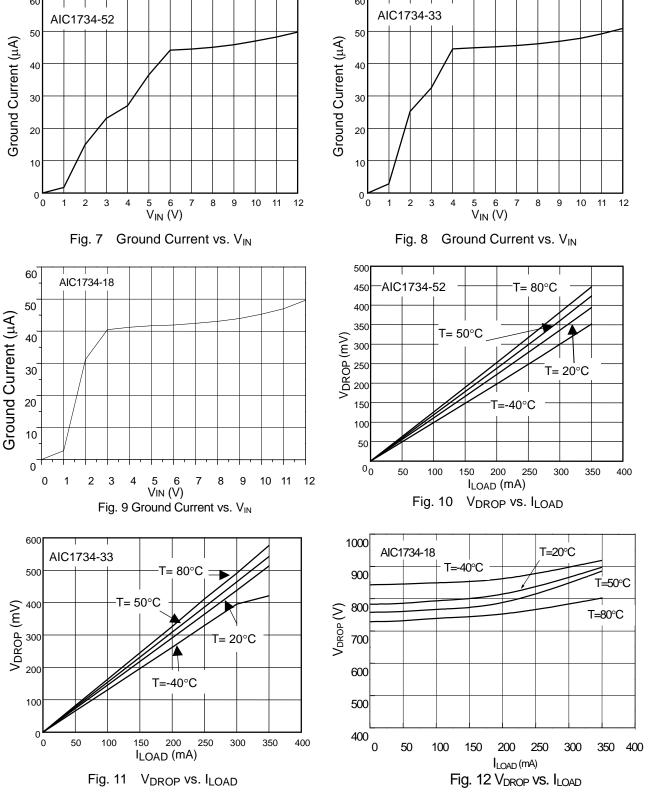
### **TYPICAL PERFORMANCE CHARACTERISTICS**

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## TYPICAL PERFORMANCE CHARACTERISTIC (Continued)

aic





aic

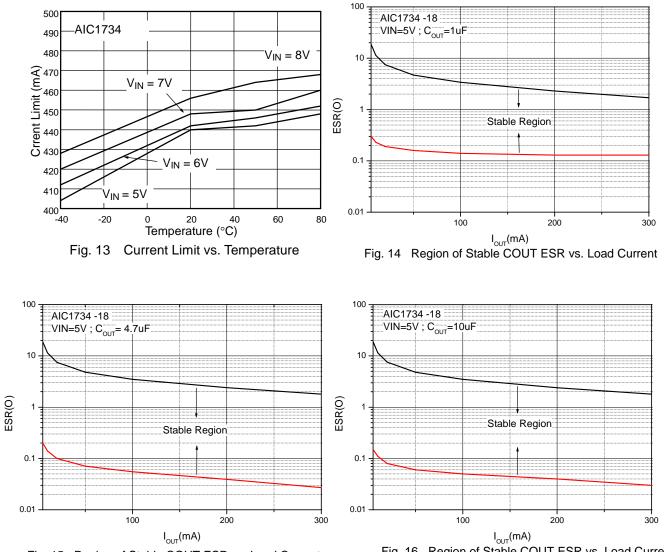
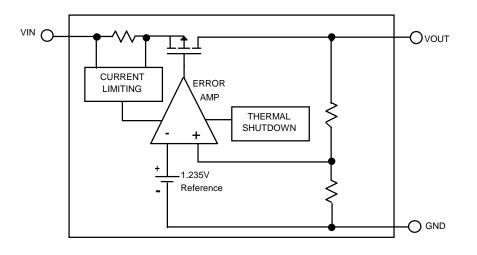


Fig. 15 Region of Stable COUT ESR vs. Load Current

Fig. 16 Region of Stable COUT ESR vs. Load Current



### BLOCK DIAGRAM



### PIN DESCRIPTIONS

VOUT PIN - Output pin.

GND PIN - Power GND.

VIN PIN - Power Supply Input.

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### **APPLICATION INFORMATION**

### **INPUT-OUTPUT CAPACITORS**

Linear regulators require input and output capacitors to maintain stability. A 1 $\mu$ F aluminum electrolytic input capacitor with a 1 $\mu$ F aluminum electrolytic output capacitor is recommended. To avoid oscillation, it is recommended to follow the figures of "Region of Stable C<sub>OUT</sub> ESR vs. Load Current" to choose proper capacitor specifications.

### POWER DISSIPATION

The AIC1734 obtains thermal-limiting circuitry, which is designed to protect the device against condition. For continuous overload load maximum condition. rating of junction temperature must not be exceeded. It is important to pay more attention in thermal resistance. It includes junction to case, junction to ambient. The maximum power dissipation of AIC1734 depends on the thermal resistance of its case and circuit board, the temperature difference between the die junction and ambient air, and the rate of airflow. The rate of temperature rise is greatly affected by the mounting pad configuration on the PCB, the

board material, and the ambient temperature. When the IC mounting with good thermal conductivity is used, the junction temperature will be low even when large power dissipation applies.

The power dissipation across the device is  $P = I_{OUT} (V_{IN} - V_{OUT})$ .

$$P_{MAX} = \frac{(I_{J-max} - I_A)}{R\theta_{JA}}$$

Where  $T_{J-max}$  is the maximum allowable junction temperature (125°C), and  $T_A$  is the ambient temperature suitable in application.

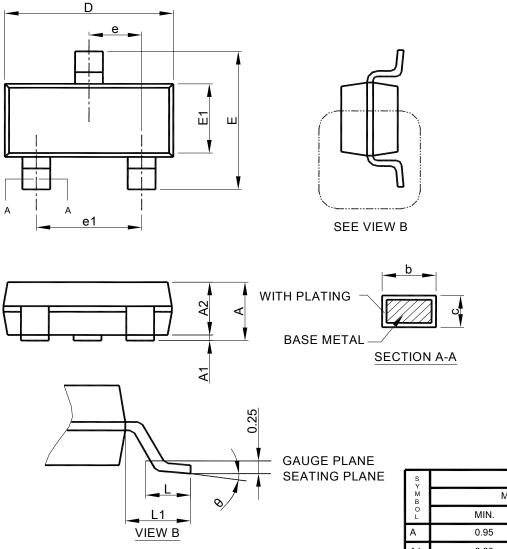
As a general rule, the lower temperature is, the better reliability of the device is. So the PCB mounting pad should provide maximum thermal conductivity to maintain low device temperature.

GND pin performs a dual function for providing an electrical connection to ground and channeling heat away. Therefore, connecting the GND pin to ground with a large pad or ground plane would increase the power dissipation and reduce the device temperature.



### PHYSICAL DIMENSIONS (unit: mm)

• SOT-23



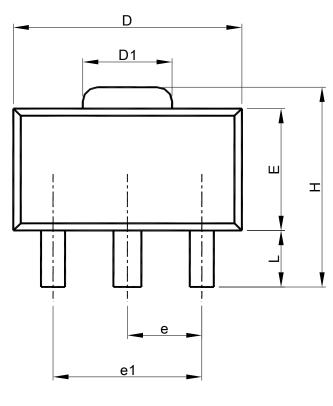
Note: 1. Refer to JEDEC MO-178.

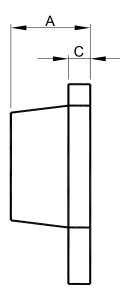
- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

s Y	SOT-23				
S Y B O L	MILLIMETERS				
O L	MIN.	MAX.			
А	0.95	1.45			
A1	0.00	0.15			
A2	0.90	1.30			
b	0.30	0.50			
с	0.08	0.22			
D	2.80	3.00			
E	2.60	3.00			
E1	1.50	1.70			
е	0.95 BSC				
e1	1.90 BSC				
L	0.30	0.60			
L1	0.60 REF				
θ	0°	8°			

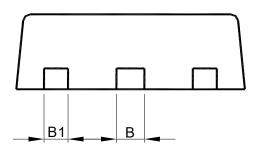
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### • SOT-89





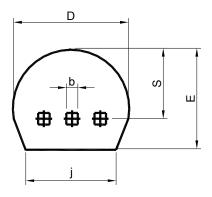
S Y	SOT-89			
M B O	MILLIMETERS			
O L	MIN.	MAX.		
А	1.40	1.60		
В	0.44	0.56		
B1	0.36	0.48		
С	0.35	0.44		
D	4.40	4.60		
D1	1.50	1.83		
Е	2.29	2.60		
е	1.50 BSC			
e1	3.00 BSC			
Н	3.94	4.25		
L	0.89	1.20		

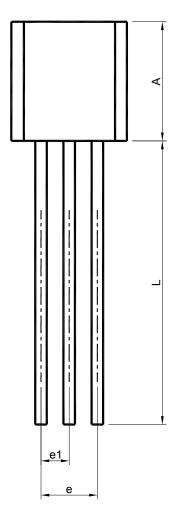


- Note: 1. Refer to JEDEC TO-243AA.
  - 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
  - 3. Dimension "E" does not include inter-lead flash or protrusions.
  - 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



• TO-92 (Straight lead option available in Bag packing type only)





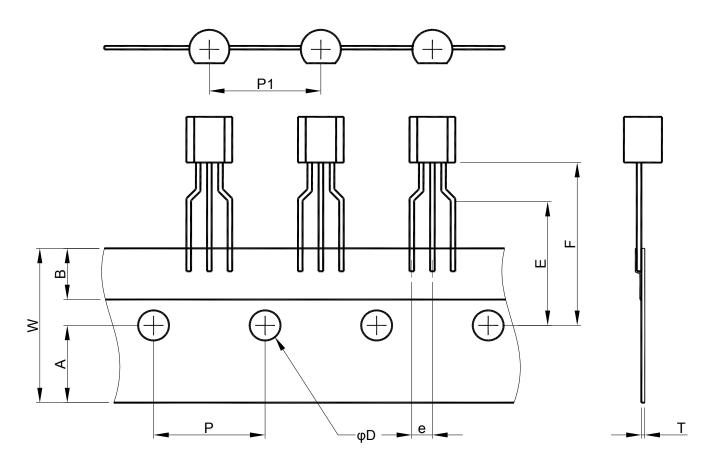
S Y	TO-9	2	
M B O L	MILLIMETERS		
O L	MIN.	MAX.	
А	4.32	5.33	
b	0.36	0.47	
D	4.45	5.20	
Е	3.18	4.19	
е	2.42	2.66	
e1	1.15	1.39	
j	3.43		
L	12.70		
S	2.03	2.66	

Note: 1. Refer to JEDEC TO-226.

- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side .
- 3. Dimension "A" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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• TO-92 (Formed lead option available in Reel packing)



SYMBOL	W	А	В	E	F
SPEC.	18.0±0.2	9.0±0.2	6.0±0.20	16.0±0.5	19.0±0.5
SYMBOL	Р	P 1	D	е	Т
SPEC.	12.7 BSC	12.7 BSC	4.0±0.2	2.5 BSC	0.6±0.1

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