

SOP-8 Plastic-Encapsulate Transistors

ALJP61089B

High Voltage Ringing SLIC Protector

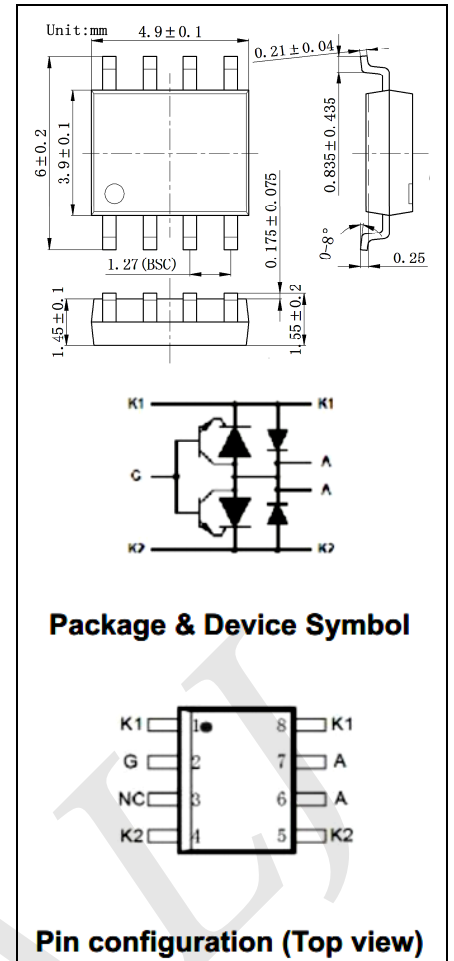
Features

- Dual programmable transient suppressor
- Wide battery voltage supports
- Low gate triggering current
- High holding current.
- ESD Immunity(HBM): JESD22 Class 3B, ≥8KV
- MLS: Lever 1 – unlimited

Applications

- Switch Line Card
- Access Network Line Card
- PBX
- VoIP

Marking: ALJP61089B



Descriptions

This device is especially designed to protect Subscriber Line Interface Circuit (SLIC) against transient overvoltage. Positive overloads are clipped with 2 diodes. Negative surges are suppressed by 2 Thyristors, their breakdown voltage being referenced to VBAT through the gate. This component presents a very low gate triggering current and minimizes overvoltage stress on the SLIC.

Pin configuration

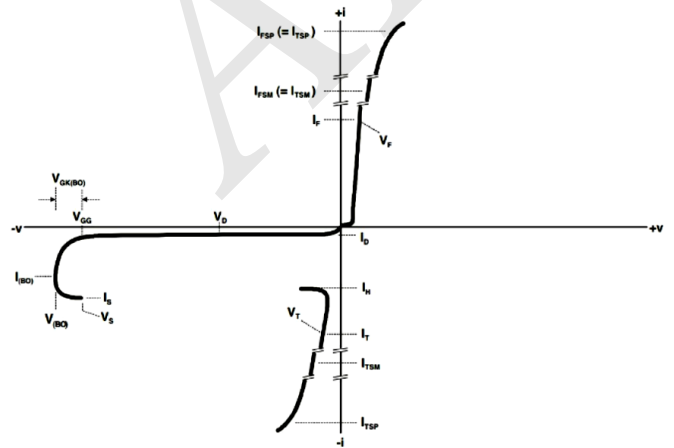
Pin #	Pin Name	Description
1,4,5,8	K1, K2	Connect to subscriber lines (Tip/Ring)
2	G	Connect to battery (Reference voltage)
6,7	A	Connect ground
3	NC	Not connected

Maximum Ratings ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
I_{PPSM}	Non-repetitive peak on-state pulse current	10/1000us (Telcordia (Bellcore) GR-1089-CORE, Issue 3)	30 A
		5/310us (TU-T K.20, K.21& K.45, K.44 open-circuit voltage wave shape 10/700 μs)	40 A
		2/10us (Telcordia (Bellcore) GR-1089-CORE, Issue 3)	120 A
I_{TSM}	Non repetitive peak on-state current (sinusoidal) 60Hz	0.1s	6.5 A
		1s	4.5 A
		5s	2.4 A
		30s	1.3 A
		900s	0.72 A
V_{DRM}	Repetitive peak off-state voltage, $V_{GK} = 0$	-170	V
V_{GKRM}	Repetitive peak gate-cathode voltage, $V_{KA}=0$	-120	V
T_A	Operating free-air temperature range	-40 ~ 85	$^{\circ}\text{C}$
T_{stg}	Storage temperature range	-40 ~ 150	$^{\circ}\text{C}$
T_j	Junction temperature	-40 ~ 150	$^{\circ}\text{C}$
T_L	Maximum lead temperature for soldering during 10s	260	$^{\circ}\text{C}$
$R_{\theta JA}$	Junction to free air thermal resistance	120	$^{\circ}\text{C/W}$

Parameter Measurement Information

Symbol	Parameter
I_D	Off-state current
I_H	Holding current
$V_{(BO)}$	Breakover voltage
V_F	Forward voltage
V_{FRM}	Peak forward recovery voltage
$V_{GK(BD)}$	Gate-cathode impulse breakover voltage
I_{GKS}	Gate reverse current
I_{GT}	Gate trigger current
V_{GT}	Gate-cathode trigger voltage
C_{KA}	Cathode-anode off-state capacitance

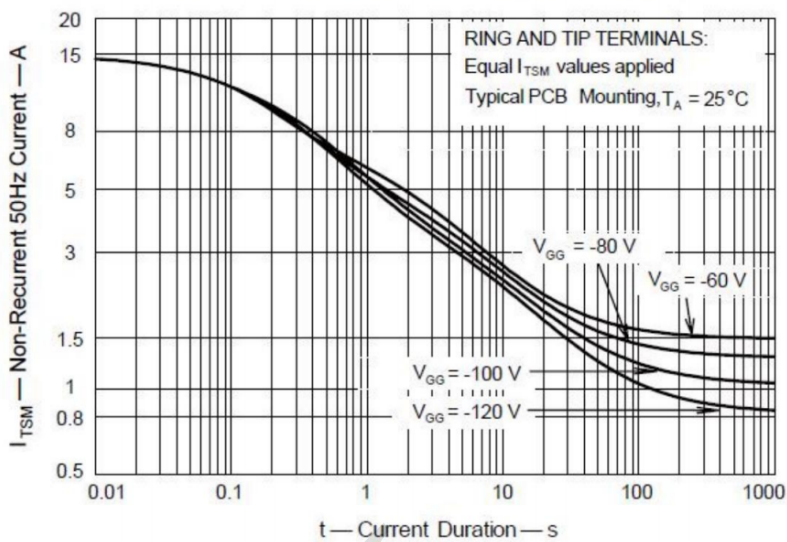


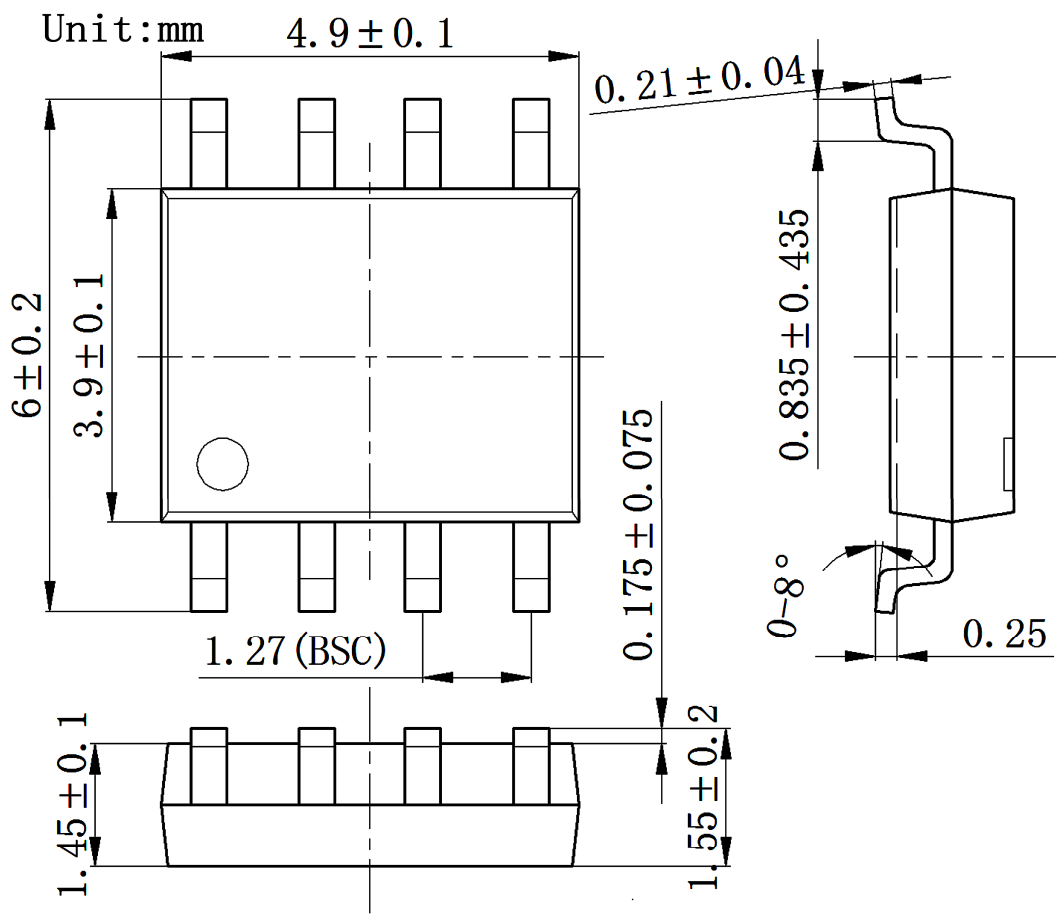
Electrical Characteristics ($T_a=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_F	Forward voltage	$I_F = 5\text{A}$, $t_W = 200\mu\text{s}$			3	V
V_{FRM}	Impulse peak forward recovery voltage	$2/10\mu\text{s}$, $I_F = 100\text{A}$, $R_S = 50\Omega$, $di/dt=80\text{A}/\mu\text{s}$			10	V
I_D	Off-state current	$V_D = -120\text{V}$, $V_{GK}=0$, $T_J = 25^\circ\text{C}$			-5	μA
		$V_D = -120\text{V}$, $V_{GK}=0$, $T_J = 85^\circ\text{C}$				
$V_{(BO)}$	Impulse breakover voltage	$2/10\mu\text{s}$, $I_{TM} = 100\text{A}$, $R_S = 50\Omega$, $di/dt = -80\text{A}/\mu\text{s}$, $V_{GG} = -100\text{V}$			-112	V
I_H	Holding current	$I_T = -1\text{A}$, $di/dt = 1\text{A}/\text{ms}$, $V_{GG} = -100\text{V}$	-150			mA
I_{GAS}	Gate reverse current	$V_{GG}=V_{GK} = -167\text{V}$, $V_{KA}=0$, $T_J = 25^\circ\text{C}$			-5	μA
		$V_{GG}=V_{GK} = -167\text{V}$, $V_{KA}=0$, $T_J = 85^\circ\text{C}$				
I_{GT}	Gate trigger current	$I_T=3\text{A}$, $t_{p(g)}\geq 20\mu\text{s}$, $V_{GG}=-100\text{V}$			5	mA
V_{GT}	Gate trigger voltage	$I_T=3\text{A}$, $t_{p(g)}\geq 20\mu\text{s}$, $V_{GG}=-100\text{V}$			2.5	V
C_{KA}	Anode-cathode offstate capacitance	$f=1\text{MHz}$, $V_D=1\text{V}$, $I_G=0$, $V_D=-3\text{V}$			110	pF
		$f=1\text{MHz}$, $V_D=1\text{V}$, $I_G=0$, $V_D=-48\text{V}$			55	

Typical Characteristics

Non-Repetitive Peak On-state Current against Duration





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