

SOT-23 Plastic-Encapsulate MOSFETS

AO3402

N-Channel MOSFET

Features

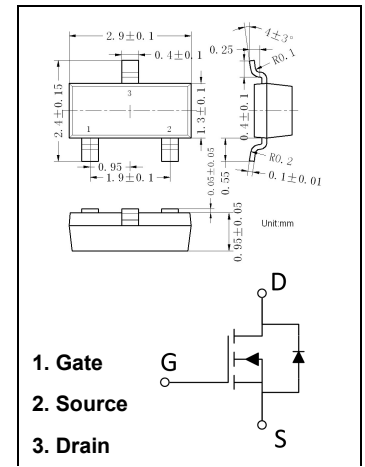
- $R_{DS(on)} < 70m\Omega (V_{GS}=10V, I_D=4.0A)$
- $R_{DS(on)} < 75m\Omega (V_{GS}=4.5V, I_D=2.3A)$
- $R_{DS(on)} < 105m\Omega (V_{GS}=2.5V, I_D=1.5A)$
- $V_{DS}=30V$
- $I_D=4.0A$

Applications

- Load Switch and in PWM applications

Description

The AO3402 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltage as low as 2.5V. This device is suitable for use as a load switch or in PWM application.



Maximum Ratings ($T_a=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source voltage	30	V
V_{GS}	Gate-Source voltage	± 12	
I_D	Continuous Drain Current	4	A
I_{DM}	Drain Current-Pulsed ¹⁾	15	
P_D	Power Dissipation	1.4	W
T_J	Junction Temperature	150	$^\circ C$
T_{STG}	Storage Temperature	-55 to +150	

Thermal Characteristics ($T_a=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Typ	Max	Unit
Maximum junction-ambient($t \leq 10s$)	$R_{\theta JA}$	70	90	$^\circ C/W$
Maximum junction-ambient	$R_{\theta JA}$	100	125	$^\circ C/W$
Maximum junction-Lead	$R_{\theta JL}$	63	80	$^\circ C/W$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$			1	μA
I_{GSS}	Gate-body Leakage current	$V_{DS} = 0V, V_{GS} = \pm 12V$			± 100	nA
On characteristics ³⁾						
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 4A$			70	m Ω
		$V_{GS} = 4.5V, I_D = 2.3A$			75	m Ω
		$V_{GS} = 2.5V, I_D = 1.5A$			105	m Ω
g_{fs}	Forward Trans conductance	$V_{DS} = 5V, I_D = 4A$		8		S
$V_{GS(th)}$	Gate-Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.8		1.6	V
Dynamic Characteristics ⁴⁾						
C_{iss}	Input Capacitance	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		390		pF
C_{oss}	Output Capacitance			54.5		
C_{rss}	Reverse Transfer Capacitance			41		
R_g	Gate resistance	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		3		Ω
Switching Characteristics ⁴⁾						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 15V,$ $R_L = 3.75\Omega, R_{GEN} = 6\Omega$		3.3		ns
t_r	Rise Time			1		
$t_{d(off)}$	Turn-Off Delay Time			21.7		
t_f	Fall Time			2.1		
Q_g	Total gate charge	$V_{DS} = 15V, V_{GS} = 4.5V, I_D = 4A$		4.34		nC
Q_{gs}	Gate-source Charge			0.6		
Q_{gd}	Gate-drain Charge			1.38		
t_{rr}	Body diode reverse recovery time	$I_F = 4A, dI/dt = 100A/\mu s$		12		nS
Q_{rr}	Body diode reverse recovery charge			6.3		nC
V_{SD}	Diode forward voltage	$V_{GS} = 0V, I_D = 1A$		0.8	1.2	V

Notes

1. Repetitive rating: Pulse width limited by junction temperature.
2. Surface mounted on FR4 board , $t \leq 10s$.
3. Pulse Test : Pulse Width $\leq 80\mu s$, Duty Cycle $\leq 0.5\%$.
4. Guaranteed by design, not subject to production.

Typical Characteristics

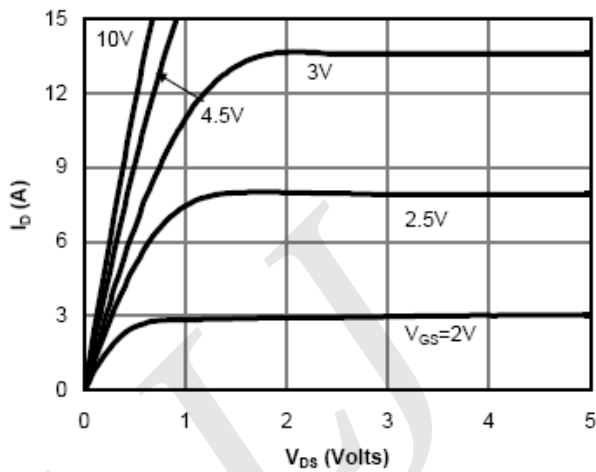


Fig 1: On-Region Characteristics

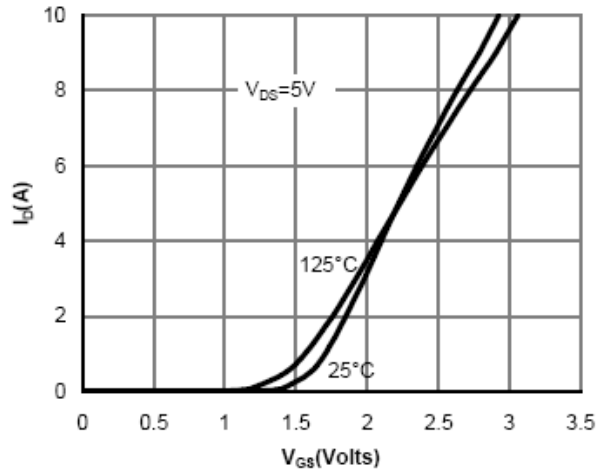


Figure 2: Transfer Characteristics

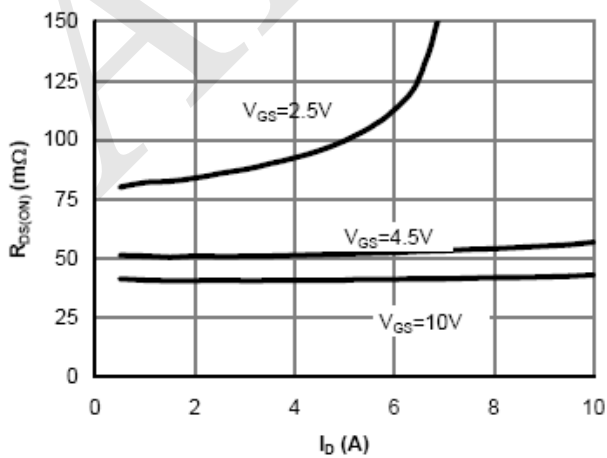


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

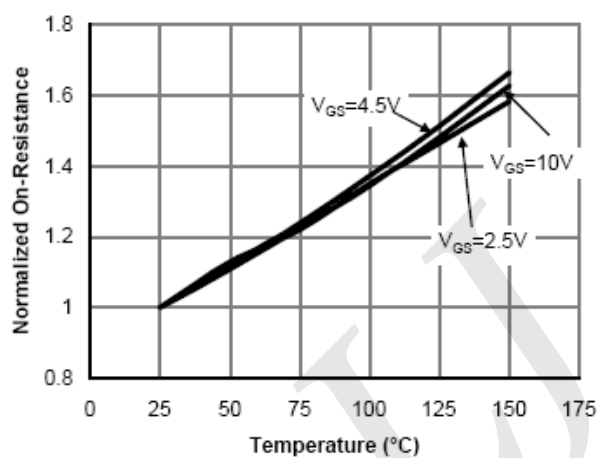


Figure 4: On-Resistance vs. Junction Temperature

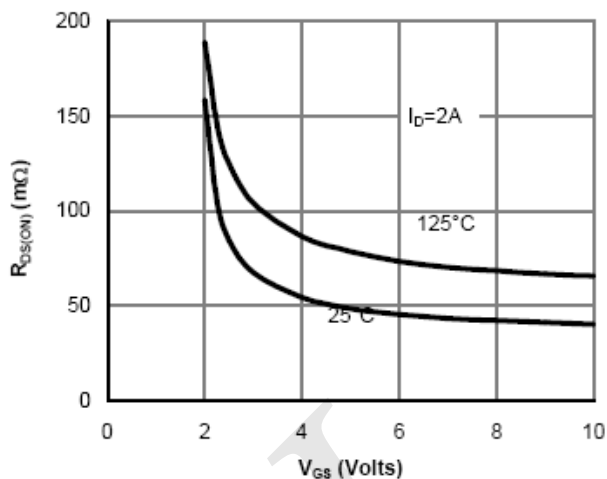


Figure 5: On-Resistance vs. Gate-Source Voltage

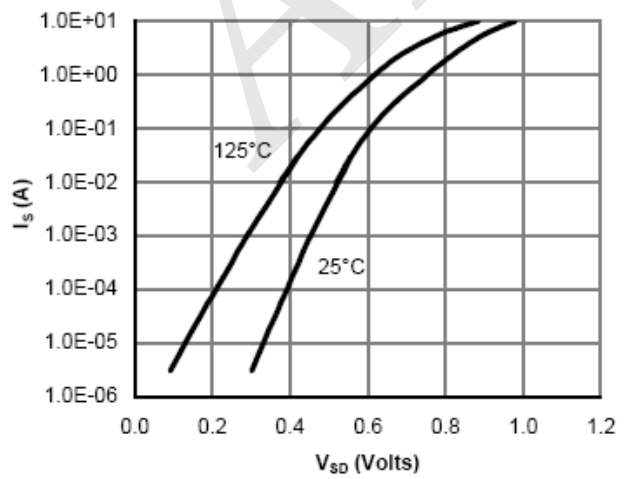


Figure 6: Body-Diode Characteristics

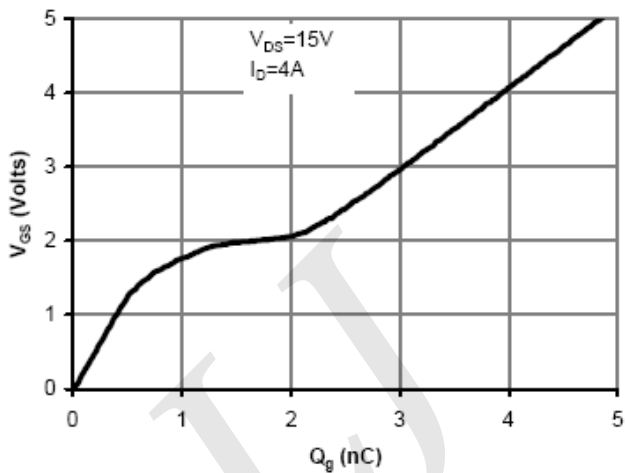


Figure 7: Gate-Charge Characteristics

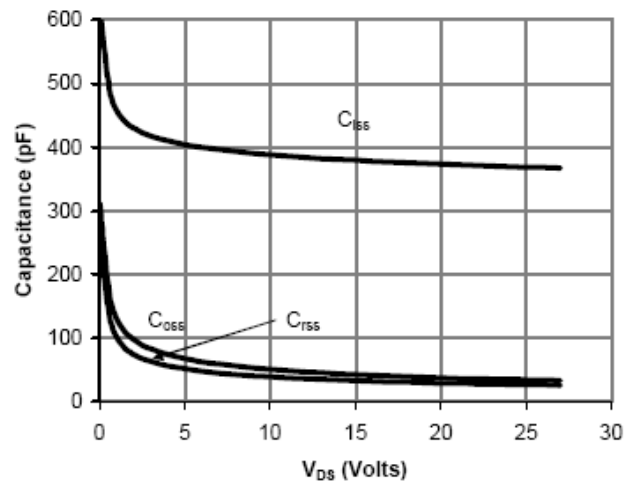


Figure 8: Capacitance Characteristics

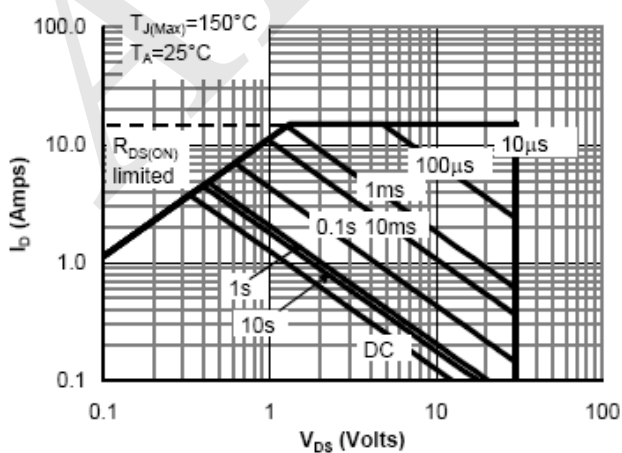


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

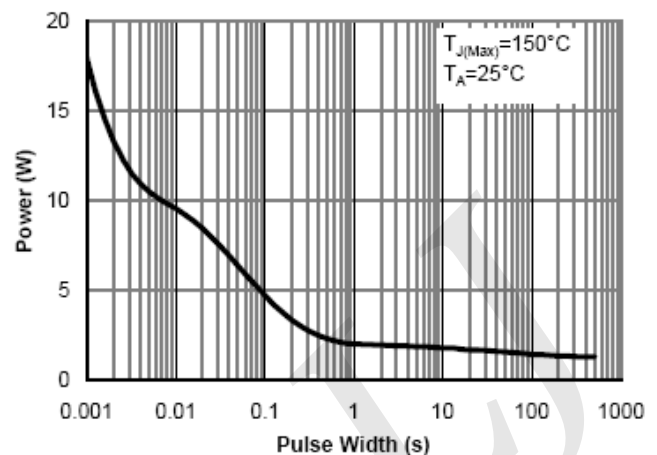


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

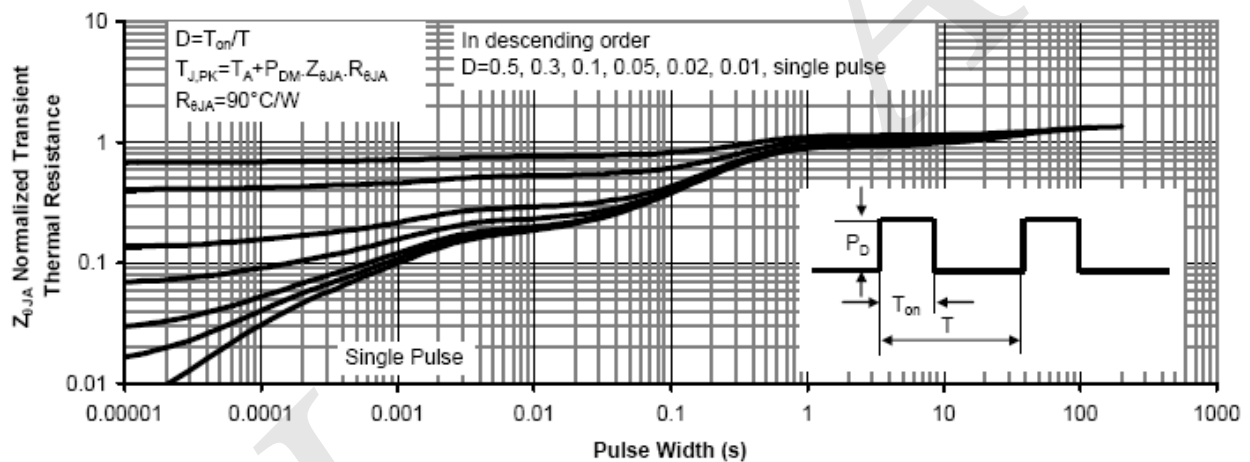


Figure 11: Normalized Maximum Transient Thermal Impedance

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