



ALPHA & OMEGA
SEMICONDUCTOR

AON6590A

40V N-Channel MOSFET

General Description

- Trench Power MV MOSFET technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Optimized for fast-switching applications
- RoHS and Halogen-Free Compliant

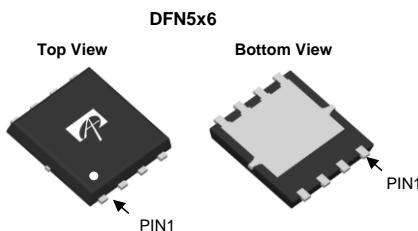
Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

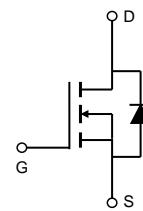
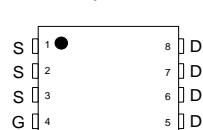
Product Summary

V_{DS}	40V
I_D (at $V_{GS}=10V$)	300A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	< 0.99mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	< 1.5mΩ

100% UIS Tested
100% R_g Tested



Top View



Orderable Part Number

Package Type

Form

Minimum Order Quantity

AON6590A	DFN 5x6	Tape & Reel	3000
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Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	300	A
		225	
Pulsed Drain Current ^C	I_{DM}	900	
Continuous Drain Current	I_{DSM}	67	A
		54	
Avalanche Current ^C	I_{AS}	65	A
Avalanche energy $L=0.3mH$ ^C	E_{AS}	634	mJ
V_{DS} Spike	10μs	V_{SPIKE}	
Power Dissipation ^B	$T_c=25^\circ C$	208	W
	$T_c=100^\circ C$	83	
Power Dissipation ^A	$T_A=25^\circ C$	7.3	W
	$T_A=70^\circ C$	4.7	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	14	17	°C/W
Maximum Junction-to-Ambient ^{A,D}		40	50	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.45	0.6	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	40			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1 5	μA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm20\text{V}$			±100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.3	1.8	2.3	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=20\text{A}$ $T_J=125^\circ\text{C}$		0.78	0.99	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=20\text{A}$		1.17	1.55	$\text{m}\Omega$
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}, I_D=20\text{A}$	100			S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.66	1	V
I_S	Maximum Body-Diode Continuous Current				160	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=20\text{V}, f=1\text{MHz}$		8320		pF
C_{oss}	Output Capacitance			1438		pF
C_{rss}	Reverse Transfer Capacitance			85		pF
R_g	Gate resistance	f=1MHz	0.5	1.15	1.8	Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, I_D=20\text{A}$		100		nC
$Q_g(4.5\text{V})$	Total Gate Charge			45		nC
Q_{gs}	Gate Source Charge			25		nC
Q_{gd}	Gate Drain Charge			7		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, R_L=1.0\Omega, R_{\text{GEN}}=3\Omega$		19		ns
t_r	Turn-On Rise Time			7		ns
$t_{D(off)}$	Turn-Off DelayTime			69		ns
t_f	Turn-Off Fall Time			10		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=20\text{A}, dI/dt=400\text{A}/\mu\text{s}$		26		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=20\text{A}, dI/dt=400\text{A}/\mu\text{s}$		83		nC

A. The value of R_{QJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\text{QJA}} \leq 10\text{s}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on $T_{J(\text{MAX})}=150^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C. Single pulse width 10us limited by junction temperature $T_{J(\text{MAX})}=150^\circ\text{C}$.

D. The R_{QJA} is the sum of the thermal impedance from junction to case R_{QJC} and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(\text{MAX})}=150^\circ\text{C}$. The SOA curve provides a single pulse rating.

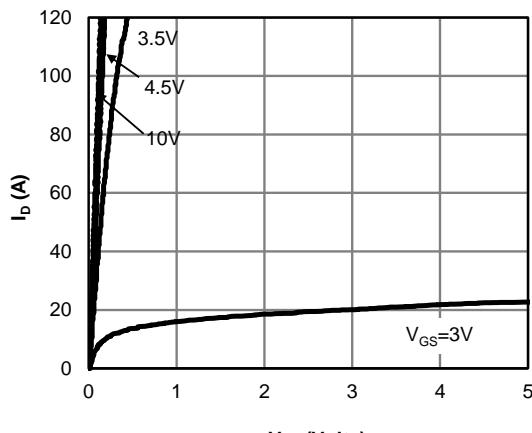
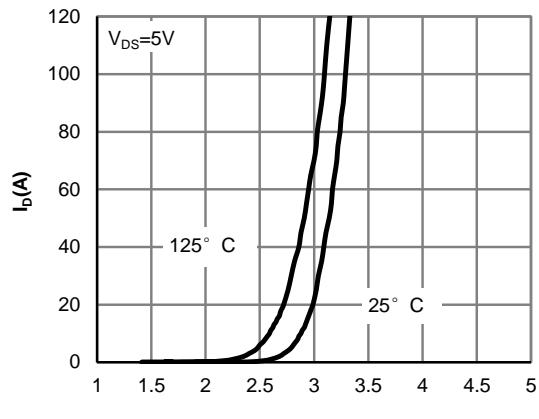
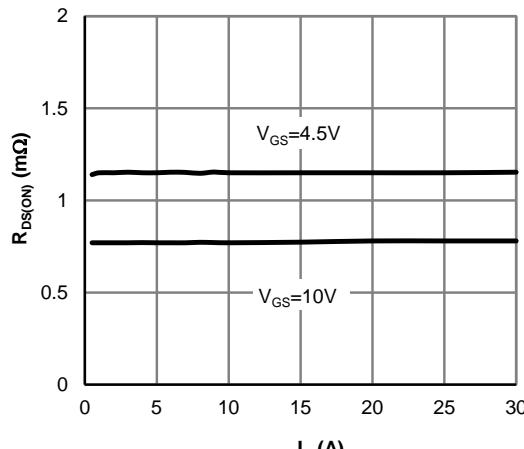
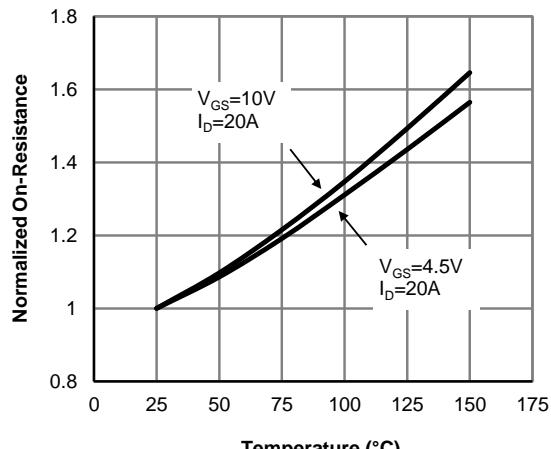
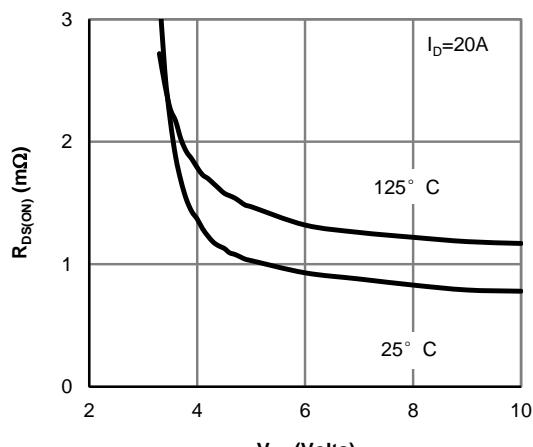
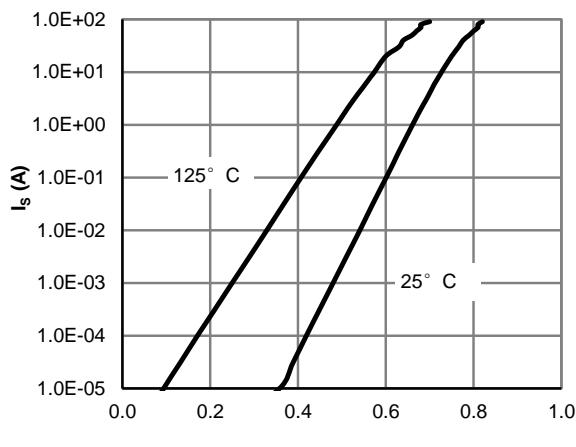
G. The maximum current rating is package limited.

H. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 1: On-Region Characteristics (Note E)

Figure 2: Transfer Characteristics (Note E)

Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

Figure 4: On-Resistance vs. Junction Temperature (Note E)

Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

Figure 6: Body-Diode Characteristics (Note E)

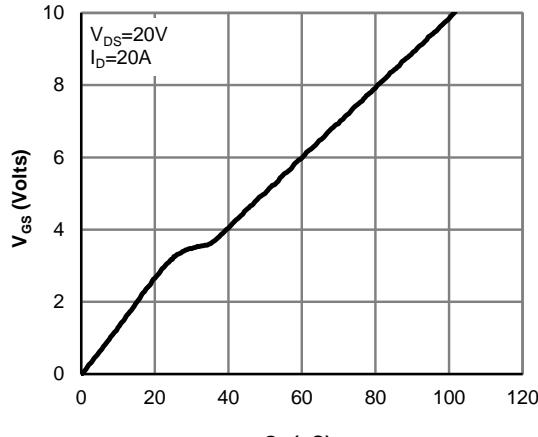
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS


Figure 7: Gate-Charge Characteristics

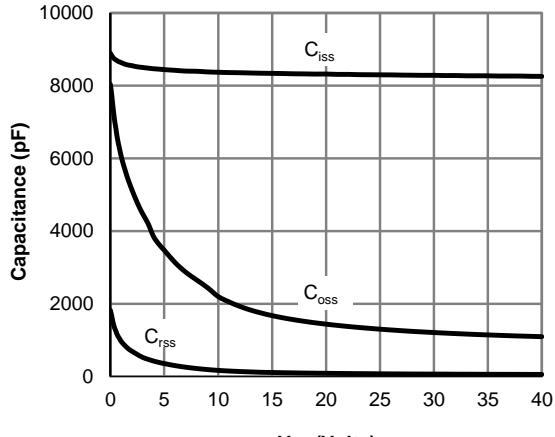


Figure 8: Capacitance Characteristics

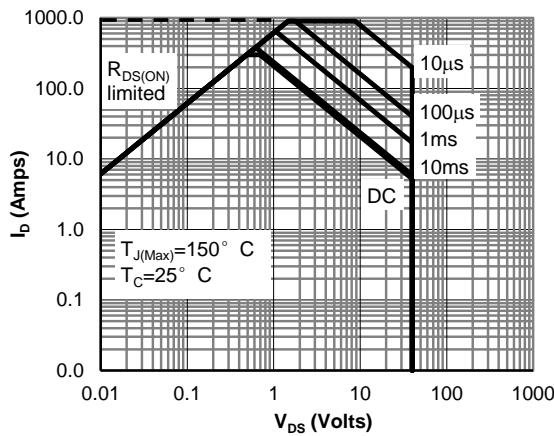


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

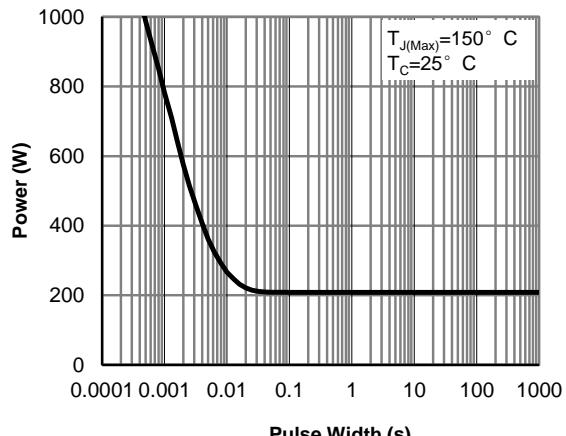
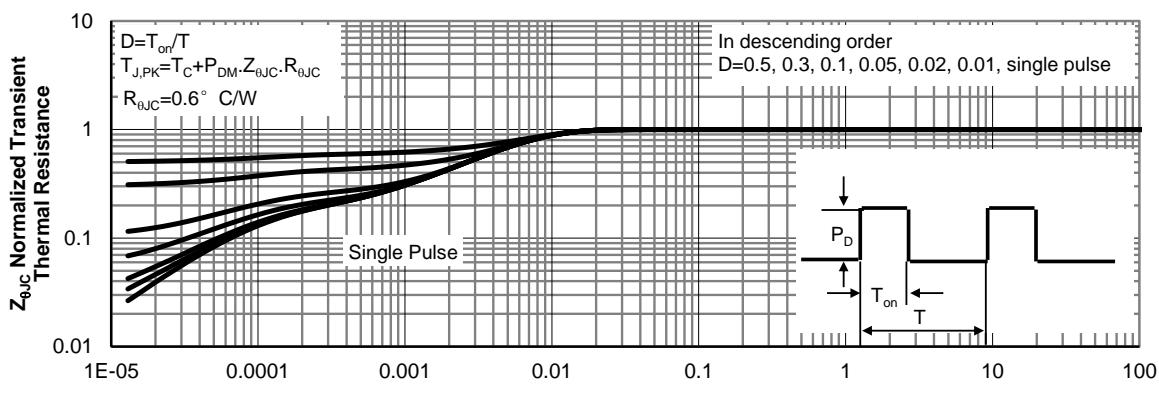
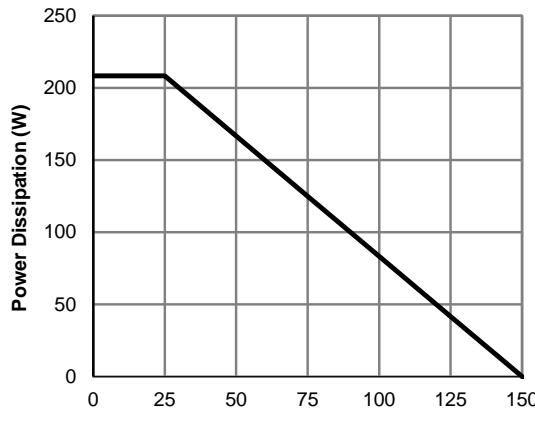
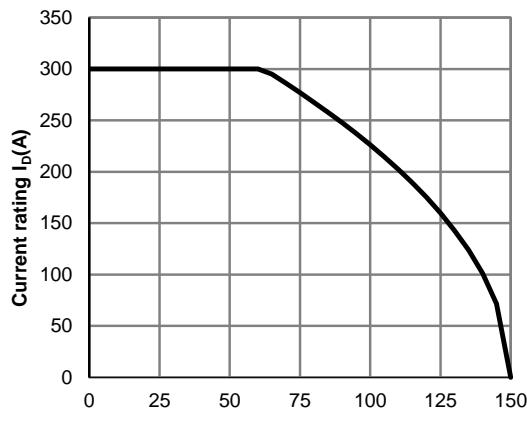
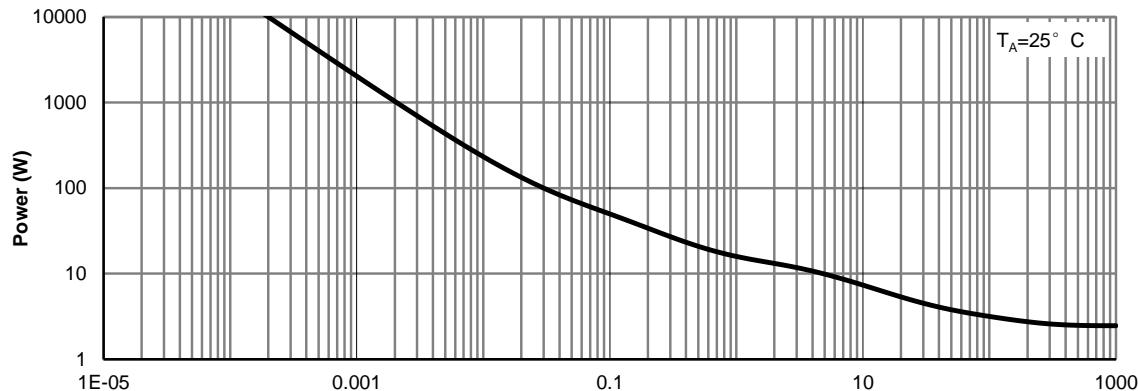
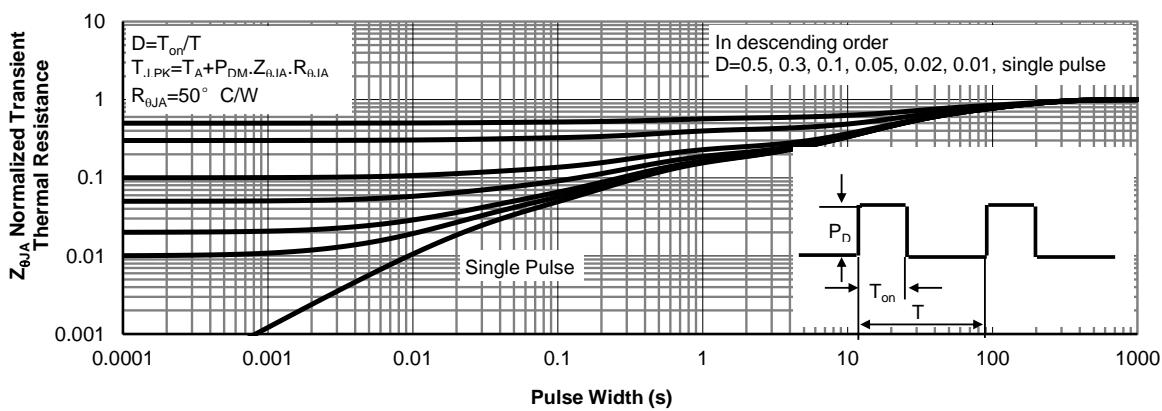
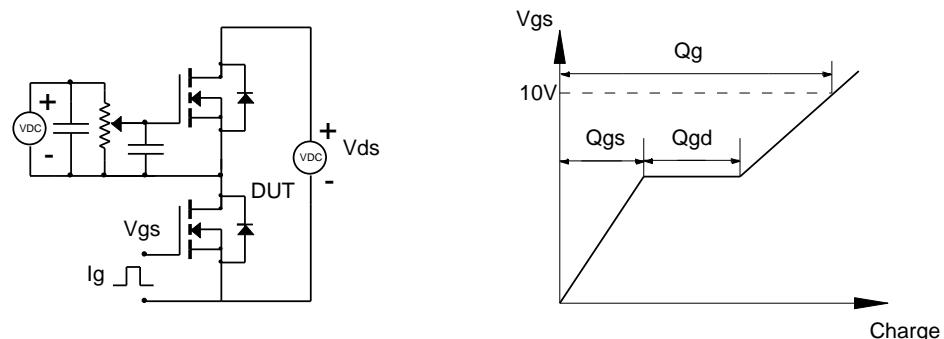
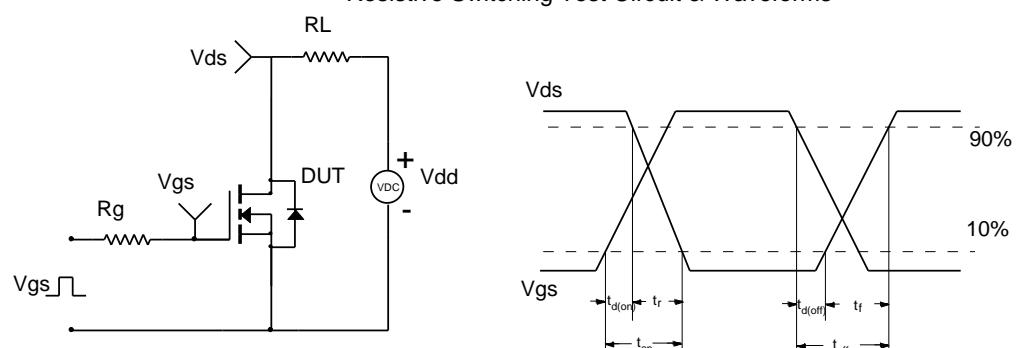
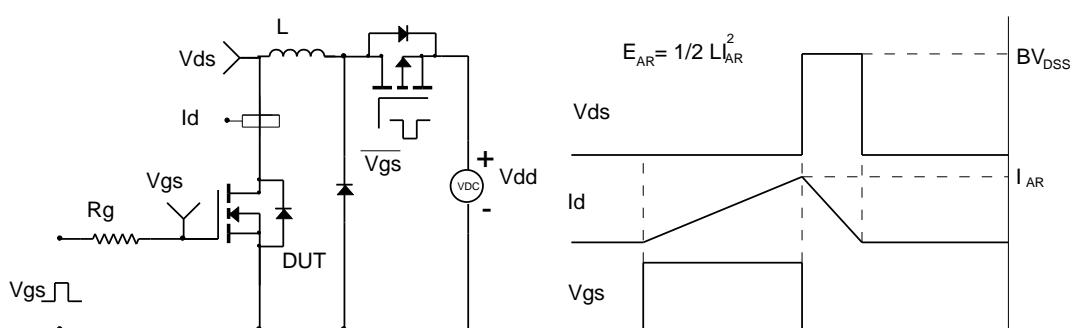
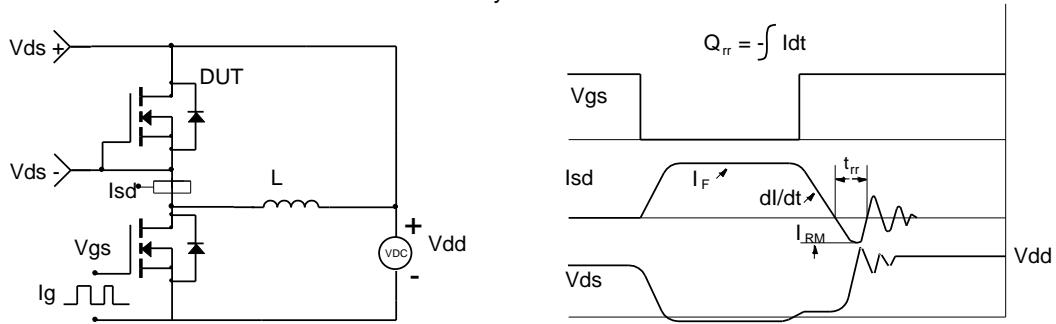


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 12: Power De-rating (Note F)

Figure 13: Current De-rating (Note F)

Figure 14: Single Pulse Power Rating Junction-to-Ambient (Note H)

Figure 15: Normalized Maximum Transient Thermal Impedance (Note H)

Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

Diode Recovery Test Circuit & Waveforms


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