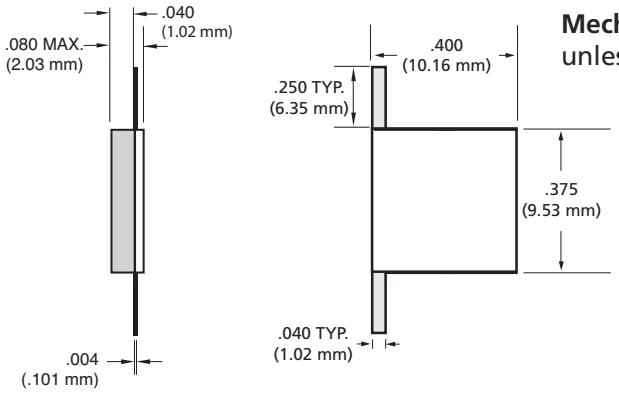
High Power Leaded Chip Attenuators

Style LA1

General Specifications

- **Frequency Range:** DC to 3 GHz
- **Input Power*:** 150 Watts
- **Operating Temp Range:** -55 to +150˚C
- **Attenuation Stability**:** 0.0001 dB/dB/°C, Max.
- **Resistive Elements:** Tantalum Nitride
- **Substrate Material:** Aluminum Nitride
- **Tabs:** 99.99% Pure Silver, .004 inches thick, Cover: Alumina
- **RoHS Compliant**
- **Reliability:** MIL-PRF-55342

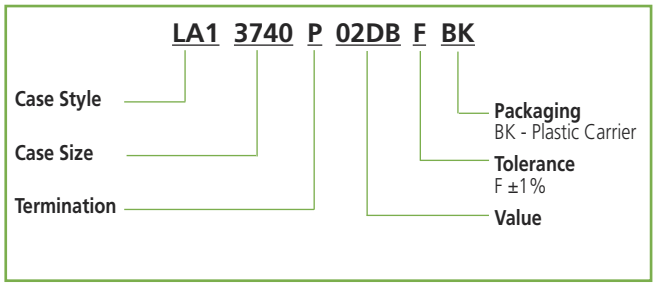


Mechanical Tolerance: ±.010 inch unless otherwise specified

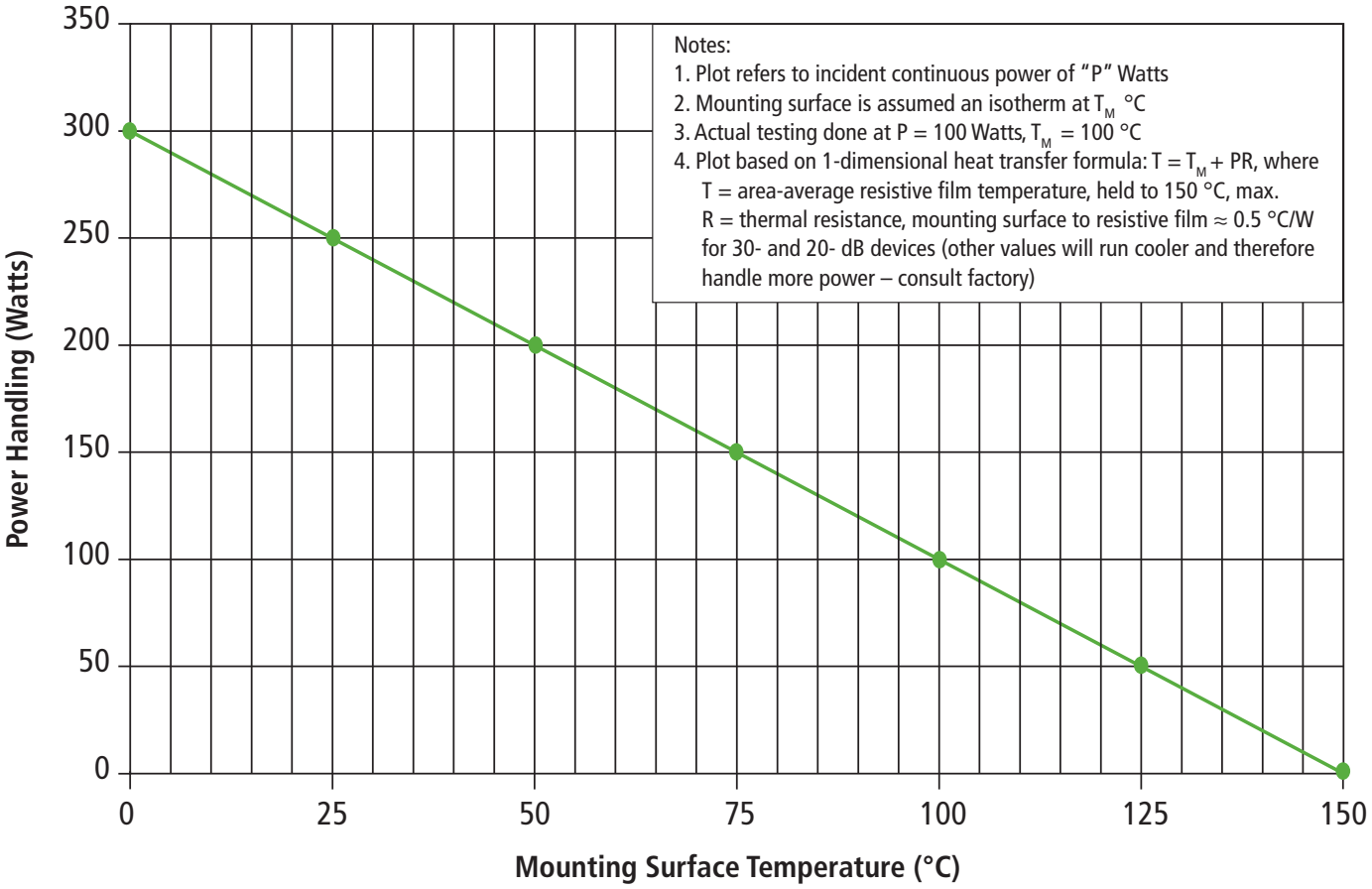
ATC Part Number	Nominal Attenuation (dB)	Frequency Sensitivity (dB, max.)	Maximum Deviation from Nominal (dB)	VSWR (max.)
LA13740P01DBFBK	1	± 0.20	+0.45 / -0.15	1.55
LA13740P02DBFBK	2	± 0.30	0.50 / -0.30	1.50
LA13740P03DBFBK	3	± 0.30	0.60 / -0.30	1.50
LA13740P06DBFBK	6	± 0.30	+0.60 / -0.20	1.30
LA13740P09DBFBK	9	± 0.30	+0.60 / -0.20	1.30
LA13740P10DBFBK	10	± 0.20	+0.40 / -0.30	1.25
LA13740P17DBFBK	17	± 1.00	+1.00 / -1.20	1.30
LA13740P20DBFBK	20	± 1.00	+1.00 / -1.20	1.25
LA13740P30DBFBK	30	± 1.05	+1.75 / -1.20	1.25

For Attenuator Power Handling vs. Mounting Surface Temperature, see following page.

ATC Leaded Chip Attenuators Part Number Code



Attenuator Power Handling vs. Mounting Surface Temperature



** Test Condition: With mounting surface temperature = 75 °C, max. (see plot above). Actual test conditions are as follows: Flange attached to a large copper carrier whose surface, directly under the flange center, is held at 100 °C; power applied = 100 Watts. Specification: The attenuation shall change no more that 0.2 dB during and after a 100-hr. Burn-in per MIL-PRF-55342.*

*** Attenuation vs. frequency as a function of temperature, -55°C to +125°C*

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