

LTC2640

Single 12-Bit SPI V_{OUT} DAC with Internal Reference

DESCRIPTION

Demonstration circuit 1333A features the [LTC®2640](#), a 12-bit SPI DAC. This device establishes a new benchmark for size and integration of 12-bit DACs and onboard reference.

The DC1333A may be connected directly to the target application's analog signals while using the DC590 USB serial controller board and supplied software to measure

performance. After evaluating with Linear Technology's software, the digital signals can be connected to the end application's processor

Design files for this circuit board are available at <http://www.linear.com/demo/DC1333A>

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DEMO BOARD VARIATIONS

DEMO BOARD TYPE	LTC2640 VARIATION	RESOLUTION (BITS)	POWER-UP	FULL-SCALE
DC1333A-A	LTC2640AITS8-LM12	12	Mid-Scale	2.5V
DC1333A-B	LTC2640AITS8-LZ12	12	Zero	2.5V
DC1333A-C	LTC2640AITS8-HM12	12	Mid-Scale	4.096V
DC1333A-D	LTC2640AITS8-HZ12	12	Zero	4.096V

BOARD PHOTO

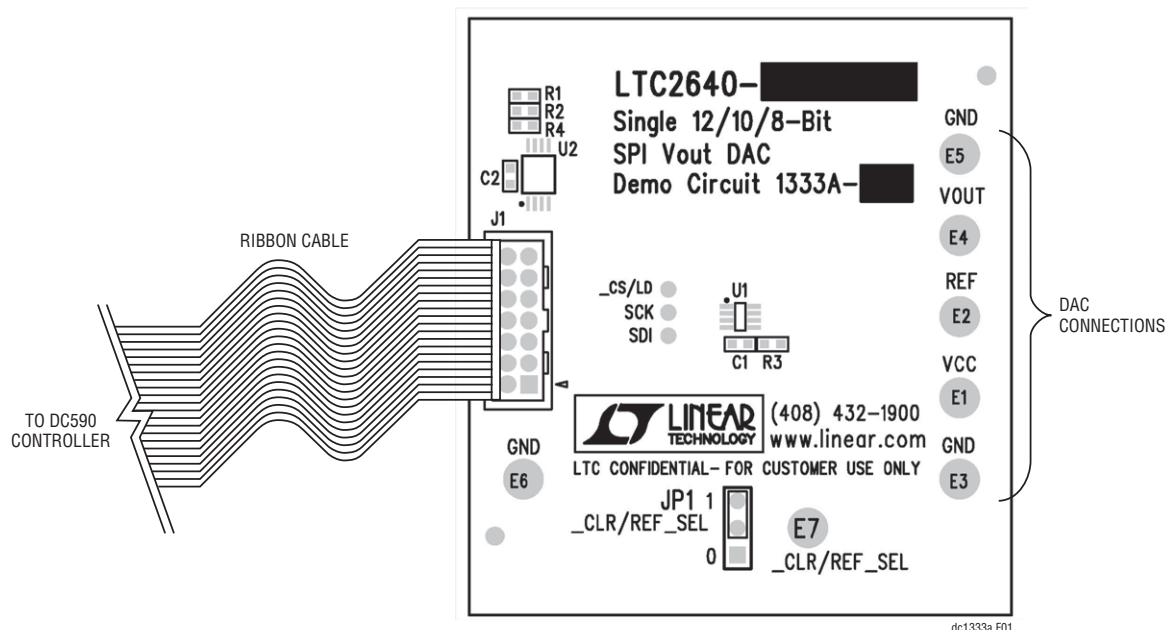


Figure 1. DC1333A Connection Diagram

QUICK START PROCEDURE

1. Connect the DC1333A to a DC590 USB serial controller using the supplied 14-conductor ribbon cable.
2. Connect the DC590 to the host PC with a standard USB A/B cable.
3. Run the QuikEval™ evaluation software supplied with the DC590 or download it from www.linear.com/software. The correct program will be loaded automatically.

Options are available to display the DAC output in voltage, hex code, or decimal count. Additionally, the reference voltage may be changed to reflect an actual measured value, such that the output voltage matches the theoretical output voltage. The reference mode may be changed from internal reference to external reference.

Features may be periodically added to the software. See the software's help menu for the latest information.

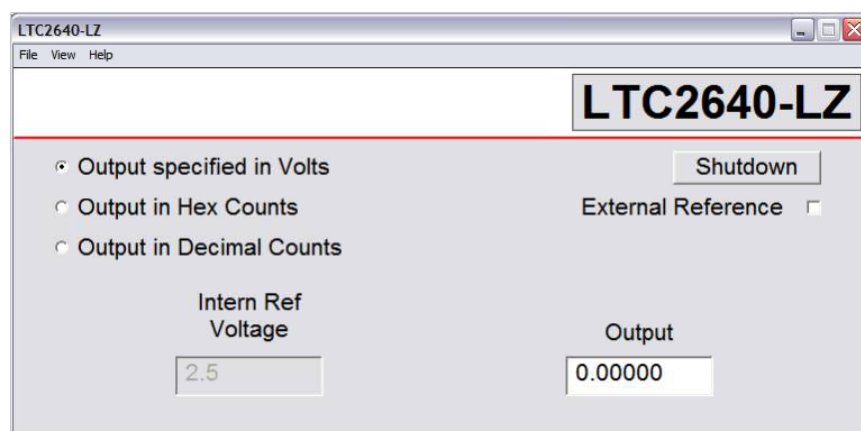


Figure 2. QuikEval Software

HARDWARE SETUP

JUMPERS

_CLR/REF_SEL: On the B and D versions of the demo board, this is not populated. On the A and C versions of the demo board, this is used to determine the reference input.

ANALOG CONNECTIONS

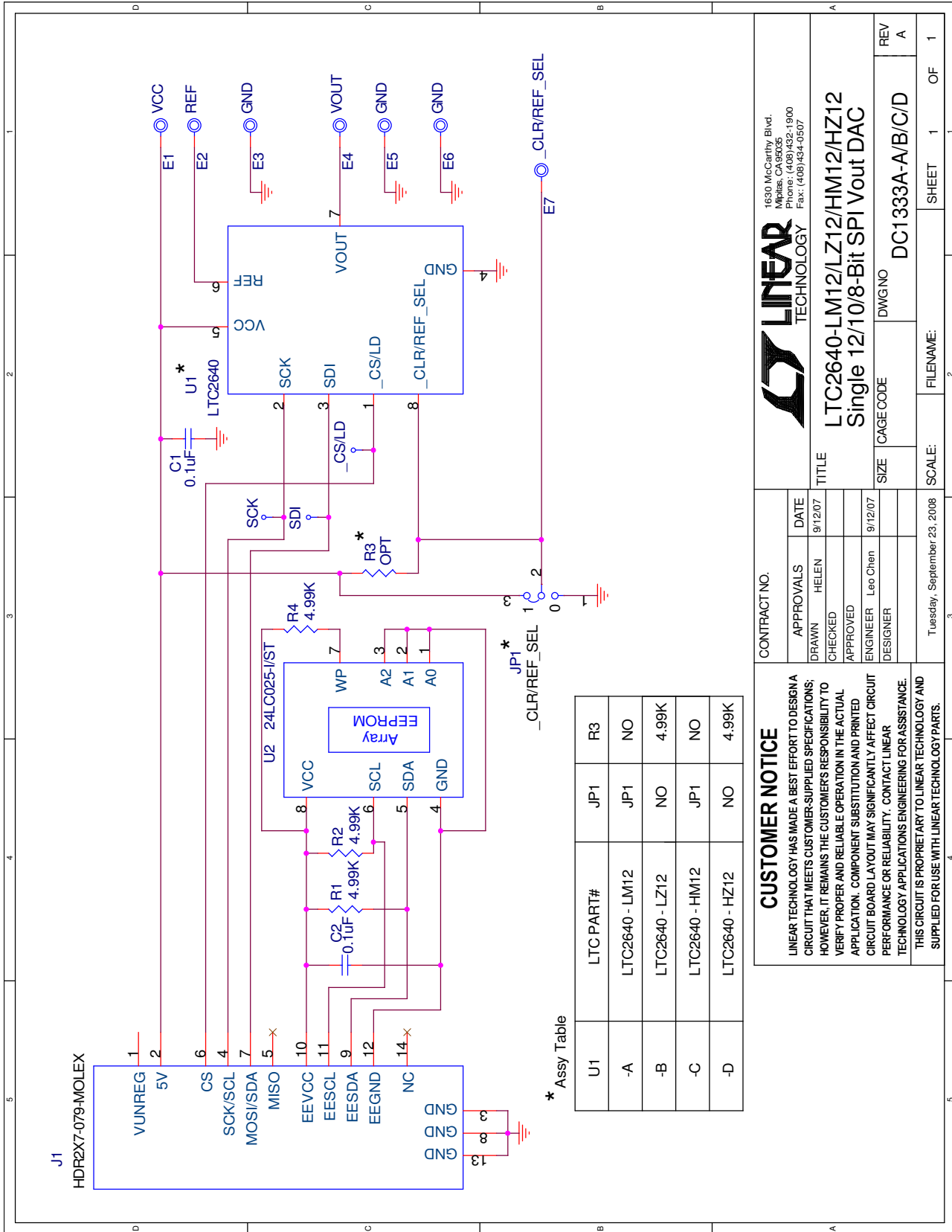
DAC outputs are provided on the row of turret posts at the edge of the board.

GROUNDING AND POWER CONNECTIONS

Power (V_{CC}): Normally, the DC1334A is powered by the DC590 controller. V_{CC} can be supplied to the 5V turret, however the power supply on the DC590 must be disabled! Refer to the DC590 Quick Start Guide for more details on this mode of operation.

Grounding: Three ground posts are provided.

SCHEMATIC DIAGRAM



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CONTRACT NO.		APPROVALS	DATE
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		CHECKED	9/12/07
		APPROVED	
		ENGINEER	Leo Chen
		DESIGNER	
TITLE		SIZE	CAGE CODE
LTC2640-LM12/LZ12/HM12/HZ12 Single 12/10/8-Bit SPI Vout DAC		DWG NO	DC1333A-A/B/C/D
SCALE:		FILENAME:	SHEET 1 OF 1
Tuesday, September 23, 2008			

Figure 3. LTC2640 Schematic Diagram

REVISION HISTORY

BOARD REV	DATE	DESCRIPTION	PAGE NUMBER
A	04/16	Corrected Part #s Removed BOM	1

DEMO MANUAL DC1333A

DEMONSTRATION BOARD IMPORTANT NOTICE

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LTC currently services a variety of customers for products around the world, and therefore this transaction **is not exclusive**.

Please read the DEMO BOARD manual prior to handling the product. Persons handling this product must have electronics training and observe good laboratory practice standards. **Common sense is encouraged.**

This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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