## DESCRIPTION

Demonstration circuit 1389A is high efficiency triple output $D C / D C$ power supply featuring the LTC3521. The IC includes a 4-switch synchronous Buck-Boost DC/DC converter with 1A switch ratings and two synchronous 600 mA Buck converters. The board will accept an input voltage between 2.4 V and 5 V , and provide fixed output voltages of $3.3 \mathrm{~V} @ 800 \mathrm{~mA}$ for the Buck-Boost converter and $1.2 \mathrm{~V} \& 1.8 \mathrm{~V} @ 600 \mathrm{~mA}$ for the Buck converters.

The board includes jumpers for shutdown for each converter, one jumper for PWM or BURST operation and terminals to monitor each converter's Power Good output signal. Other features are; $1 \mu \mathrm{~A}$ shutdown current, integrated soft start, thermal and over current protection, under voltage lockout, $100 \%$ duty cycle for the Buck converter and a 1 MHz switching frequency that allows small L and C components to be used.
With BURST mode selected, the buck converters operate in BURST mode at low load current and automatically
switch to PWM mode at higher loads. The Buck-Boost converter will also operate in burst mode but the available load current is reduced considerably, to approximately 50 mA . No load BURST mode quiescent current is approximately $50 \mu \mathrm{~A}$. When all three converters are shutdown, the quiescent current drops to less than $1 \mu \mathrm{~A}$.

The Power Good output terminals pull low if the output voltage is $8 \%$ or more below the regulation voltage.
The LTC3521 datasheet gives a complete description of the part, operation and application information. The datasheet must be read in conjunction with this quick start guide.

## Design files for this circuit board are available. Call the LTC factory.

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Performance Summary ( $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}^{\circ} \mathrm{C}$ )

| PARAMETER | CONDITION | VALUE |
| :--- | :--- | :--- |
| Input Voltage Range |  | 2.4 V to 5V |
| Buck-Boost Output Voltage (Vout 1) | Vin $=2.4 \mathrm{~V}$, Vout Load $=100 \mathrm{~mA}$ | $3.3 \mathrm{~V}+/-2.5 \%$ |
| Buck Output Voltage (Vout 2) | Vin $=2.4 \mathrm{~V}$, Vout Load $=100 \mathrm{~mA}$ | $1.2 \mathrm{~V}+/-2.5 \%$ |
| Buck Output Voltage (Vout 3) | Vin $=2.4 \mathrm{~V}$, Vout Load $=100 \mathrm{~mA}$ | $1.8 \mathrm{~V}+/-2.5 \%$ |
| Maximum Output Current (Buck-Boost) | Vin $=2.4 \mathrm{~V}$, PWM mode | 800 mA |
| Maximum Output Current (Buck) | Vin $=2.4 \mathrm{~V}$ | 600 mA |
| Input Supply Current | All Converters Shutdown, Vin = 2.4V | $<1 \mu \mathrm{~A}$ |
| Input Supply Current (no load) | All Converters Enabled, BURST Mode, Vin = 2.4V, No Load | $50 \mu \mathrm{~A}$, typ. |
| Input Supply Current (no load) | All Converters Enabled, PWM Mode, Vin $=2.4 \mathrm{~V}$, No Load | 8.5 mA, typ. |

Table 1. Circuit Specifications

## PUICK START PROCEDURE

This Demonstration circuit 1389A can be evaluated using the setup shown in Figure 1.

1. Place all Shutdown jumpers in the ON position and the MODE jumper to the PWM position.

## QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 1389A

2. With AC power off, connect the input power supply to Vin and GND.
3. Connect voltmeters to Vin and ground terminals and each Vout and Ground terminals.
4. With the input voltage turned down, switch on the input power supply and raise the input voltage to 1.8 V .
5. Verity that each output voltage meets the values as shown in Table 1.
6. Increase the input voltage to 2.5 V and connect suitable load resistors between each output and ground.
7. Verify each output voltage. If an output is low, temporarily disconnect the load to make sure that the load current is not set too high. Also, monitor the input voltage at the board terminals to verify that the input voltage has not dropped due to the increased input current. Note: The Buck-Boost converter output
current is limited to approximately 50 mA if the Mode jumper is setfor BURST.
8. When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the Vin or Vout and GND terminals. See Figure 2 for proper scope probe technique.
9. The Power Good terminals can be monitored with a voltmeter or an oscilloscope. The Power Good outputs pull low if the output voltage is $8 \%$ or more below the regulation voltage, otherwise they are pulled up to their respective output voltage through a 1 M 2 pull-up resistor.
10. To measure no load input current, connect a mA/ $/ \mathrm{A}$ meter in series with the input supply, adjust for 2.4 V and read meter. Both BURST and PWM quiescent current can be verified by moving JP1 to the appropriate position.


Figure 1. Proper Measurement Equipment Setup


Figure 2. Measuring Input or Output Voltage Ripple


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