

DEMO MANUAL DC1866A

LTM8052 36V, 5A, 2-Quadrant CVCC Step-Down µModule Regulator

DESCRIPTION

Demonstration circuit 1866 is a 36V, 5A, 2-quadrant CVCC step-down μ Module[®] regulator featuring the LTM[®]8052. The demo circuit is designed for a 2.5V output from an input voltage range of 6V to 36V. The output can source 5A or sink 5A. The circuit can be operated in either constant voltage mode or constant current mode.

When the output sinks current, the circuit maintains its output voltage regulation by power conversion, not power dissipation. This means that the energy provided to LTM8052 is in turn delivered to its input power bus. There must be something on the input power bus to accept or use the energy.

The LTM8052 data sheet gives a complete description of the part, operation and application information. The datasheet must be read in conjunction with this demo manual for demo circuit 1866.

Design files for this circuit board are available at http://www.linear.com/demo

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PERFORMANCE SUMMARY (T_A = 25°C)

PARAMETER	CONDITIONS	VALUE	UNITS
Input Supply Voltage Range		6 to 36	V
Output Voltage	I _{OUT} = 5A to -5A	2.5 ±5%	V
Switching Frequency		450	kHz
Maximum Output Current		±5	A
Typical Full-Load Efficiency	V _{IN} = 12V, I _{OUT} = 5A	84	%

BOARD PHOTO





QUICK START PROCEDURE

Demonstration circuit 1866 is an easy way to evaluate the performance of the LTM8052. Refer to Figure 2 for proper measurement equipment setup and follow the procedure below:

Note: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the $V_{\rm IN}$ and GND or $V_{\rm OUT}$ and GND terminals.

- 1. With power off, connect the input power supply to $V_{\mbox{\scriptsize IN}}$ and GND.
- 2. Connect a load to V_{OUT} and GND.
- 3. Turn on the power at the input.
- 4. Check for the proper output voltage and current.

Note: If there is no output, temporarily disconnect the load to make sure that the load is not set too high.

5. Once the proper output voltage is established, adjust the load and input within the operating ranges and observe the output voltage regulation, output current regulation, ripple voltage, efficiency and other parameters

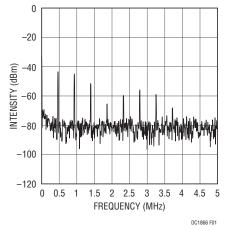


Figure 1. DC1866A Output Noise Spectrum (V_{IN} = 12V, V_{OUT} = 2.5V, I_{OUT} = 5A)



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QUICK START PROCEDURE

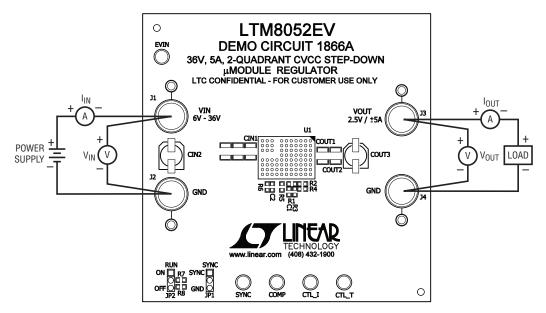


Figure 2. Proper Measurement Equipment Setup. Board Can Sink Current from the Load



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PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required	Circuit C	omponents		
1	1	CIN1	Cap., X7R 10µF 50V 10% 1210	MURATA GRM32ER71H106KA12L
2	1	CIN2	Cap., ELECTROLYTIC 22µF 50V 10%	SUN ELECT. 50CE22BSS
3	1	COUT1	Cap., X5R 100µF 6.3V 20% 1210	TDK C3225X5R0J107M
4	1	COUT3	Cap., OSCON 330µF 4V 20%	SANYO 4SVPC330M
5	1	C1	Cap., NPO 1000pF 50V 20% 0603	AVX 06035A102MAT1A
6	1	C2	Cap., X7R 1500pF 25V 10% 0603	AVX 06033C152KAT1A
7	1	R1	Res., Chip 10k 0.1W 5% 0603	VISHAY CRCW060310K0JNEA
8	2	R2, R3	Res., Chip 100k 0.1W 5% 0603	VISHAY CRCW0603100KJNEA
9	1	R4	Res., Chip 301k 0.1W 1% 0603	VISHAY CRCW0603301KFKEA
10	1	R5	Res., Chip 90.9k 0.1W 1% 0603	VISHAY CRCW060390K9FKEA
11	1	R6	Res., Chip 9.09k 0.1W 1% 0603	VISHAY CRCW06039K09FKEA
12	1	R7	Res., Chip 510k 0.1W 5% 0603	VISHAY CRCW0603510KJNEA
13	1	U1	I.C., Step-Down Voltage Regulator LGA(81) 15mm x 11.25mm x 2.82mm	LINEAR TECH. CORP. LTM8052EV
Additiona	l Demo E	Board Circuit Componen	ts	
1	0	COUT2 (OPT)	Cap., 1210	
2	0	C3 (OPT)	Cap., 1206	
3	0	C4 (OPT)	Cap., 1210	
4	0	C5 (OPT)	Cap., OSCON	
5	0	FB1 (OPT)	Ferrite Bead, 1206	
6	0	L1 (0PT)	Inductor	
7	0	R8 (OPT)	Res., 0603	
Hardware	-For Den	no Board Only		
1	5	E1, E2, E3, E8, E9	Turret, Testpoint	MILL-MAX 2501-2-00-80-00-00-07-0
2	4	E4, E5, E6, E7	Turret, Testpoint	MILL-MAX 2308-2-00-80-00-00-07-0
3	2	JP1, JP2	Headers, Single Row 3 Pins 2mm Ctrs.	SAMTEC TMM-103-02-L-S
4	4	J1, J2, J3, J4	Connector, Banana Jack	KEYSTONE 575-4
5	2	XJP1, XJP2	Shunt, 2mm Ctrs.	SAMTEC 2SN-BK-G





DATE 09/27/11 - R -36V, 5A, 2-QUADRANT CVCC STEP-DOWN μ MODULE REGULATOR MER USE ONLY 1630 McCarthy Blvd. Mipitas, CA 95035 www.linear.com Phone: (408)434-1507 TECHNOLOGY Ь 2.5V / ±5A -VOUT WALKER B. SHEET APPROVED GND 10 x @1 £R⊗ LTM8052EV DEMO CIRCUIT 1866A Tuesday, September 27, 2011 REVISION HISTORY DESCRIPTION PRODUCTION Vout 1000 € COUT2 (0PT) TITLE: SCHEMATIC COUT1 100uF 6.3V 1210 IC NO. R6 9.09k 1% size N/A DATE: h 2222222222 8 APPROVALS REV WALKER B. ч SCALE = NONE ADJ RB VOUT VOUT VOUT VOUT VOUT ECO , TUOV TUOV TUOV TUOV TUOV TUOV TUOV TUOV PCB DES. APP ENG. R1 83 83 81 82 82 82 82 82 82 82 82 HOWEVER, IT REMAND THE CONCENTRATES DECIFICATIONS: VEHICY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SIRESTITION OF COMPONENT SIRESTITICO SIRESTITICO SIRESTITICO SIRESTITICO SIRESTITICO SIRESTINO S PRINTED FECT CIRCUIT CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS. TO DESIGN C:\PADS PROJECTS\1866A(LTM8052EV)\SCH\1866A_REV1.DSN ^{2%} **CUSTOMER NOTICE** LINEAR TECHNOLOGY HAS MADE A BEST CIRCUIT THAT MEETS CUSTOMER-SUPPLI HOWEVER, IT REMAINS THE CUSTOMER'S Ľ©--170 U1 LTM8052EV Śğ ß VREF 1000pF ü zζ 10k Чī т_@ כור"ו 80 집 Ì릴 կլ COMP © E1 COME 81 ъ CNV: SUN NO Ĕ NN +++(-+ģ ß C2 =1500pF **Optional Circuit** CIN1 100F 50V 1210 Ξ R5 90.9k 1% R8 (OPT) C4 1210 CIN2 22uF 50V 1206 BI 510k 2 ₹ŀ N۷ EVIN 1206 1206 ž l l VIN 6V - 36V SYNCOB © [] RUN £2 ⊚ 6 Τđ GND (SYNC EVIN GND OFF NO

SCHEMATIC DIAGRAM



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This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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