# LTC3649 60V, Low IQ Monolithic High Efficiency Step-Down Regulator 

## DESCRIPTIOn

Demonstration circuit 2112A is a high input voltage, high efficiency synchronous monolithic buck converter featuring the LTC3649 in a 28 -lead UFD package. The DC2112A has wide input voltage range from 3.1 V up to 60 V . The output voltage of the DC2112A can be set as to 3.3 V or 5 V . However, the "USER SELECT" option of DC2112A allows output voltage to be as high as input voltage minus 0.5 V , with certain modifications. DC2112A is capable of delivering up to 4A of output current. DC2112A supports three operation modes: Fixed-Frequency modulation and Burst Mode, user can synchronize it with an external clock also. Fixed-Frequency mode of operation maximizes the output current, reduces output voltage ripple, and yields a low noise switching spectrum. Burst Mode employs a variable frequency switching algorithm that minimizes the no-load input quiescent current and improves efficiency at light loads.

The DC2112A consumes less than $15 \mu \mathrm{~A}$ of quiescent current during shutdown and it consumes less than $440 \mu \mathrm{~A}$ at no load conditions in Burst Mode of operation. The DC2112A has a standard operating frequency of 500 kHz , but can be adjusted in a range between 300 kHz and as high as 3MHz. DC2112A is a monolithic step-down converter, LTC3649 integrates top and bottom N-channel MOSFETs, significantly reducing circuit footprint. DC2112A was designed to support multiple footprints of input/output capacitors and inductor to accommodate variety of applications. The data sheet of LTC3649 gives a complete description functionality of this regulator; also contains operation and application information and must be read in conjunction with this demo board manual for DC2112A.
Design files for this circuit board are available at http://www.linear.com/demo/DC2112A

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## PERFORMANCE SUMMARY Specifications are at $T_{A}=25^{\circ} \mathrm{C}$

| PARAMETER | CONDITIONS | VALUE |
| :---: | :---: | :---: |
| Minimum Input Voltage |  | 4V |
| Maximum Input Voltage |  | 60 V |
| Output Voltage $\mathrm{V}_{\text {OUT }}$ Regulation | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}-60 \mathrm{~V}$ | $5 \mathrm{~V} \pm 2 \%$ or $3.3 \mathrm{~V} \pm 2 \%$ |
| Maximum Continuous Output Current | $V_{\text {OUT }}$ | 4A |
| Preset Operating Frequency | $\mathrm{R} 10=200 \mathrm{k} \Omega$ | 500 kHz |
| External Clock Sync. Frequency Range |  | $300 \mathrm{kHz}-3 \mathrm{MHz}$ |
| Efficiency | $\begin{aligned} & V_{\text {IN }}=12 \mathrm{~V}, V_{\text {OUT }}=5 \mathrm{~V} \\ & V_{\text {IN }}=12 \mathrm{~V}, V_{\text {OUT }}=3.3 \mathrm{~V} \end{aligned}$ | Up to 95\% Up to 95\% |
| Typical Output Ripple $\mathrm{V}_{\text {OUT }}$ | $V_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=4 \mathrm{~A}(2 \mathrm{MHz} \mathrm{BW})$ | <15mVP-p |
| Quiescent Current at Shutdown | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}-60 \mathrm{~V}$ | $<14 \mu \mathrm{~A}$ |
| Input Current at No Load | $\mathrm{V}_{\text {IN }}=4 \mathrm{~V}-60 \mathrm{~V}$, Burst Mode | <430 A |

## DEMO MANUAL DC2112A

## QUICK START PROCEDURE

Demonstration circuit 2112A is easy to set up to evaluate the performance of the LTC3649. For proper measurement equipment configuration, set up the circuit according to the diagram in Figure 1. Before proceeding to test, insert shunt into JP2 (RUN) into OFF position, which connects the RUN pin to ground (GND), and thus, shutdown the output. Set jumper JP1 (MODE) into FCC (Forced Counties Conduction Mode) position. Set jumper JP3 (VOUT) into 5.0V position.

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the $\mathrm{V}_{\text {IN }}$ or $V_{\text {OUt }}$ and GND terminals. See Figure 2 for proper scope probe technique.

1. With the DC2112A set up according to the proper measurement and equipment in Figure 1, apply 12 V at $\mathrm{V}_{\text {IN }}$. Measure $\mathrm{V}_{\text {OUT }}$; it should read OV. If desired, one can measure the shutdown supply current at this point. The supply current will be approximately $14 \mu \mathrm{~A}$, or less, in shutdown.
2. Turn on $\mathrm{V}_{\text {OUT }}$ of the circuit by inserting the shunt in header JP2 (RUN) into the ON position. The output
voltage should be regulating. Measure $\mathrm{V}_{\text {OUT }}{ }^{-}$it should measure $5.0 \mathrm{~V} \pm 2 \%$ (Do not apply more than the rated maximum voltage of 60 V to the board or the part may be damaged). Vary the $\mathrm{V}_{\text {OUT }}$ load, which should not exceed 4A. Vary the input voltage from 6 V to 55 V , the $V_{\text {OUT }}{ }^{-}$it should measure $5.0 \mathrm{~V} \pm 2 \%$.
3. Set JP2 (RUN) into OFF and then jumper JP3 (V $\mathrm{V}_{\text {OUT }}$ ) into 3.3V position.
4. Turn on $\mathrm{V}_{\text {OUT2 }}$ of the circuit by inserting the shunt in header JP2 (RUN) into the ON position. The output voltage should be regulating. Measure $\mathrm{V}_{\text {OUT }}{ }^{-}$it should measure $3.3 \mathrm{~V} \pm 2 \%$ (Do not apply more than the rated maximum voltage of 60 V to the board or the part may be damaged). Vary the $\mathrm{V}_{\text {Out }}$ load, which should not exceed 4A. Vary the input voltage from 16-55V, the $V_{\text {OUT }}{ }^{-}$it should measure $3.3 \mathrm{~V} \pm 2 \%$
5. Set output current to zero and move jumper JP1 (MODE) into BURST position and measure $\mathrm{V}_{\text {OUT }}$ for 3.3 V .
6. Set output current to zero and move jumper JP1 (MODE) into BURST position and measure $\mathrm{V}_{\text {OUT }}$ for 5.0 V .


Figure 1. Proper Measurement Equipment Setup

## PUICK START PROCEDURE



Figure 2. Measuring Input or Output Ripple


Figure 3. Efficiency vs Input Voltage and Load Current, Vout 5 V , Burst Mode Operation


Figure 4. Thermal Map, $\mathrm{V}_{\mathrm{IN}} 14 \mathrm{~V}, \mathrm{I}_{0 \mathrm{UT}} 15 \mathrm{~V}$ at 4 A , No Air Flow

## DEMO MANUAL DC2112A

## PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :---: | :---: | :---: |
| Required Circuit Components |  |  |  |  |
| 1 | 2 | CIN1, CIN2 | CAP, 1210 10⿲F 10\% 63V X7R | MURATA GRM32ER71J106KA12L |
| 2 | 1 | CIN3 | CAP, 10 $10 \mathrm{~F} 20 \% 63 \mathrm{~V}$ ELEC | SUN ELECT. 63CE10KX |
| 3 | 2 | CIN4, C7 | CAP, $08051 \mu \mathrm{~F} 10 \% 100 \mathrm{~V}$ X7S | TDK C2012X7S2A105K |
| 4 | 2 | COUT1, COUT2 | CAP, $121022 \mu \mathrm{~F} 10 \% 25 \mathrm{~V}$ X7R | MURATA GRM32ER71E226KE15L |
| 5 | 1 | COUT3 | CAP, 3528 150uF 20\% 6.3V POSCAP | PANASONIC 6TPE150MAZB |
| 6 | 1 | CTH | CAP, 0603 4700pF 10\% 25V X7R | AVX 06033C472KAT2A |
| 7 | 1 | CTHP | CAP, 0603 10pF 5\% 25V COG | AVX 06033A100JAT2A |
| 8 | 1 | CVCC | CAP, 0805 2.2 $\mu \mathrm{F} 10 \% 16 \mathrm{~V}$ X7R | AVX 0805YC225KAT2A |
| 9 | 1 | C1 | CAP, 0603 0.01 1 F 10\% 50V X7R | AVX 06035C103KAT4A |
| 10 | 1 | C5 | CAP, $06031 \mu \mathrm{~F} 10 \% 16 \mathrm{~V}$ X 5 R | AVX 0603YD105KAT2A |
| 11 | 1 | C6 | CAP, $06030.1 \mu \mathrm{~F} 10 \%$ 50V X7R | AVX 06035C104KAT2A |
| 12 | 1 | L1 | IND, $5.6 \mu \mathrm{H}$ | COILCRAFT XAL5050-562MEB |
| 13 | 1 | RIMON | RES, 0603 10k $21 \%$ 1/10W | VISHAY CRCW060310K0FKEA |
| 14 | 1 | RTH | RES, $06031.0 \mathrm{k} \Omega 1 \% 1 / 10 \mathrm{~W}$ | VISHAY CRCW06031K00FKEA |
| 15 | 1 | R2 | RES, 0603 10M $1 \% 1 / 10 \mathrm{~W}$ | VISHAY CRCW060310MOFKEA |
| 16 | 1 | R3 | RES, 0603 100k $\Omega$ 1\% 1/10W | VISHAY CRCW0603100KFKEA |
| 17 | 1 | R5 | RES, 0603 10k 5 \% 1/10W | VISHAY CRCW060310KOJNEA |
| 18 | 1 | R10 | RES, 0603 340k $\Omega$ 1\% 1/10W | VISHAY CRCW0603340KFKEA |
| 19 | 1 | R13 | RES, 0603 100k 3 \% 1/10W | VISHAY CRCW0603100KJNEA |
| 20 | 1 | R14 | RES, $06030 \Omega$ JUMPER | VISHAY CRCW06030000ZOEA |
|  | 1 | R17 | RES, $0603100 \Omega 1 \% 1 / 10 \mathrm{~W}$ | VISHAY CRCW0603100RFKEA |
| 21 | 1 | R18 | RES, $08051 \mathrm{M} \Omega 1 \% 1 / 8 \mathrm{~W}$ | VISHAY CRCW08051M00FKEA |
| 22 | 1 | R25 | RES, $06031 \mathrm{M} \Omega$ 1\% 1/10W | VISHAY CRCW06031M00FKEA |
|  | 1 | R27 | RES, $060310 \Omega 1 \% 0.1 \mathrm{~W}$ | VISHAY CRCW060310ROFKEA |
| 23 | 1 | R26 | RES, 0603 137k $1 \% 0.063 \mathrm{~W}$ | VISHAY CRCW0603137KFKEA |
| 24 | 1 | R28 | RES, 0603 196k $\Omega$ 1\% 1/10W | VISHAY CRCW0603196KFKEA |
| 25 | 1 | U1 | IC, SYNCHRONOUS BUCK REGULATOR | LINEAR TECH. LTC3649EUFD\#PBF |

Additional Demo Board Circuit Components


## Hardware: For Demo Board Only

|  | 11 | E1, E2, E3, E4, E5, E6, E7, E8, E9, <br> E10, E12 | TURRET | MILL MAX 2501-2-00-80-00-00-07-0 |
| :--- | :---: | :--- | :--- | :--- |
|  | 3 | JP1, JP2, JP3 | HEADER, 3-PIN, DBL ROW 2mm | SULLINS, NRPN032PAEN |
|  | 4 | MH1, MH2, MH3, MH4 | STANDOFF, SNAP ON | KEYSTONE 8833 |
|  | 3 | XJP1, XJP2, XJP3 | SHUNT | SAMTEC 2SN-BK-G |

## SCHEMATIC DIAGRAM


ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection
circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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