

DESCRIPTION

Demonstration circuit 801 is a general purpose positive to negative converter featuring the LT[®]3483ES6. The demo board demonstrates two separate DC/DC converters. One converter generates a -8V at 20mA to 30mA from a 2.7V-4.2V input using a step up inverting charge pump topology. The other uses a dual coupled inductor in an inverting flyback topology to generate a -15V at 8mA to 40mA from a 2.7-12V input. This topology is useful to generate output voltages of lower, equal or higher magnitude than the input. For example, for a -12V supply just change the

value of R2 to 1.21M and for a -5V, change R2 to 511K. Both circuits demonstrate low component count and are intended for space-conscious applications such as LCD bias, Handheld Computers, Battery Backup, Digital Cameras and OLED Bias.

Design files for this circuit board are available. Call the LTC factory.

LTC is a registered trademark of Linear Technology Corporation

Table 1. Performance Summary (T_A = 25°C)

PARAMETERS FOR -8V INVERTER	CONDITION	VALUE
Minimum Input Voltage		2.7V
Maximum Input Voltage		4.2V
Output Voltage V _{OUT}	V _{IN} = 2.5V to 4.2V, I _{OUT} = 20mA to 30mA	-8V ±3%
Typical efficiency	V _{IN} = 3.3V, I _{OUT} = 25mA	67%
Typical Output Ripple	V _{IN} = 3.3V, I _{OUT} = 25mA	60mV _{P-P}
PARAMETERS FOR -15V INVERTER		VALUE
Minimum Input Voltage		2.7V
Maximum Input Voltage		12V
Output Voltage V _{OUT}	V _{IN} = 2.7V to 12V, I _{OUT} = 8mA to 40mA	-15V ±3%
Typical efficiency	V _{IN} = 6V, I _{OUT} = 20mA	75%
Typical Output Ripple V _{OUT}	V _{IN} = 6V, I _{OUT} = 20mA	80mV _{P-P}

QUICK START PROCEDURE

Demonstration circuit 801 is easy to set up to evaluate the performance of the LT3483ES6. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead

on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the V_{in} or V_{out} and GND terminals. See Figure 2 for proper scope probe technique.

1. Place jumper in the following positions for testing of -15V inverter:

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 801

INVERTING DC/DC CONVERTER

JP1 On

2. With power off, connect the input power supply to Vin and GND.
3. Turn on the power at the input.
4. Check for the proper output voltages.

NOTE: If there is no output, temporarily disconnect the load to make sure that the load is not set too high.

5. Once the proper output voltages are established, adjust the loads within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other parameters.
6. For testing of -8V Inverter repeat steps 1-5 placing **JP2** in the **ON** position.

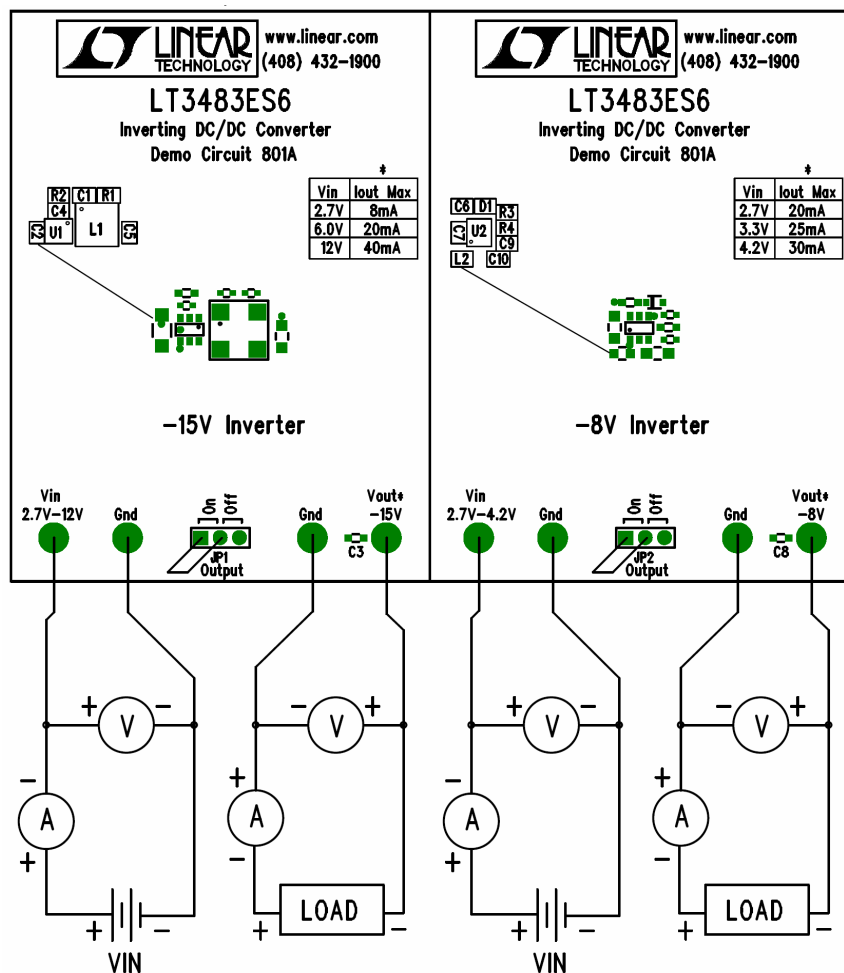


Figure 1. Proper Measurement Equipment Setup

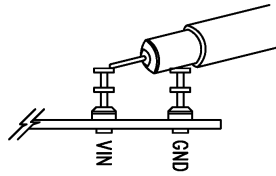
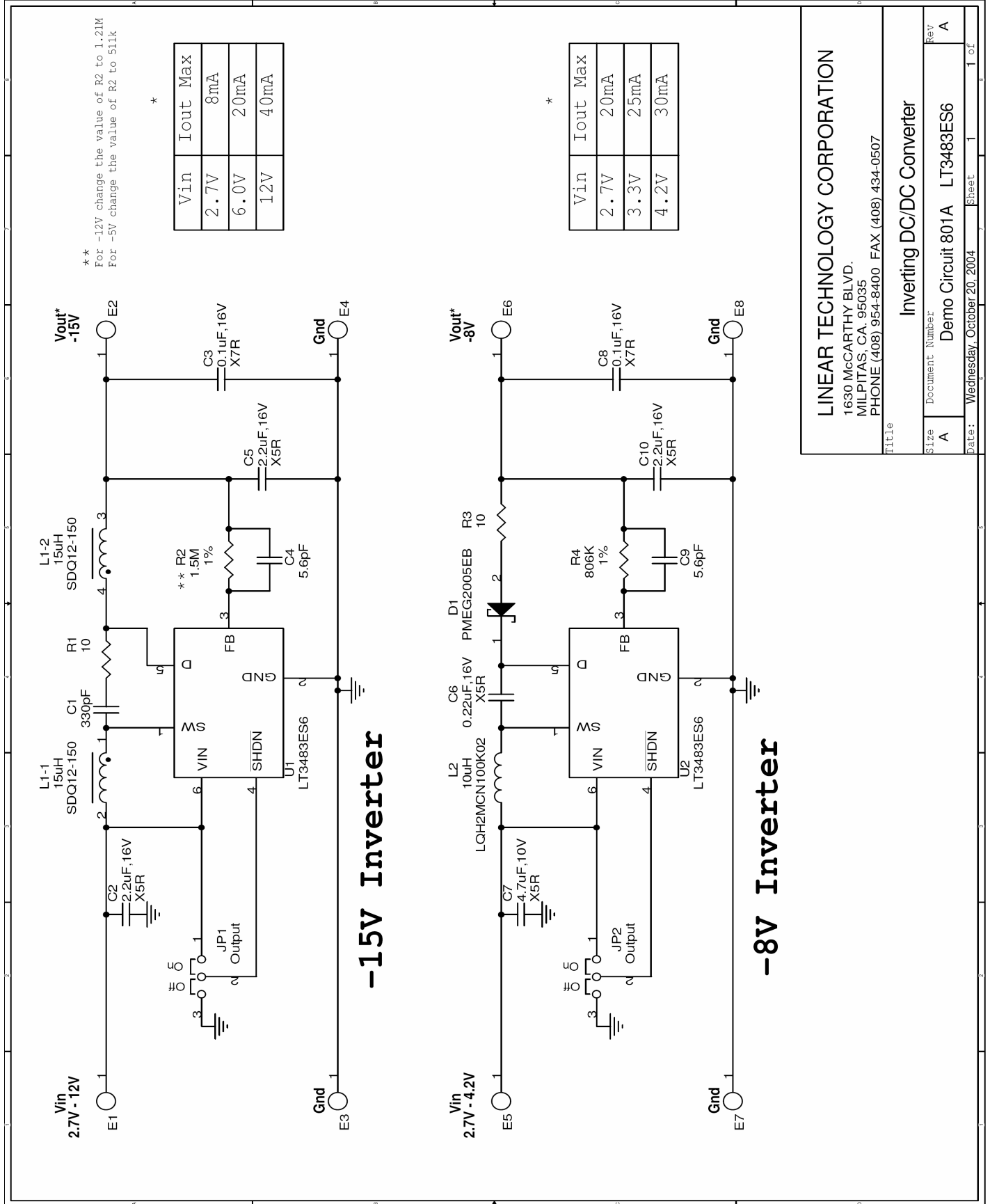


Figure 2. Measuring Input or Output Ripple

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 801 INVERTING DC/DC CONVERTER



** For -12V change the value of R2 to 1.21M
For -5V change the value of R2 to 511k

*

Vin	Iout Max
2.7V	8mA
6.0V	20mA
12V	40mA

*

Vin	Iout Max
2.7V	20mA
3.3V	25mA
4.2V	30mA

LINEAR TECHNOLOGY CORPORATION
 1630 MCCARTHY BLVD.
 MILPITAS, CA. 95035
 PHONE (408) 954-8400 FAX (408) 434-0507

Title: Inverting DC/DC Converter

Size	Document Number	Rev
A	Demo Circuit 801A	A

Date: Wednesday, October 20, 2004 Sheet 1 of 1

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Power Management IC Development Tools](#) category:

Click to view products by [Analog Devices](#) manufacturer:

Other Similar products are found below :

[EVAL-ADM1168LQEBZ](#) [EVB-EP5348UI](#) [MIC23451-AAAYFL EV](#) [MIC5281YMME EV](#) [DA9063-EVAL](#) [ADP122-3.3-EVALZ](#) [ADP130-0.8-EVALZ](#) [ADP130-1.2-EVALZ](#) [ADP130-1.5-EVALZ](#) [ADP130-1.8-EVALZ](#) [ADP1714-3.3-EVALZ](#) [ADP1716-2.5-EVALZ](#) [ADP1740-1.5-EVALZ](#) [ADP1752-1.5-EVALZ](#) [ADP1828LC-EVALZ](#) [ADP1870-0.3-EVALZ](#) [ADP1871-0.6-EVALZ](#) [ADP1873-0.6-EVALZ](#) [ADP1874-0.3-EVALZ](#) [ADP1882-1.0-EVALZ](#) [ADP199CB-EVALZ](#) [ADP2102-1.25-EVALZ](#) [ADP2102-1.875EVALZ](#) [ADP2102-1.8-EVALZ](#) [ADP2102-2-EVALZ](#) [ADP2102-3-EVALZ](#) [ADP2102-4-EVALZ](#) [ADP2106-1.8-EVALZ](#) [ADP2147CB-110EVALZ](#) [AS3606-DB](#) [BQ24010EVM](#) [BQ24075TEVM](#) [BQ24155EVM](#) [BQ24157EVM-697](#) [BQ24160EVM-742](#) [BQ24296MEVM-655](#) [BQ25010EVM](#) [BQ3055EVM](#) [NCV891330PD50GEVB](#) [ISLUSBI2CKIT1Z](#) [LM2744EVAL](#) [LM2854EVAL](#) [LM3658SD-AEV/NOPB](#) [LM3658SDEV/NOPB](#) [LM3691TL-1.8EV/NOPB](#) [LM4510SDEV/NOPB](#) [LM5033SD-EVAL](#) [LP38512TS-1.8EV](#) [EVAL-ADM1186-1MBZ](#) [EVAL-ADM1186-2MBZ](#)