

8					7					6					5					4					3					2					1				
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RELAY CONTROL CHART																																							
CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																																			

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	XX/XX	X.X.

A

B

C

D

A

B

C

D

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER																				

8 7 6 5 4 3 2 1

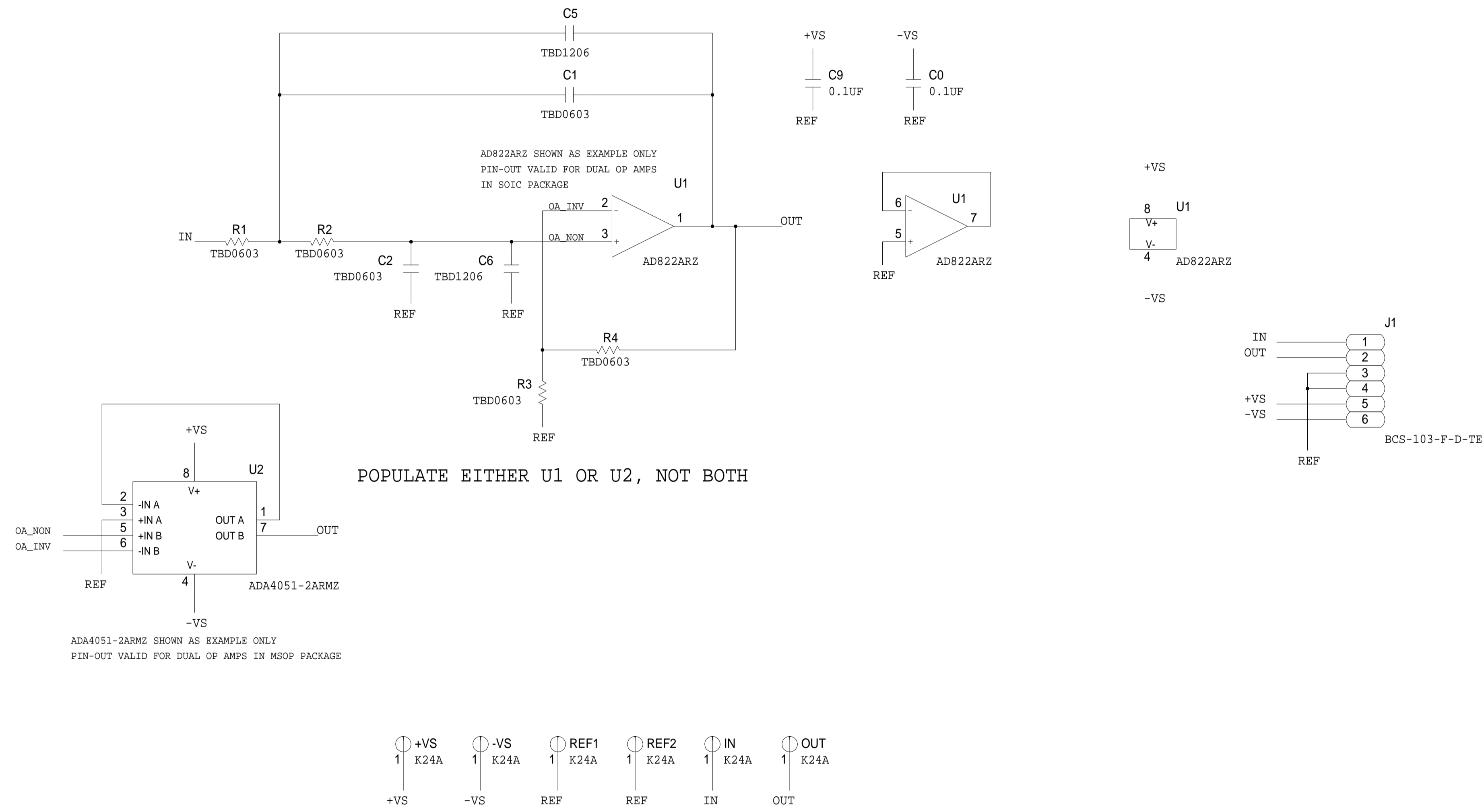
TEMPLATE ENGINEER	DATE	<h2 style="margin: 0;">SCHEMATIC</h2>			
HARDWARE SERVICES					
HARDWARE SYSTEMS					
TEST ENGINEER					
COMPONENT ENGINEER					
TEST PROCESS					
HARDWARE RELEASE		EVAL BOARD SALLEY KEY LOW PASS DUAL <PRODUCT_1> <DRAWING_TITLE1> <DRAWING_TITLE2> <DRAWING_TITLE3> <DRAWING_TITLE4> <DRAWING_TITLE5>			
DESIGNER LABRANCHE		MASTER PROJECT TEMPLATE <DRAWING_TITLE6>	TESTER TEMPLATE -	DRAWING NO. 02-034897	REV. A
PTD ENGINEER <PTD_ENGINEER>		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES			
CHECKER		DECIMALS FRACTIONS ANGLES X.XX +-0.010 +-1/32 +-2 X.XXX +-0.005	SIZE D	SCALE <SCALE>-	CODE ID NO. SHEET 1 OF 2

8 7 6 5 4 3 2 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

SALLEN KEY LOW PASS FILTER STAGE FOR DUAL OP AMPS
 CAN ALSO BE USED TO CREATE FIRST ORDER LOW PASS STAGE

TO ALLOW USE OF LARGER VALUE COG/NPO CAPACITORS,
 BOARD INCLUDES 1206 PACKAGE OPTION



	SCHEMATIC		
	EVAL BOARD SALLEN KEY LOW PASS DUAL <PRODUCT_1>		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-034897	REV A
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 2 OF 2

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