

HMC194AMS8 / 194AMS8E

ROHS

GaAs MMIC SPDT SWITCH DC - 3 GHz

Typical Applications

The HMC194AMS8 /HMC194AMS8E is ideal for:

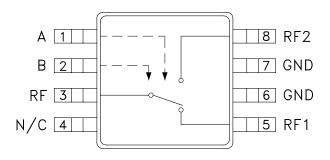
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- Cellular/PCS Base Stations
- Portable Wireless
- MMDS & WirelessLAN

Features

Ultra Small Package: MSOP8 High Isolation: 50 dB Positive Control: 0/+3V to 0/+7V

Functional Diagram



General Description

The HMC194AMS8 & HMC194AMS8E are low-cost SPDT switches in 8-lead MSOP packages for use in applications which require high isolation between two RF paths. The devices can control signals from DC to 3 GHz and have been optimized to provide extremely high isolation with minimal insertion loss in medium and low power applications. On chip circuitry allows positive voltage control operation at very low DC currents with control inputs compatible with CMOS and most TTL logic families. RF1 and RF2 are reflective opens when "OFF".

Electrical Specifications, $T_A = +25^{\circ}$ C, Vctl = 0/+5 Vdc, 50 Ohm System

Parameter	Frequency	Min.	Тур.	Max.	Units
Insertion Loss	DC - 2.0 GHz DC - 2.5 GHz DC - 3.0 GHz		0.5 0.5 0.5	0.9 1.0 1.1	dB dB dB
Isolation	DC - 1.0 GHz DC - 2.0 GHz DC - 2.5 GHz DC - 3.0 GHz	50 42 31 24	55 45 38 30		dB dB dB dB
Return Loss	DC - 2.0 GHz DC - 3.0 GHz		26 24		dB dB
Input Power for 1 dB Compression 0/+5V Control	0.5 - 3.0 GHz	24	28		dBm
Input Third Order Intercept (Two-tone Input Power = +7 dBm Each Tone) 0/+5V Control	0.5 - 3.0 GHz	49	53		dBm
Switching Characteristics	DC - 3.0 GHz				
tRISE, tFALL (10/90% RF) tON , tOFF (50% CTL to 10/90% RF)			3 20		ns ns

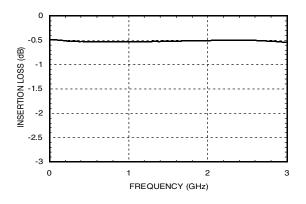
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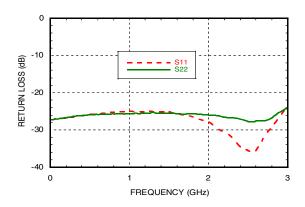
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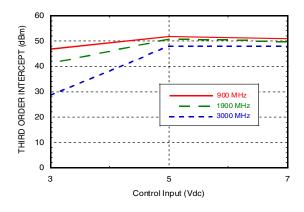
Insertion Loss



Return Loss



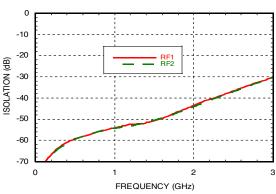
Input Third Order Intercept Point vs. Control Voltage



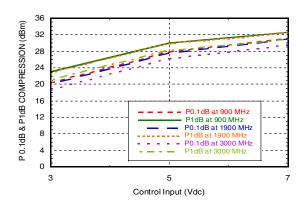
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Isolation

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Input 0.1 and 1.0 dB **Compression vs. Control Voltage**



Truth Table *Control Input Voltage Tolerances are ± 0.2 Vdc.

Contro	l Input*	Control Current		Signal Pa	ath State
A (Vdc)	B (Vdc)	la (uA)	lb (uA)	RF to RF1	RF to RF2
0	+3	-0.05	+0.05	ON	OFF
+3	0	+0.05	-0.05	OFF	ON
0	+5	-0.6	+0.6	ON	OFF
+5	0	+0.6	-0.6	OFF	ON
0	+7	-5	+5	ON	OFF
+7	0	+5	-5	OFF	ON

SWITCHES - SPDT - SMT

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Absolute Maximum Ratings

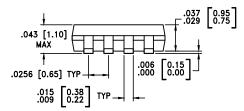
RF Input Power (Vctl= 0V/+5V)	+27 dBm
Control Voltage Range (A & B)	-0.2 to +7.5 Vdc
Hot Switch Power Level (Vctl= 0V/+5V)	+24 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 4.6 mW/°C above 85 °C)	300 mW
Thermal Resistance	216 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

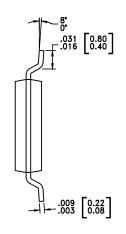
Note: DC blocking capacitors are required at ports RFC, RF1 and RF2. Their value will determine the lowest transmission frequency.

Outline Drawing



 $\begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} 122 \\ 2.90 \\ \end{array} \\ \hline \\ 8 \\ 7 \\ 6 \\ \end{array} \\ \hline \\ 8 \\ 7 \\ 6 \\ \end{array} \\ \hline \\ 8 \\ 7 \\ 6 \\ \end{array} \\ \hline \\ \\ 114 \\ \hline \\ 2.90 \\ \end{array} \\ \hline \\ \\ 120 \\ \hline 120 \\ \hline \\ 120 \\ \hline 120 \\ \hline 120$





NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY

2. DIMENSIONS ARE IN INCHES [MILLIMETERS].

DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.

A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.

5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

6. CLASSIFIED AS MOISTURE SENSITIVITY LEVEL (MSL) 1.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC194AMS8	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H194A XXXX
HMC194AMS8E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>H194A</u> XXXX

[1] Max peak reflow temperature of 235 $^\circ\text{C}$

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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RF2 ○--

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GND

6

5

7

2

В

3

RFC

4

74HC04 or 74HCT04

1

А

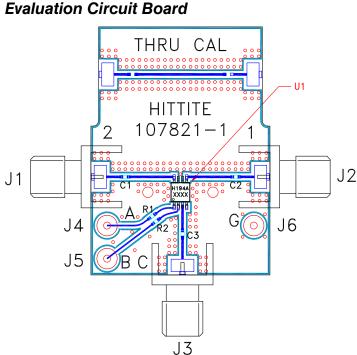
+Vdd

CTI

-0 RF1

Notes:

- 1. Set logic gate and switch Vdd = +3V to +5V and use HCT series logic to provide a TTL driver interface.
- 2. Control inputs A/B can be driven directly with CMOS logic (HC) with Vdd of 3 to 7 Volts applied to the CMOS logic gates.
- 3. DC Blocking capacitors are required for each RF port as shown. Capacitor value determines lowest frequency of operation.
- 4. Highest RF signal power capability is achieved with Control set to 0/+7V.





Item	Description
J1 - J3	PC Mount SMA RF Connector
J4 - J6	DC Pin
C1 - C3	100 pF capacitor, 0402 Pkg.
R1, R2	100 Ω resistor, 0402 Pkg.
U1	HMC194AMS8 / 194AMS8E SPDT Switch
PCB [2]	107821 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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