## GaAs MMIC SPDT NON-REFLECTIVE SWITCH, DC - 12 GHz

## Typical Applications

The HMC232ALP4E is ideal for:

- Telecom Infrastructure
- Microwave Radio \& VSAT
- Military Radios, Radar \& ECM
- Test Instrumentation

Functional Diagram


## Features

Isolation: 57 dB @ 3 GHz 50 dB @ 6 GHz

Input P1dB: +30 dBm
Insertion Loss: 1.5 dB Typical @ 6 GHz
Non-Reflective Design
24 Lead 4x4mm QFN Package: 16mm²
Included in the HMC-DK005 Designer's Kit

## General Description

The HMC232ALP4E is a broadband high isolation non-reflective GaAs MESFET SPDT switch in a low cost leadless QFN surface mount plastic package. Covering DC to 12 GHz , the switch features $>57 \mathrm{~dB}$ isolation up to 3 GHz and $>45 \mathrm{~dB}$ isolation up to 12 GHz . Input P1dB compression is +30 dBm typical, while input IP3 is +48 dBm . The switch operates using complementary negative control voltage logic lines of $-5 / 0 \mathrm{~V}$ and requires no bias supply.

Electrical Specifications, $T_{A}=+25^{\circ} \mathrm{C}$, With 0/-5V Control, 50 Ohm System

| Parameter | Frequency | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{aligned} & \mathrm{DC}-3.0 \mathrm{GHz} \\ & \mathrm{DC}-6.0 \mathrm{GHz} \\ & \mathrm{DC}-9.0 \mathrm{GHz} \\ & \mathrm{DC}-12.0 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 1.4 \\ & 1.5 \\ & 2.0 \\ & 2.5 \end{aligned}$ | $\begin{aligned} & 1.7 \\ & 1.8 \\ & 2.3 \\ & 3.1 \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Isolation | $\begin{aligned} & \mathrm{DC}-3.0 \mathrm{GHz} \\ & \mathrm{DC}-6.0 \mathrm{GHz} \\ & \mathrm{DC}-9.0 \mathrm{GHz} \\ & \mathrm{DC}-12.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 52 \\ & 45 \\ & 42 \\ & 40 \end{aligned}$ | $\begin{aligned} & 57 \\ & 50 \\ & 47 \\ & 45 \end{aligned}$ |  | dB <br> dB <br> dB <br> dB |
| Return Loss "On State" | $\begin{gathered} \mathrm{DC}-6.0 \mathrm{GHz} \\ \mathrm{DC}-9.0 \mathrm{GHz} \\ \mathrm{DC}-12.0 \mathrm{GHz} \end{gathered}$ |  | $\begin{aligned} & 18 \\ & 14 \\ & 12 \end{aligned}$ |  | dB <br> dB <br> dB |
| Return Loss RF1, RF2 "Off State" | DC - 12.0 GHz |  | 14 |  | dB |
| Input Power for 1 dB Compression | $0.5-12.0 \mathrm{GHz}$ | 26 | 30 |  | dBm |
| Input Third Order Intercept <br> (Two-Tone Input Power $=+10 \mathrm{dBm}$ Each Tone, 1 MHz Tone Separation) | 0.5-12.0 GHz | 45 | 48 |  | dBm |
| Switching Characteristics tRISE, tFALL (10/90\% RF) tON, tOFF ( $50 \%$ CTL to $10 / 90 \%$ RF) | DC - 12.0 GHz |  | $\begin{gathered} 6 \\ 25 \end{gathered}$ |  | $\begin{aligned} & \text { ns } \\ & \text { ns } \end{aligned}$ |

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Insertion Loss


## Return Loss



Isolation

0.1 and 1 dB Input Compression Point


Input Third Order Intercept Point


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## Absolute Maximum Ratings

| RF Input Power (Vctl $=-5 \mathrm{~V})$ <br> $(0.5-12 \mathrm{GHz})$ |  |
| :--- | :--- |
| Insertion Loss Path <br> Terminated Path | +30.9 dBm <br> +23.7 dBm |
| Control Voltage Range (A \& B) | +1 V to -7.5 V |
| Channel Temperature | $150^{\circ} \mathrm{C}$ |
| Thermal Resistance ( $\mathrm{R}_{\text {TH }}$ ) <br> (channel to ground paddle) <br> Terminated Path | $277^{\circ} \mathrm{C} / \mathrm{W}$ |
| Storage Temerature | -65 to $+150^{\circ} \mathrm{C}$ |
| Operating Temperature | -40 to $+85^{\circ} \mathrm{C}$ |

## Control Voltages

| State | Bias Condition |
| :--- | :--- |
| Low | 0 to -0.2V @ 0.2 uA Max. |
| High | $-5 \mathrm{~V} @ 2$ uA Typ. to -7V @ 20 uA Typ. ( $\pm 0.5 \mathrm{Vdc})$ |

## Truth Table

| Control Input |  | Signal Path State |  |
| :---: | :---: | :---: | :---: |
| A | B | RFC to RF1 | RFC to RF2 |
| High | Low | ON | OFF |
| Low | High | OFF | ON |

Caution: Do not "Hot Switch" power levels greater than +27 dBm (Vctl = 0/-5 Vdc).


ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

## Outline Drawing



## Package Information

| Part Number | Package Body Material | Lead Finish | MSL Rating | Package Marking ${ }^{[2]}$ |
| :---: | :---: | :---: | :---: | :---: |
| HMC232ALP4E | RoHS-compliant Low Stress Injection Molded Plastic | $100 \%$ matte Sn | MSL3 $^{[1]}$ | $\frac{\text { H232A }}{\text { XXXX }}$ |

[^0]GaAs MMIC SPDT NON-REFLECTIVE SWITCH, DC - 12 GHz

## Suggested Driver Circuit



Pin Descriptions

| Pin Number | Function | Description | Interface Schematic |
| :---: | :---: | :---: | :---: |
| $\begin{gathered} 1,2,6,7,11 \\ 12,13,14,17, \\ 18,19,20,24 \\ \hline \end{gathered}$ | N/C | The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally. |  |
| $\begin{gathered} 3,5,8 \\ 10,21,23 \end{gathered}$ | GND | Package bottom must also be connected to PCB RF ground. | $\frac{O G N D}{=}$ |
| 4, 9, 22 | RFC, RF1, RF2 | This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if RF line potential is not equal to 0 V . |  |
| 15 | B | See truth table and control voltage table. |  |
| 16 | A | See truth table and control voltage table. |  |

Evaluation PCB


List of Materials for Evaluation PCB EV1HMC232ALP4 ${ }^{[1]}$

| Item | Description |
| :--- | :--- |
| J1 - J3 | PCB Mount SMA RF Connector |
| J4- J6 | DC Pin |
| C1, C2 | 100 pF Capacitor, 0603 Pkg. |
| U1 | HMC232ALP4E SPDT Switch |
| PCB [2] | 107602 Evaluation PCB |

[1] Reference this number when ordering complete evaluation PCB
[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads and package bottom should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Analog Devices, upon request.

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## Notes:

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[^0]:    [1] Max peak reflow temperature of $260^{\circ} \mathrm{C}$
    [2] 4-Digit lot number XXXX

