

# HMC344LP3 / 344LP3E

v06.1210



## GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 8 GHz

### Typical Applications

This switch is suitable for usage in DC - 8.0 GHz 50-Ohm or 75-Ohm systems:

- Broadband
- Fiber Optics
- Switched Filter Banks
- Wireless below 8 GHz

#### **Features**

Broadband Performance: DC - 8 GHz

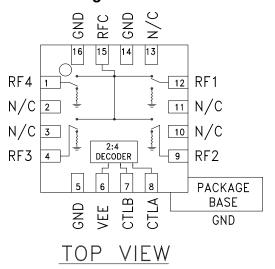
High Isolation: 40 dB@ 6 GHz

Low Insertion Loss: 1.8 dB@ 6 GHz

Integrated 2:4 TTL Decoder

16 Lead 3x3mm QFN Package: 9 mm²

### **Functional Diagram**



### **General Description**

The HMC344LP3 & HMC344LP3E are broadband non-reflective GaAs MESFET SP4T switches in low cost leadless surface mount packages. Covering DC to 8 GHz, this switch offers high isolation and low insertion loss and extends the frequency coverage of Hittite's SP4T switch product line. This switch also includes an on board binary decoder circuit which reduces the required logic control lines to two. The switch operates using a negative control voltage of 0/-5V, and requires a fixed bias of -5V.

## Electrical Specifications, $T_A = +25^{\circ}$ C, With 0/-5V Control, 50 Ohm System

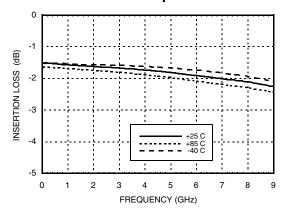
| Parameter   |        | Frequency  | Min.                 | Тур.                 | Max.              | Units                |
|---|--------|--|----------------------|----------------------|-------------------|----------------------|
| Insertion Loss  |        | DC - 2.0 GHz<br>DC - 6.0 GHz<br>DC - 8.0 GHz                 |                      | 1.6<br>1.8<br>2.1    | 2.0<br>2.2<br>2.5 | dB<br>dB<br>dB       |
| Isolation   |        | DC - 2.0 GHz<br>DC - 4.0 GHz<br>DC - 6.0 GHz<br>DC - 8.0 GHz | 43<br>36<br>34<br>31 | 48<br>41<br>40<br>36 |                   | dB<br>dB<br>dB<br>dB |
| Return Loss "On S   | State" | DC - 2.0 GHz<br>DC - 4.0 GHz<br>DC - 6.0 GHz<br>DC - 8.0 GHz | 12<br>9<br>8<br>5    | 15<br>12<br>11<br>8  |                   | dB<br>dB<br>dB<br>dB |
| Return Loss "Off S  | State" | DC - 8.0 GHz   | 7                    | 10                   |                   | dB                   |
| Input Power for 1 dB Compression  |        | 0.5 - 8.0 GHz  | 17                   | 21                   |                   | dBm                  |
| Input Third Order Intercept<br>(Two-Tone Input Power = +7 dBm Each Tone)                  |        | 0.5 - 8.0 GHz  | 37                   | 40                   |                   | dBm                  |
| Switching Characteristics<br>tRISE, tFALL (10/90% RF)<br>tON, tOFF (50% CTL to 10/90% RF) |        | DC - 8.0 GHz   |                      | 35<br>150            |                   | ns<br>ns             |



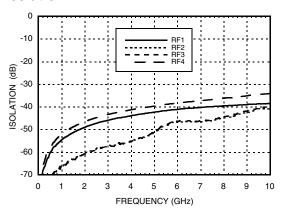


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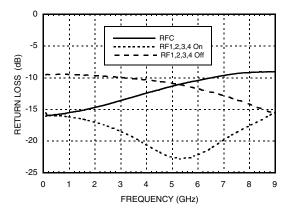
#### Insertion Loss vs. Temperature



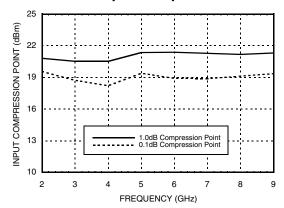
#### Isolation



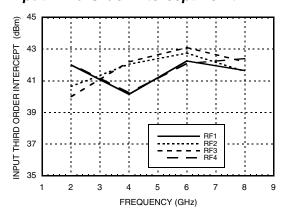
#### **Return Loss**



#### 0.1 and 1 dB Input Compression Point



#### Input Third Order Intercept Point



### Bias Voltage & Current

| Vee Range = -5.0 Vdc ± 10% |                    |                    |  |
|----------------------------|--------------------|--------------------|--|
| Vee<br>(Vdc)               | lee (Typ.)<br>(mA) | lee (Max.)<br>(mA) |  |
| -5.0                       | 3.0                | 6.0                |  |

### **Control Voltages**

| State | Bias Condition                |  |
|-------|-------------------------------|--|
| Low   | -3V to 0 Vdc @ 60 μA Typical  |  |
| High  | -5 to -4.2 Vdc @ 5 μA Typical |  |

<sup>\*</sup> Isolation is recorded above insertion loss & measured at output of switch.





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#### **Absolute Maximum Ratings**

| Bias Voltage Range (Vee)   | -7.0 Vdc   |
|--|--|
| Control Voltage Range (A & B)  | Vee -0.5V to +1.0 Vdc                                    |
| Channel Temperature  | 150 °C   |
| Thermal Resistance<br>(Insertion Loss Path)  | 143 °C/W   |
| Thermal Resistance<br>(Terminated Path)  | 1,030 °C/W   |
| Storage Temperature  | -65 to +150 °C   |
| Operating Temperature  | -40 to +85 °C  |
| Maximum Input Power  | +24 dBm  |
| ESD Sensitivity (HBM)  | Class 1A   |
| (Insertion Loss Path) Thermal Resistance (Terminated Path) Storage Temperature Operating Temperature Maximum Input Power | 1,030 °C/W<br>-65 to +150 °C<br>-40 to +85 °C<br>+24 dBm |

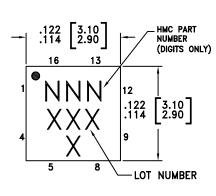
#### **Truth Table**

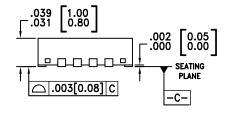
| Control Input |      | Signal Path State |
|---------------|------|-------------------|
| А             | В    | RFCOM to:         |
| High          | High | RF1               |
| Low           | High | RF2               |
| High          | Low  | RF3               |
| Low           | Low  | RF4               |



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### **Outline Drawing**





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#### NOTES:

- 1. LEADFRAME MATERIAL: COPPER ALLOY
- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
- PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
   PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- 6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED PCB LAND PATTERN.

### Package Information

| Part Number | Package Body Material                              | Lead Finish   | MSL Rating | Package Marking [3] |
|-------------|--|---------------|------------|---------------------|
| HMC344LP3   | Low Stress Injection Molded Plastic                | Sn/Pb Solder  | MSL1 [1]   | 344<br>XXXX         |
| HMC344LP3E  | RoHS-compliant Low Stress Injection Molded Plastic | 100% matte Sn | MSL1 [2]   | 344<br>XXXX         |

- [1] Max peak reflow temperature of 235 °C
- [2] Max peak reflow temperature of 260  $^{\circ}\text{C}$
- [3] 4-Digit lot number XXXX





## GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 8 GHz

### **Pin Descriptions**

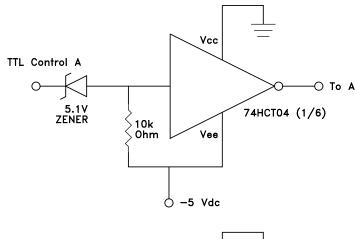
| Pin Number          | Function                   | Description   | Interface Schematic |  |
|---------------------|----------------------------|---|---------------------|--|
| 1, 4,<br>9, 12, 15  | RF4, RF3, RF2,<br>RF1, RFC | This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if RF line potential is not equal to 0V. |                     |  |
| 2, 3,<br>10, 11, 13 | N/C                        | This pin should be connected to PCB RF ground to maximize isolation.  | ○ GND<br>=          |  |
| 5, 14, 16           | GND                        | Package bottom has exposed metal paddle that must also be connected to PCB RF ground.                                   | ○ GND<br>=          |  |
| 6                   | VEE                        | Supply Voltage -5V ± 10%  | VEE 0 5pF 2K        |  |
| 7                   | CTLB                       | See truth table and control voltage table.  | 100K                |  |
| 8                   | CTLA                       | See truth table and control voltage table.  | ↓ ±                 |  |

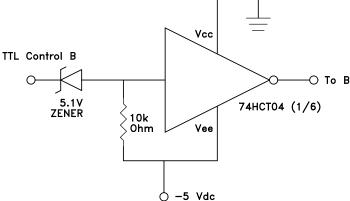




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#### **TTL Interface Circuit**



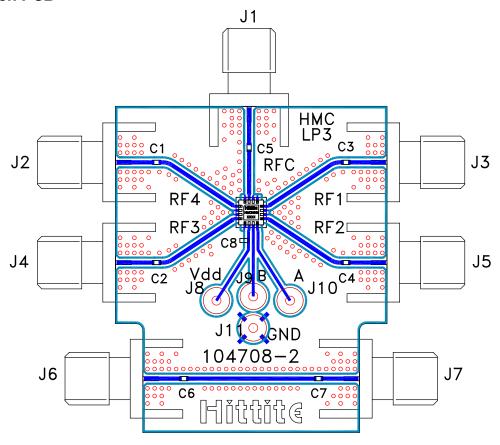






### GaAs MMIC SP4T NON-REFLECTIVE SWITCH. DC - 8 GHz

#### **Evaluation PCB**



#### List of Materials for Evaluation PCB 105311 [1]

| Item     | Description                           |  |
|----------|---------------------------------------|--|
| J1 - J7  | PCB Mount SMA RF Connector            |  |
| J8 - J11 | DC Pin                                |  |
| C1 - C7  | 0 ohm res, 0402 Pkg. [3]              |  |
| C8       | 10k pF Capacitor, 0603 Pkg.           |  |
| U1       | HMC344LP3 / HMC344LP3E<br>SP4T Switch |  |
| PCB [2]  | 104708 Evaluation PCB 1.29"x1.55"     |  |

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

[3] Select and replace with a suitable capacitor value for applicable operating frequency range.

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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