ANALOGDEVICES

HMC362S8G / 362S8GE

v04.0919



SMT GaAs HBT MMIC DIVIDE-BY-4, DC - 12 GHz

Typical Applications

Prescaler for DC to X Band PLL Applications:

- Satellite Communication Systems
- Fiber Optic
- Point-to-Point and Point-to-Multi-Point Radios
- VSAT

Features

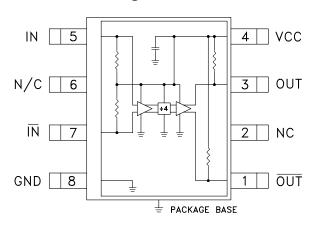
Ultra Low SSB Phase Noise: -149 dBc/Hz

Wide Bandwidth

Output Power: -6 dBm Single DC Supply: +5V

S8G SMT Package

Functional Diagram



General Description

The HMC362S8G & HMC362S8GE are low noise Divide-by-4 Static Dividers with InGaP GaAs HBT technology in 8 lead surface mount plastic packages. This device operates from DC (with a square wave input) to 12 GHz input frequency with a single +5V DC supply. The low additive SSB phase noise of -149 dBc/Hz at 100 kHz offset helps the user maintain good system noise performance.

Electrical Specifications, $T_A = +25^{\circ}$ C, 50 Ohm System, Vcc = 5V

Parameter	Conditions	Min.	Тур.	Max.	Units
Maximum Input Frequency		12	13		GHz
Minimum Input Frequency	Sine Wave Input. [1]		0.2	0.5	GHz
Input Power Range	Fin = 1 to 8 GHz	-15	>-20	+10	dBm
	Fin = 8 to 10 GHz	-10	>-15	+3	dBm
	Fin = 10 to 12 GHz	-5	>-8	0	dBm
Ouput Power	Fin = 11 GHz	-9	-6		dBm
Reverse Leakage	Both RF Outputs Terminated		60		dB
SSB Phase Noise (100 kHz offset)	Pin = 0dBm, Fin = 6 GHz		-149		dBc/Hz
Output Transition Time	Pin = 0 dBm, Fout = 882 MHz		100		ps
Supply Current (Icc)			68		mA

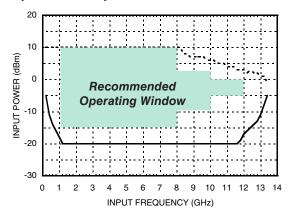
^{1.} Divider will operate down to DC for square-wave input signal.



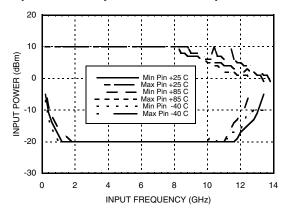


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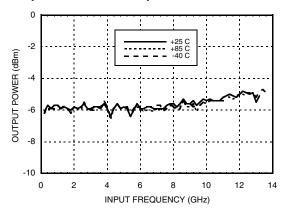
Input Sensitivity Window, T= 25 °C



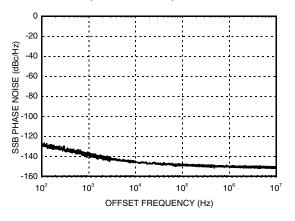
Input Sensitivity Window vs. Temperature



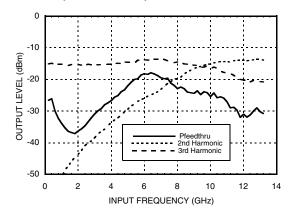
Output Power vs. Temperature



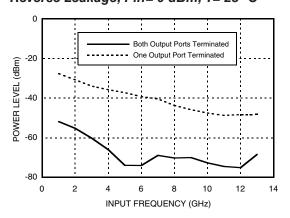
SSB Phase Noise Performance, Pin= 0 dBm, T= 25 °C



Output Harmonic Content, Pin= 0 dBm, T= 25 °C



Reverse Leakage, Pin= 0 dBm, T= 25 °C

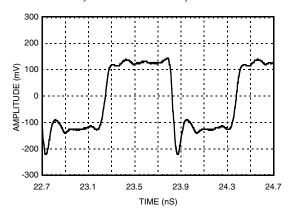






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Output Voltage Waveform, Pin= 0 dBm, Fout= 882 MHz, T= 25 °C



Absolute Maximum Ratings

RF Input (Vcc = +5V)	+13 dBm
Vcc	+5.5V
VLogic	Vcc -1.6V to Vcc -1.2V
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

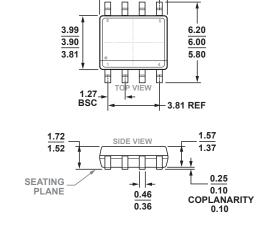
9-A

Typical Supply Current vs. Vcc

Vcc (V)	Icc (mA)
4.75	61
5.0	68
5.25	74

Note: Divider will operate over full voltage range shown above

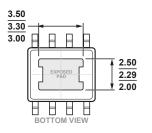
Outline Drawing

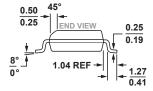


4.98 4.90

4.80

0.53 REF





8-Lead Standard Small Outline Package, with Exposed Pad [SOIC_N_EP]

Narrow Body

(RD-8-3)

Dimensions shown in millimeters.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC362S8G	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	HMC362 XXXX
HMC362S8GE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	HMC362 XXXX

- [1] Max peak reflow temperature of 235 °C
- [2] Max peak reflow temperature of 260 $^{\circ}\text{C}$
- [3] 4-Digit lot number XXXX





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Pin Description

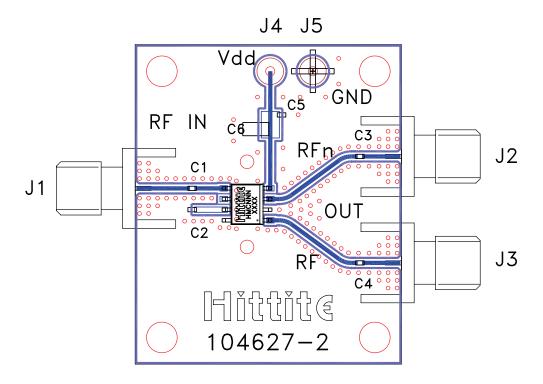
Pin Number	Function	Description	Interface Schematic
1	ОUТ	Divided output 180° out of phase with pin 3.	5V OUT
2, 6	N/C	No connection. These pins must not be grounded.	
3	OUT	Divided Output.	OUT
4	vcc	Supply voltage 5V ± 0.25V.	5V 25 50
5	IN	RF Input must be DC blocked.	50 5V
7	ĪN	RF Input 180° out of phase with pin 5 for differential operation. A/C ground for single ended operation	50 5V
8	GND	Ground Backside of package has exposed metal ground slug which must be connected to ground.	GND =





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Evaluation PCB



List of Materials for Evaluation PCB 104631 [1]

A RF Connector or, 0402 Pkg.
or, 0402 Pkg.
itor, 0603 Pkg.
Capacitor
HMC362S8GE Divide-by-4
pard

^[1] Reference this number when ordering complete evaluation PCB

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request. This evaluation board is designed for single ended input testing. J2 and J3 provide differential output signals.

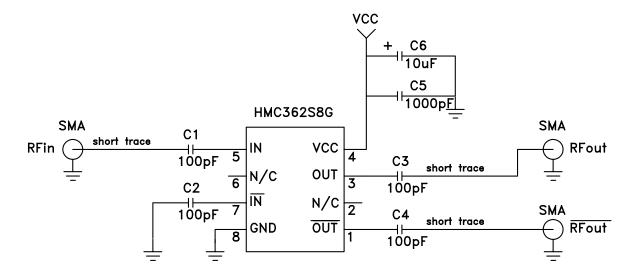
^[2] Circuit Board Material: Rogers 4350





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Application Circuit



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HMC988LP3ETR HMC361G8 HMC361S8G HMC361S8GETR HMC363G8 HMC363S8G HMC363S8GETR HMC365G8

HMC365S8GETR HMC394LP4ETR HMC437MS8G HMC447LC3 HMC447LC3TR HMC492LP3ETR HMC492LP3 HMC493LP3E

HMC433 HMC432ETR HMC434ETR HMC434E HMC432 HMC432E HMC794LP3E HMC859LC3 HMC983LP5E HMC438MS8GTR

ADMV2101BRHZ UXM15P HMC437MS8GETR HMC438MS8G HMC438MS8GE HMC438MS8GETR MC12026ADG