



# HMC544A / 544AE

Very Low Insertion Loss: 0.2 dB @ 1.0 GHz

### GaAs MMIC T/R SWITCH, DC - 4 GHz

### **Typical Applications**

The HMC544A / 544AE is ideal for:

- Cellular/PCS/3G Infrastructure
- Basestations & Repeaters
- WLAN, WiMAX and WiBro
- Microwave and Fixed Wireless Radios

### **General Description**

Features

High Input P1dB: +39 dBm

Positive Control: 0/+3V to 0/+5V

Compact SOT26 SMT Package

High Input IP3: +60 dBm

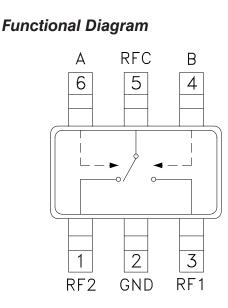
The HMC544A & HMC544AE are low cost SPDT switches in 6-lead SOT26 packages for use in transmitreceive applications which require very low insertion loss at medium power levels. These devices can control signals from DC to 4.0 GHz and are especially suited for 450, 900, 1900, 2300, and 2700 MHz applications with <0.5 dB insertion loss. This GaAs PHEMT design provides exceptional linearity performance of +36 dBm 1dB compression point and +60 dBm third order intercept at +3 volt bias. RF1 and RF2 are reflective opens when "Off". On-chip circuitry allows positive control operation at very low DC current.

### Electrical Specifications, $T_A = +25^{\circ}$ C, Vctl = 0/+3 Vdc, 50 Ohm System

Parameter	Frequency	Min.	Тур.	Max.	Units
Insertion Loss	DC - 1.0 GHz DC - 2.5 GHz DC - 3.0 GHz DC - 4 GHz		0.25 0.4 0.5 0.7	0.5 0.7 0.8 1.0	dB dB dB dB
Isolation	DC - 1.0 GHz DC - 2.5 GHz DC - 3.0 GHz DC - 4 GHz	18 11 9 8	23 16 14 12		dB dB dB dB
Return Loss	DC - 4 GHz		28		dB
Input Power for 1 dB Compression 0/+5V Control 0/+3V Control	0.3 - 4.0 GHz	36 33	39 35		dBm dBm
Input Third Order Intercept (Two-Tone Input Power = +13 dBm Each Tone)	0.3 - 4.0 GHz		60		dBm
Switching Characteristics	DC - 4 GHz				
tRISE, tFALL (10/90% RF) tON, tOFF (50% CTL to 10/90% RF)			30 50		ns ns

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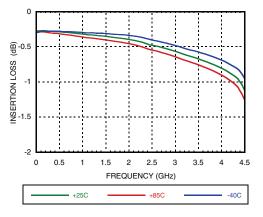
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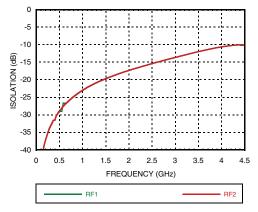


# ROHS V

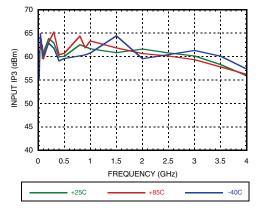
### **Insertion Loss**



Isolation Between Ports RFC & RF1 / RF2



Input IP3 vs. Temperature, Vctl = 0/+3V

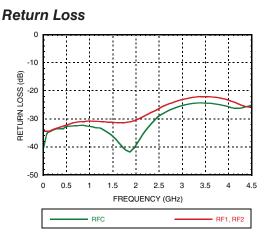


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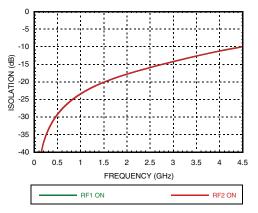
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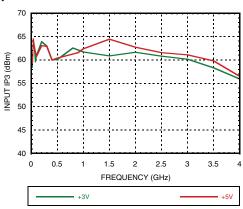
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Isolation Between Ports RF1 & RF2



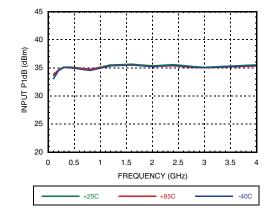








### Input P1dB vs. Temperature, Vctl = 0/+3V



### Truth Table

Control Input		Signal Path		
А	В	RFC to RF1 RFC to R		
Low	High	On	Off	
High	Low	Off	On	

# Compression vs. Vctl

P0.1dB +3V P0.1dB +5V

### **Control Voltages**

20

0 0.5 1 1.5 2 2.5 3

State	Bias Condition		
Low	0 to 0.2 Vdc @ 1 µA Typical		
High	+3 Vdc @ 0.5 μA Typical to +5 Vdc @ 1 μA Typical (±0.2 Vdc)		

FREQUENCY (GHz)

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### GaAs MMIC T/R SWITCH, DC - 4 GHz

3.5 4

P1dB +3V P1dB +5V





### Absolute Maximum Ratings

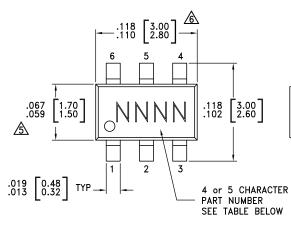
-
+39 dBm
-0.2 to +12 Vdc
+39 dBm
150 °C
0.574 W
113.3 °C/W
-65 to +150 °C
-40 to +85 °C
Class 1B

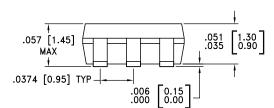
DC blocks are required at ports RFC, RF1 and RF2.

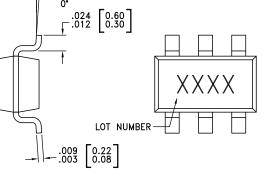
### **Outline Drawing**



ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS** 







NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY

2. DIMENSIONS ARE IN INCHES [MILLIMETERS].

DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.

A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.

5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

### Package Information

Part Number	Package Body Material	Package Body Material Lead Finish MSL Rating		Package Marking <sup>[3]</sup>	
HMC544A	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 <sup>[1]</sup>	544A XXXX	
HMC544AE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	544AE XXXX	

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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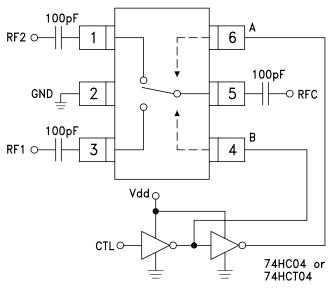
**RoHS**√

v00.1212



### GaAs MMIC T/R SWITCH, DC - 4 GHz

### Typical Application Circuit



### Notes:

- 1. Set logic gate Vdd = +3V to +5V and use HCT series logic to provide a TTL driver interface.
- 2. Control inputs A/B can be driven directly with CMOS logic (HC) with Vdd of +3V to +5V applied to the CMOS logic gates.
- 3. DC Blocking capacitors are required for each RF port as shown. Capacitor value determines lowest frequency of operation.

### **Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1, 3, 5	RF2, RF1, RFC	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
2	GND	This pin must be connected to RF/DC ground.	
4	В	See truth and control voltage tables.	R
6	А	See truth and control voltage tables.	± c

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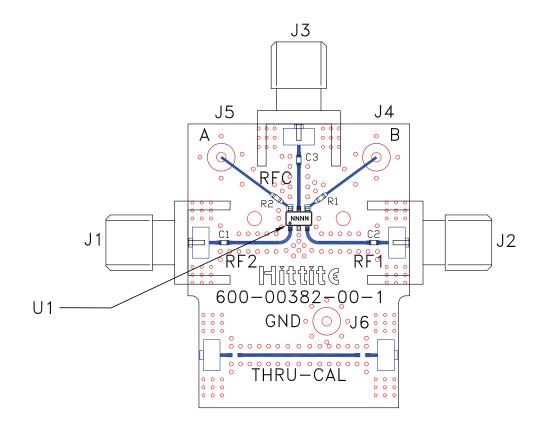
## HMC544A / 544AE

### v00.1212



### GaAs MMIC T/R SWITCH, DC - 4 GHz

### **Evaluation Circuit Board**



### List of Materials for Evaluation PCB EVAL01-HMC544A [1]

Item	Description
J1 - J3	PCB Mount SMA RF Connector
J4 - J6	DC Pin
C1 - C2	330 pF capacitor, 0402 Pkg.
R1 - R2	1 KOhm resistor, 0402 Pkg.
U1	HMC544A / 544AE SPDT Switch
PCB [2]	600-00382-00-1 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

### The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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