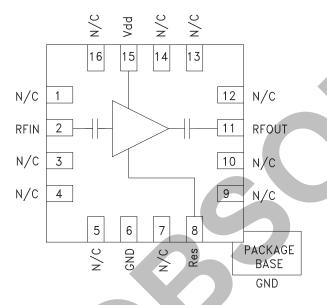




The HMC617LP3(E) is ideal for:

- Cellular/3G and LTE/WiMAX/4G
- BTS & Infrastructure
- Repeaters and Femtocells
- Public Safety Radio
- Access Points

Functional Diagram



HMC617LP3 / 617LP3E

GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz

Features

Noise Figure: 0.5 dB Gain: 16 dB Output IP3: +37 dBm Single Supply: +3V to +5V 50 Ohm Matched Input/Output 16 Lead 3x3mm QFN Package: 9 mm²

General Description

The HMC617LP3(E) is a GaAs PHEMT MMIC Low Noise Amplifier that is ideal for Cellular/3G and LTE/WiMAX/4G basestation front-end receivers operating between 550 and 1200 MHz. The amplifier has been optimized to provide 0.5 dB noise figure, 16 dB gain and +37 dBm output IP3 from a single supply of +5V. Input and output return losses are excellent and the LNA requires minimal external matching and bias decoupling components. The HMC617LP3(E) shares the same package and pinout with the HMC618LP3(E) 1.7 - 2.2 GHz LNA. The HMC617LP3(E) can be biased with +3V to +5V and features an externally adjustable supply current which allows the designer to tailor the linearity performance of the LNA for each application. The HMC617LP3(E) offers improved noise figure versus the previously released HMC372LP3(E) and the HMC376LP3(E).

Electrical Specifications, $T_A = +25^{\circ}$ C, Rbias = 3.92k Ohms*

	Vdd = +3 Vdc Vdd = +5 Vdc					Linita							
Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max. Units	
Frequency Range	(698 - 960)	5	50 - 120	0		698 - 960)	5	50 - 120	0	MHz
Gain	13	16		11	15		13.5	16		11.5	16		dB
Gain Variation Over Temperature		0.003			0.003			0.005			0.005		dB/ °C
Noise Figure		0.5	0.8		0.5	1.1		0.55	0.85		0.6	1.1	dB
Input Return Loss		28			22			22			17		dB
Output Return Loss		12			14			12			15		dB
Output Power for 1 dB Compression (P1dB)	14	16		12.5	16		18.5	21		16.5	20		dBm
Saturated Output Power (Psat)		17			16.5			21			20.5		dBm
Output Third Order Intercept (IP3)		31			30			37			37		dBm
Supply Current (Idd)		30	45		30	45		88	115		88	115	mA

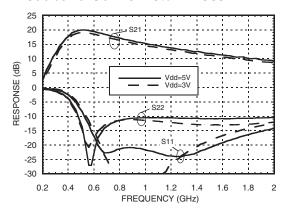
* Rbias resistor sets current, see application circuit herein

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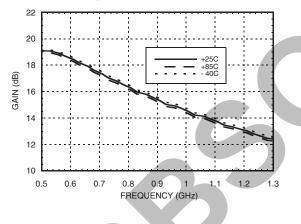




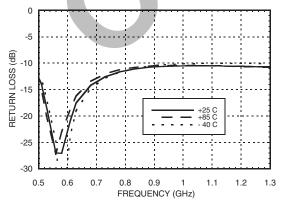
Broadband Gain & Return Loss [1] [2]



Gain vs. Temperature [2]



Output Return Loss vs. Temperature [1]

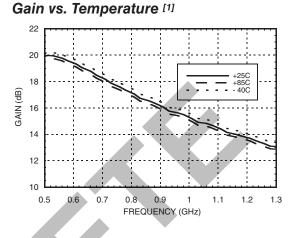


[1] Vdd = 5V, Rbias = 3.92K [2] Vdd = 3V, Rbias = 3.92K

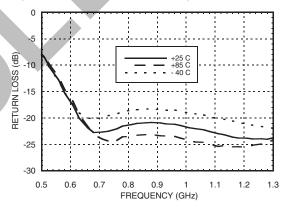
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HMC617LP3 / 617LP3E

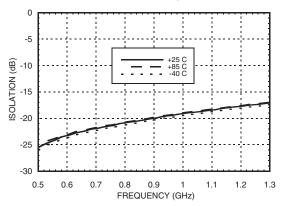
GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz



Input Return Loss vs. Temperature [1]



Reverse Isolation vs. Temperature [1]



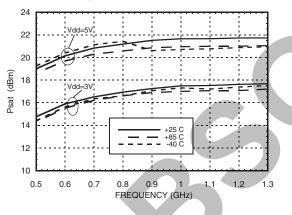




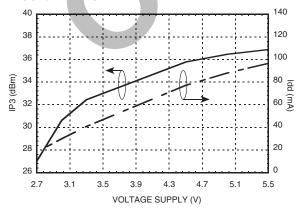
1.2 Vdd=5V Vdd=3V **VOISE FIGURE (dB)** 0.8 +85C 0.6 -25 C 1 Erara 0.4 -400 0.2 0 0.6 0.7 1.2 0.5 0.8 0.9 1.1 1.3 1 FREQUENCY (GHz)

Noise Figure vs. Temperature [1] [2] [4]

Psat vs. Temperature [1] [2]



Output IP3 and Idd vs. Supply Voltage @ 700 MHz ^[3]



Vdd = 5V, Rbias = 3.92K [2] Vdd = 3V, Rbias = 3.92K
Rbias = 3.92K [4] Measurement reference plane shown on evaluation PCB drawing.

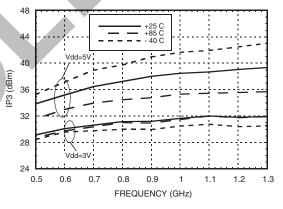
HMC617LP3 / 617LP3E

GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz

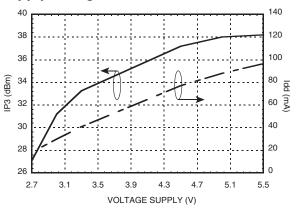
24 22 20 (dBm) 18 Vdd=3\ P1dB 16 14 12 10 0.6 0.5 0.7 0.8 0.9 1 1.1 1.2 1.3 FREQUENCY (GHz)

P1dB vs. Temperature [1] [2]

Output IP3 vs. Temperature [1] [2]



Output IP3 and Idd vs. Supply Voltage @ 900 MHz ^[3]

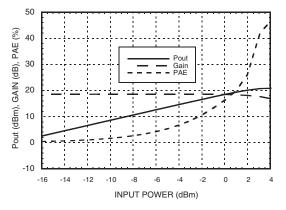


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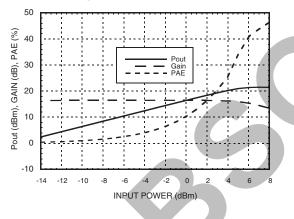




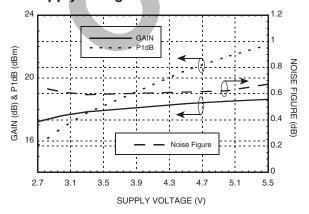
Power Compression @ 700 MHz [1]



Power Compression @ 900 MHz [1]



Gain, Power & Noise Figure vs. Supply Voltage @ 700 MHz [3]



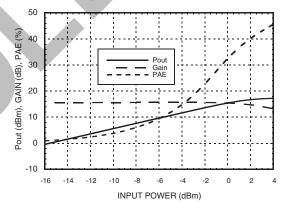
Power Compression @ 700 MHz [2]

AMPLIFIER, 0.55 - 1.2 GHz

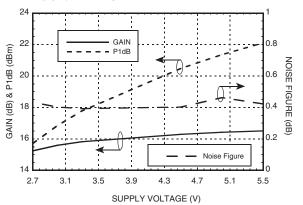
HMC617LP3 / 617LP3E

50 Pout (dBm), GAIN (dB), PAE (%) 40 30 20 10 0 -10 -18 -16 -12 -10 -8 -6 -4 -2 0 INPUT POWER (dBm)

Power Compression @ 900 MHz [2]







[1] Vdd = 5V, Rbias = 3.92K [2] Vdd = 3V, Rbias = 3.92K [3] Rbias = 3.92K

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40

38

36

34

32

30 28

26

24 22

500

IP3 (dBm)

HMC617LP3 / 617LP3E

GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz

0.8 FIGURE (0.6 0.4

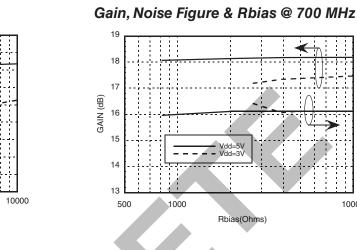
(dB

0.4

0.2

0

10000



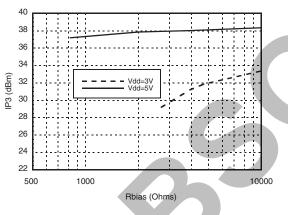
Output IP3 vs. Rbias @ 900 MHz

1000

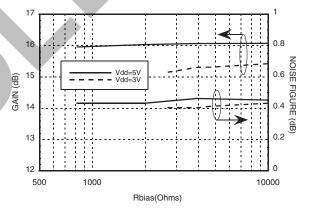
Output IP3 vs. Rbias @ 700 MHz

Vdd=3V Vdd=5V

Rbias (Ohms)



Gain, Noise Figure & Rbias @ 900 MHz



Absolute Bias Resistor Range & Recommended Bias Resistor Values for Idd

		ldd (m A)		
Vdd (V)	Min	Max	Recommended	ldd (mA)
			2.7k	24
зv	1K ^[1]	[1] Open Circuit	3.92k	30
3V	IN U		4.7k	33
			10k	40
			820	65
5V		Open Circuit 2k 3.92k	78	
	0		3.92k	88
			10k	90

[1] With Vdd= 3V and Rbias < 1K Ohm may result in the part becoming conditionally stable which is not recommended.

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v00.0807



Absolute Maximum Ratings

Drain Bias Voltage (Vdd)	+6V
RF Input Power (RFIN) (Vdd = +5 Vdc)	+10 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 8.33 mW/°C above 85 °C)	0.54 W
Thermal Resistance (channel to ground paddle)	120 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C



ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS**

Outline Drawing

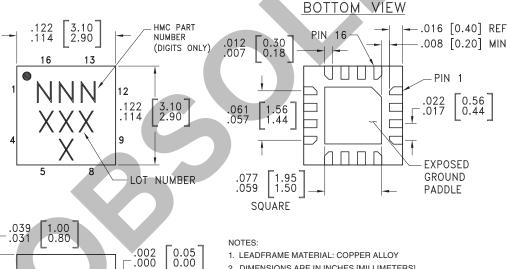


GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz

Typical Supply Current vs. Vdd (Rbias = 3.92k)

Vdd (V)	ldd (mA)
2.7	18
3.0	30
3.3	41
4.5	77
5.0	88
5.5	97

Note: Amplifier will operate over full voltage ranges shown above.



- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
- PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- 6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

.003[0.08]|C

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC617LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	617 XXXX
HMC617LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>617</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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SFATING

PLANE

-C-



HMC617LP3 / 617LP3E

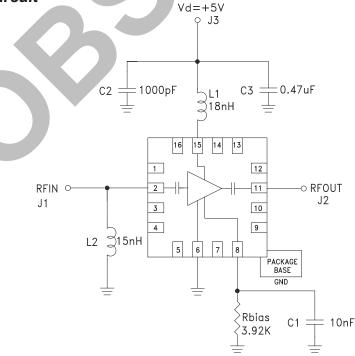
GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz



Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3 - 5, 7, 9, 10, 12 - 14, 16	N/C	No connection required. These pins may be connected to RF/ DC ground without affecting performance.	
2	RFIN	This pin is matched to 50 Ohms.	
6	GND	This pin and ground paddle must be connected to RF./DC ground.	GND
11	RFOUT	This pin is matched to 50 Ohms.	
8	RES	This pin is used to set the DC current of the amplifier by selection of external bias resistor. See application circuit.	
15	Vdd	Power Supply Voltage. Choke inductor and bypass capacitors are required. See application circuit.	Vdd

Application Circuit



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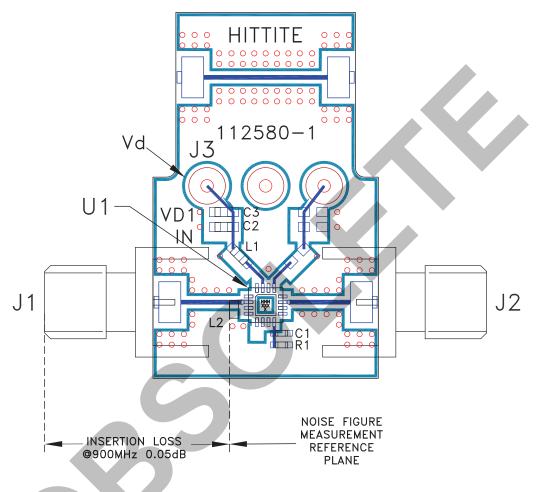
HMC617LP3 / 617LP3E

GaAs SMT PHEMT LOW NOISE AMPLIFIER, 0.55 - 1.2 GHz



v02.0610

Evaluation PCB



List of Materials for Evaluation PCB 118357 [1]

Item	Description	
J1, J2	PCB Mount SMA RF Connector	
J3, J4	DC Pin	
C1	10nF Capacitor, 0402 Pkg.	
C2	1000 pF Capacitor, 0603 Pkg.	
C3	0.47µF Capacitor, 0603 Pkg.	
L1	18 nH, Inductor, 0603 Pkg.	
L2	15 nH, Inductor, 0402 Pkg.	
R1	3.92K Ohm Resistor, 0402 Pkg.	
U1	HMC617LP3(E) Amplifier	
PCB [2]	112580 Evaluation PCB	

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350.

The circuit board used in this application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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