

# f 5V Low Power RS232 5-Driver/3-Receiver Transceiver

# **FEATURES**

- Low Supply Current: 500µA
- 0.2µA Supply Current in SHUTDOWN
- 50µA Supply Current in RECEIVER ALIVE Mode
- ESD Protection Over ±10kV
- Operates from a Single 5V Supply
- Uses Small Capacitors: 0.1µF
- Operates to 120k Baud
- Three-State Outputs Are High Impedance When Off
- Output Overvoltage Does Not Force Current Back into Supplies
- RS232 I/O Lines Can Be Forced to ±25V Without Damage
- Flowthrough Architecture

# **APPLICATIONS**

- Battery-Powered Modems
- Battery-Powered DCE
- Notebook Computers
- Palmtop Computers

# DESCRIPTION

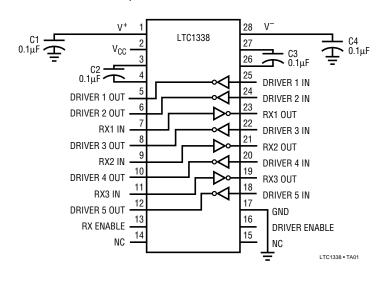
The LTC1338 is a 5-driver/3-receiver RS232 transceiver with very low supply current. In the no load condition, the supply current is only  $500\mu A$ . The charge pump only requires four  $0.1\mu F$  capacitors.

In SHUTDOWN mode, the supply current is further reduced to 0.2 $\mu$ A. In RECEIVER ALIVE mode, all three receivers are kept alive and the supply current is 50 $\mu$ A. All RS232 outputs assume a high impedance state in SHUTDOWN and with the power off.

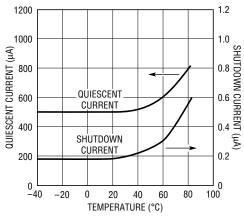
The LTC1338 is fully compliant with all data rate and overvoltage RS232 specifications. The transceiver can operate up to 120k baud with a 2500pF,  $3k\Omega$  load. Both driver outputs and receiver inputs can be forced to  $\pm 25V$  without damage, and can survive multiple  $\pm 10kV$  ESD strikes.

# TYPICAL APPLICATION

## 5-Drivers/3-Receivers with SHUTDOWN



## **Supply Current**

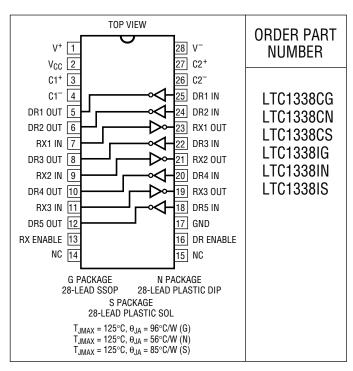


TEST CONDITION:  $V_{CC} = 5V$ , ALL DRIVER INPUTS TIED TO  $V_{CC}$ . LTC1338 - TA02

# **ABSOLUTE MAXIMUM RATINGS**

(Note 1)
Supply Voltage (V <sub>CC</sub> ) 6\
Input Voltage
Driver $-0.3V$ to $V_{CC} + 0.3V$
Receiver –25V to 25\
Driver/Receiver Enable Pin $-0.3V$ to $V_{CC} + 0.3V$
Output Voltage
Driver – 25V to 25\
Receiver0.3V to V <sub>CC</sub> + 0.3V
Short Circuit Duration
V <sup>+</sup>
V <sup>-</sup>
Driver Output Indefinite
Receiver Output Indefinite
Operating Temperature Range
Commercial (LTC1338C) 0°C to 70°C
Industrial (LTC1338I) –40°C to 85°C
Storage Temperature Range65°C to 150°C
Lead Temperature (Soldering, 10 sec) 300°C

# PACKAGE/ORDER INFORMATION



# DC ELECTRICAL CHARACTERISTICS $v_{cc} = 5V$ , $c1 = c2 = c3 = c4 = 0.1 \mu F$ , unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Any Driver						
Output Voltage Swing	3k to GND Posi Nega		5.0 -5.0	7.0 -6.5		V V
Logic Input Voltage Level	Input Low Level (V <sub>OUT</sub> = High) Input High Level (V <sub>OUT</sub> = Low)	•	2.0	1.4 1.4	0.8	V V
Logic Input Current	$0 \le V_{IN} \le V_{CC}$	•			±5	μА
Output Short-Circuit Current	$V_{OUT} = 0V$			±12		mA
Output Leakage Current	SHUTDOWN, $V_{OUT} = \pm 20V$ (Note 3)	•		±10	±500	μΑ
Any Receiver		•				
Input Voltage Thresholds	Input Low Threshold Input High Threshold	•	0.8	1.3 1.7	2.4	V V
Hysteresis		•	0.1	0.4	1	V
Input Resistance	$-10V \le V_{IN} \le 10V$		3	5	7	kΩ
Output Voltage	Output Low, $I_{OUT} = -1.6$ mA ( $V_{CC} = 5V$ ) Output High, $I_{OUT} = 160\mu$ A ( $V_{CC} = 5V$ )	•	3.5	0.2 4.8	0.4	V
Output Short-Circuit Current	Sinking Current, V <sub>OUT</sub> = V <sub>CC</sub>		-15	-40		mA
Output Leakage Current	SHUTDOWN, $0 \le V_{OUT} \le V_{CC}$ (Note 3)	•		1	10	μΑ
Power Supply Generator	,					
V <sup>+</sup> Output Voltage	I <sub>OUT</sub> = 0mA I <sub>OUT</sub> = 12mA			8.0 7.5		V
V <sup>-</sup> Output Voltage	I <sub>OUT</sub> = 0mA I <sub>OUT</sub> = -12mA			-8.0 -7.0		V V
Supply Rise Time	SHUTDOWN to Turn-On			0.2		ms

# DC ELECTRICAL CHARACTERISTICS $v_{cc} = 5V$ , $c1 = c2 = c3 = c4 = 0.1 \mu F$ , unless otherwise noted.

PARAMETER	CONDITIONS			TYP	MAX	UNITS
Power Supply			•			
V <sub>CC</sub> Supply Current	No Load (All Driver $V_{IN} = V_{CC}$ ) (Note 2) $0^{\circ}C \le T_A \le 70^{\circ}C$			0.5	1.0	mA
	No Load (All Driver $V_{IN} = 0V$ )(Note 2) $0^{\circ}C \le T_A \le 70^{\circ}C$			1.0	1.5	mA
	No Load (All Driver $V_{IN} = V_{CC}$ )(Note 2) $-40^{\circ}C \le T_A \le 85^{\circ}C$			0.5	1.5	mA
	No Load (All Driver $V_{IN} = 0V$ )(Note 2) $-40^{\circ}C \le T_A \le 85^{\circ}C$			1.0	2.0	mA
	RECEIVER ALIVE Mode (Note 4)	•		50	80	μΑ
Supply Leakage Current (V <sub>CC</sub> )	SHUTDOWN (Note 3)	•		0.2	10	μА
Driver/Receiver Enable Threshold Low		•		1.4	0.8	V
Driver/Receiver Enable Threshold High		•	2.0	1.4		V

# AC CHARACTERISTICS $V_{CC}=5V,\ C1=C2=C3=C4=0.1\mu F,\ unless\ otherwise\ noted.$

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Slew Rate	$R_L = 3k, C_L = 51pF$			8	30	V/µs
	$R_L = 3k, C_L = 2500pF$		3	5		V/µs
Driver Propagation Delay	t <sub>HLD</sub> (Figure 1)	•		2	3.5	μS
(TTL to RS232)	t <sub>LHD</sub> (Figure 1)	•		2	3.5	μS
Receiver Propagation Delay	t <sub>HLR</sub> (Figure 2)	•		0.3	0.8	μS
(RS232 to TTL)	t <sub>LHR</sub> (Figure 2)	•		0.2	8.0	μS

The  $\bullet$  denotes specifications which apply over the operating temperature range of 0°C to 70°C or -40°C to 85°C.

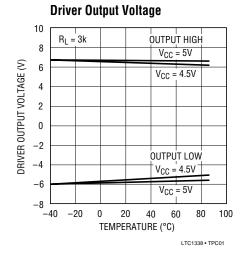
**Note 1:** Absolute maximum ratings are those values beyond which the life of the device may be impaired.

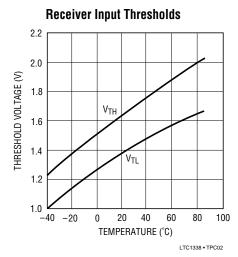
**Note 2:** Supply current is measured with driver and receiver outputs unloaded. The  $V_{DRIVER\ ENABLE}$  and  $V_{RECEIVER\ ENABLE} = V_{CC}$ .

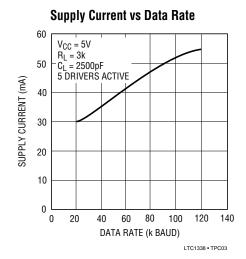
**Note 3:** Supply current and leakage current measurements in SHUTDOWN are performed with  $V_{DRIVER\ ENABLE}$  and  $V_{RECEIVER\ ENABLE} = 0V$ .

**Note 4:** Supply current measurement in RECEIVER ALIVE mode is performed with  $V_{DRIVER\ ENABLE} = 0V$  and  $V_{RECEIVER\ ENABLE} = V_{CC}$ .

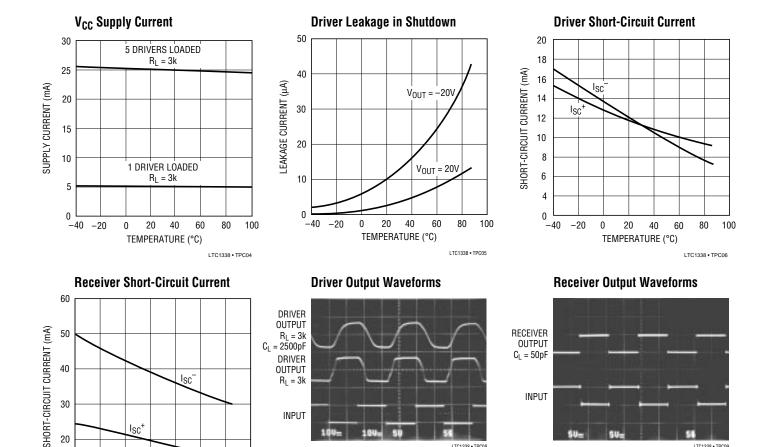
# TYPICAL PERFORMANCE CHARACTERISTICS







# TYPICAL PERFORMANCE CHARACTERISTICS



# PIN FUNCTIONS

-20

**V<sub>CC</sub>**: 5V Input Supply Pin. Supply current less than 0.2μA in the SHUTDOWN mode. This pin should be decoupled with a 0.1µF ceramic capacitor.

100

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40

TEMPERATURE (°C)

60 80

GND: Ground Pin.

20

10

-40

**RECEIVER ENABLE:** TTL/CMOS Compatible Enable Pin. Refer to Table 1 for its functional description. This pin can not float.

**DRIVER ENABLE:** TTL/CMOS Compatible Enable Pin. Refer to Table 1 for its functional description. This pin can not float.

**V**<sup>+</sup>: Positive Supply Output (RS232 Drivers).  $V^{+} \cong 2V_{CC}$  – 1V. This pin requires an external capacitor  $C = 0.1 \mu F$  for

charge storage. The capacitor may be tied to ground or 5V. With multiple devices, the V + and V - pins may be paralleled into common capacitors. For large numbers of devices, increasing the size of the shared common storage capacitors is recommended to reduce ripple.

**V**<sup>-</sup>: Negative Supply Output (RS232 Drivers).  $V^- \cong 2V_{CC}$ -1.5V. This pin requires an external capacitor  $C = 0.1 \mu F$  for charge storage.

C1+, C1-, C2+, C2-: Commutating Capacitor Inputs. These pins require two external capacitors  $C = 0.1 \mu F$ : one from C1+ to C1-, and another from C2+ to C2-. To maintain charge pump efficiency, the capacitor's effective series resistance should be less than  $20\Omega$ .

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# PIN FUNCTIONS

**DRIVER IN:** RS232 Driver Input Pins. Inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to  $V_{CC}$ .

**DRIVER OUT:** Driver Outputs at RS232 Voltage Levels. Outputs are in a high impedance state when in SHUT-DOWN, RECEIVER ALIVE mode or  $V_{CC}$  = 0V. The driver outputs are protected against ESD to  $\pm 10 \text{kV}$  for human body model discharges.

**RX IN:** Receiver Inputs. These pins can be forced to  $\pm 25$ V without damage. The receiver inputs are protected against ESD to  $\pm 10$ kV for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity.

**RX OUT:** Receiver Outputs with TTL/CMOS Voltage Levels. Outputs are in a high impedance state when in SHUT-DOWN or RECEIVER DISABLE mode to allow data line sharing.

**Table 1. Functional Description** 

MODE	RX ENABLE	DR ENABLE	DRIVERS	RECEIVERS	I <sub>CC</sub> (μA)(TYP)
SHUTDOWN	0	0	All driver outputs are high impedance.	All receiver outputs are high impedance.	0.2
Receiver Disable	0	1	All drivers alive.	All receiver outputs are high impedance.	500
RECEIVER ALIVE	1	0	All driver outputs are high impedance.	All receivers alive.	
Normal	1	1	All drivers alive.	All receivers alive.	500

# **SWITCHING TIME WAVEFORMS**

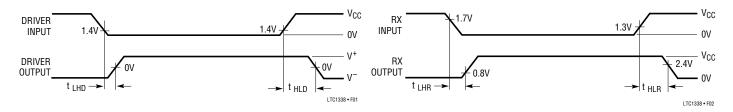


Figure 1. Driver Propagation Delay Timing

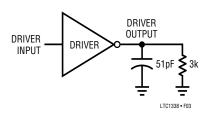
Figure 2. Receiver Propagation Delay Timing

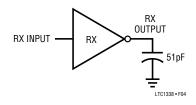


# **TEST CIRCUITS**

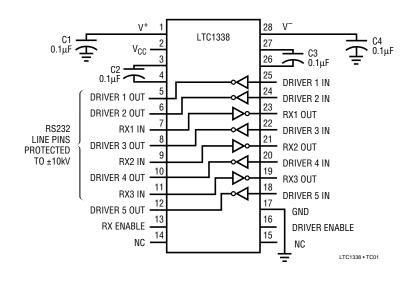
# **Driver Timing Test Load**

# **Receiver Timing Test Load**



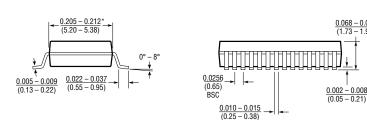


## **ESD Test Circuit**

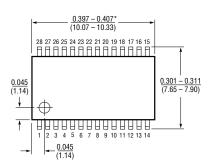


# PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

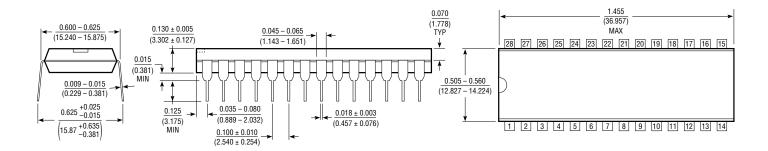
## G Package 28-Lead SSOP



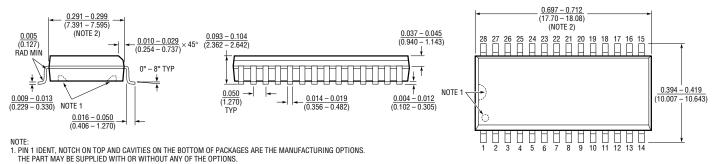
<sup>\*</sup>THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006 INCH (0.15mm)



### N Package 28-Lead Plastic DIP



## S Package 28-Lead SOL



2. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006 INCH (0.15mm).



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