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# MAX77511/MAX77711 10V Input, Quad-Phase Configurable, 3A/Phase, High-Efficiency Buck Converter

## General Description

The MAX77511/MAX77711 are four-phase, 3A/phase, configurable single to quad output, step-down buck regulators for 1s/2s Li+ battery inputs. Output voltage is programmable through an I<sup>2</sup>C interface between 0.25V and 5.2V. The buck has four combinable 3A switching phases ( $\Phi$ ) for up to four regulated outputs. Phases are configurable to higher-current multiphase outputs:  $4\Phi$ ,  $(3+1)\Phi$ ,  $(2+2)\Phi$ ,  $(2+1+1)\Phi$ , or  $(1+1+1+1)\Phi$ .

Six GPIOs can be purposed for I/O expansion or buck status and control. Pseudo-random spread-spectrum modulation suppresses EMI. Soft-start, soft-stop, and DVS slew times are programmable with I<sup>2</sup>C. MAX77711 offers a 300mA linear regulator.

MAX77511/MAX77711 are available in a 64-bump, 3.54mm x 3.54mm wafer-level package (WLP).

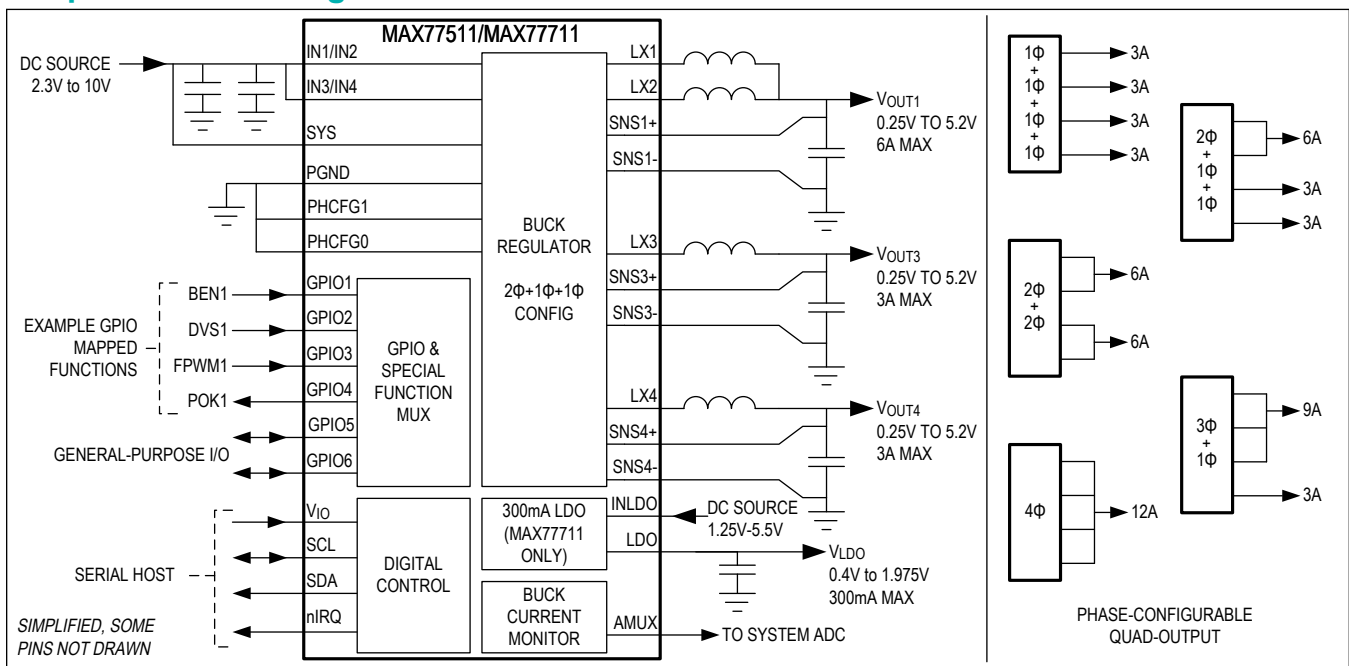
## Applications

- DSLR, Mirrorless, HD Video, and Action Cameras
- 2-Cell Li+/Li-ion Equipment
- Notebook Computers and Robots
- Embedded Microprocessors, FPGAs, or ASICs

## Benefits and Features

- 3A/Phase Configurable Buck Regulator
  - 3A, 6A, 9A, or 12A Output Current Capability
- 2.3V to 10V Input Voltage Range
- 0.25V to 5.2V Output Voltage Range
  - 0.25V to 1.3V (5mV steps) in Low-Range
  - 1V to 5.2V (20mV steps) in High-Range
- Pin Programmable Output/Phase ( $\Phi$ ) Configuration
  - $4\Phi$ ,  $(3+1)\Phi$ ,  $(2+2)\Phi$ ,  $(2+1+1)\Phi$ ,  $(1+1+1+1)\Phi$
- High-Efficiency and Low-Heat
- Uses Small 2520 Inductor
- 1MHz Nominal Switching Frequency per Phase
  - Pseudo-Random Spread-Spectrum Options
- Six Multi-Function GPIOs for Hardware Buck Control
  - Enable, DVS, FPWM, and Power-OK
- 300mA pMOS LDO (MAX77711 Only)
- Built-In Flexible Power Sequencing with Soft-Start/Stop
- Protection Features
  - Hard and Soft-Short Protection, UVLO, and Thermal Protection

## Simplified Block Diagram

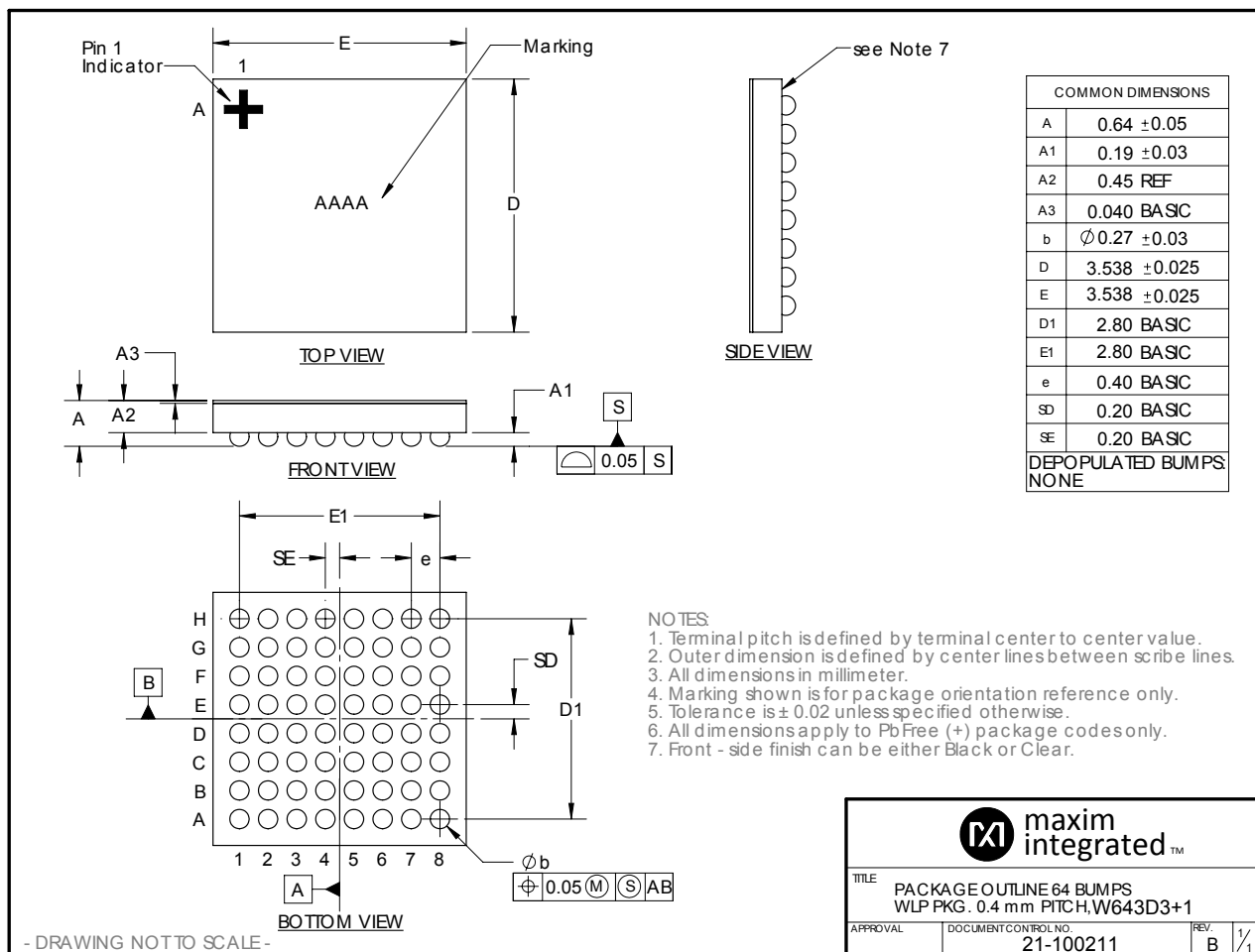


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Package Information

64 WLP

Package Code	W643D3+1
Outline Number	<a href="#">21-100211</a>
Land Pattern Number	Refer to <a href="#">Application Note 1891</a>
<b>Thermal Resistance, Four-Layer Board:</b>	
Junction to Ambient ( $\theta_{JA}$ )	38.20°C/W



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For the latest package outline information and land patterns (footprints), go to [www.maximintegrated.com/packages](http://www.maximintegrated.com/packages). Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to [www.maximintegrated.com/thermal-tutorial](http://www.maximintegrated.com/thermal-tutorial).

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