

## 5.0A Surface Mount Schottky Barrier Rectifiers

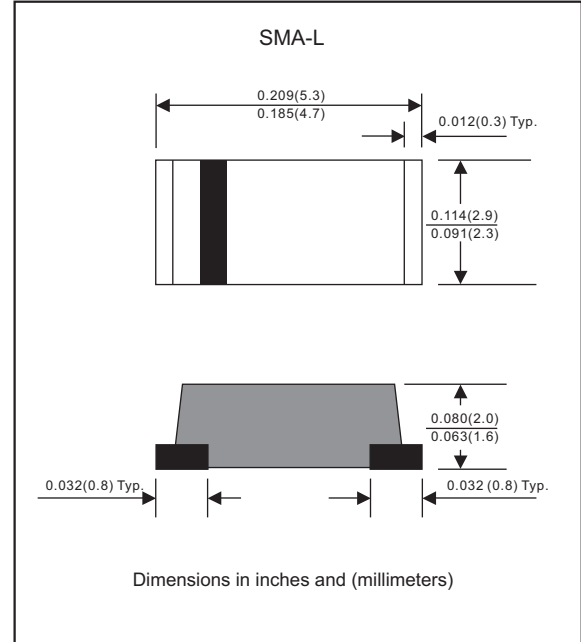
### Features

- Batch process design, excellent power dissipation offers better reverse leakage current and thermal resistance.
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- High surge capability.
- Guardring for overvoltage protection.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228
- Suffix "-H" indicates Halogen-free parts, ex. AS540-AL-H.

### Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMA-L
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.05 gram

### Package outline



### Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	$I_O$			5.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC methode)	$I_{FSM}$			150	A
Reverse current	$V_R = V_{RRM} \quad T_J = 25^\circ\text{C}$	$I_R$			0.5	mA
	$V_R = V_{RRM} \quad T_J = 100^\circ\text{C}$				20	
Thermal resistance	Junction to case	$R_{\theta JC}$		28		$^\circ\text{C/W}$
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	$C_J$		380		pF
Storage temperature		$T_{STG}$	-65		+175	$^\circ\text{C}$

SYMBOLS	$V_{RRM}^{*1}$ (V)	$V_{RMS}^{*2}$ (V)	$V_R^{*3}$ (V)	$V_F^{*4}$ (V)	Operating temperature $T_J$ , ( $^\circ\text{C}$ )
AS540-AL	40	28	40	0.55	-55 to +125
AS560-AL	60	42	60	0.70	-55 to +150
AS5100-AL	100	70	100	0.85	

\*1 Repetitive peak reverse voltage

\*2 RMS voltage

\*3 Continuous reverse voltage

\*4 Maximum forward voltage@ $I_F=5.0\text{A}$

## Rating and characteristic curves (AS540-AL & AS560-AL & AS5100-AL )

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

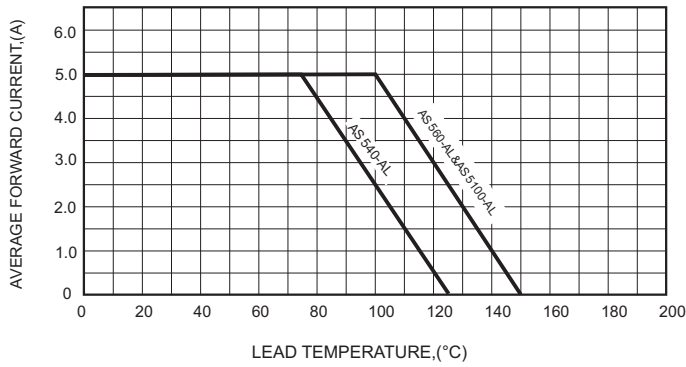


FIG.2-TYPICAL FORWARD CHARACTERISTICS

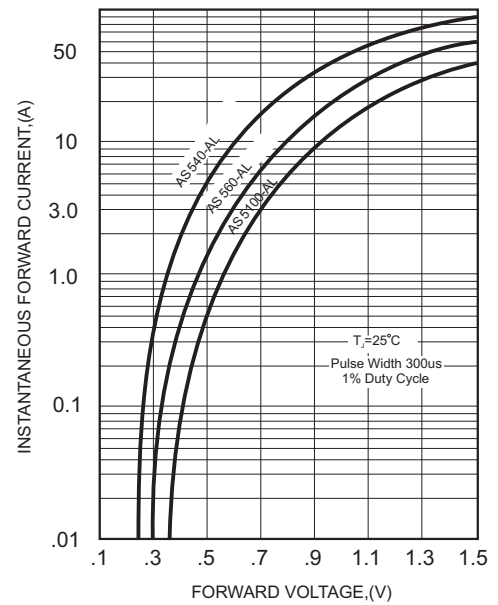


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

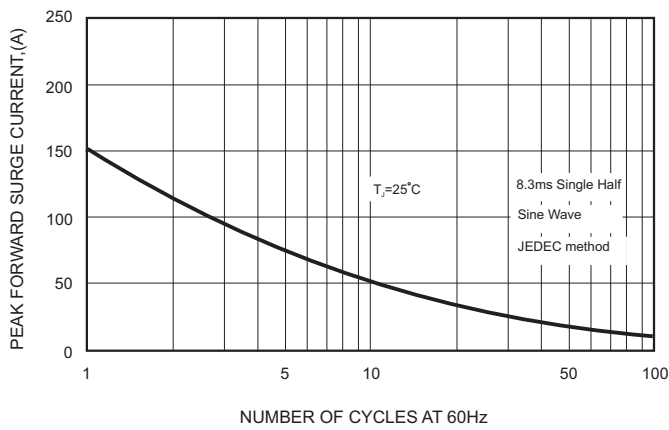


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

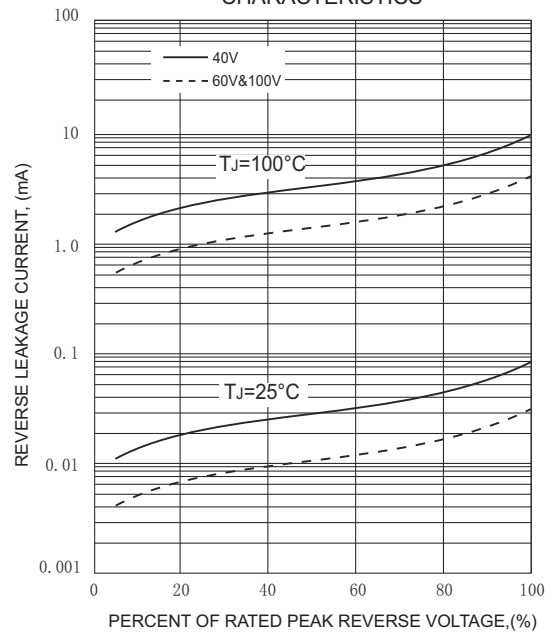
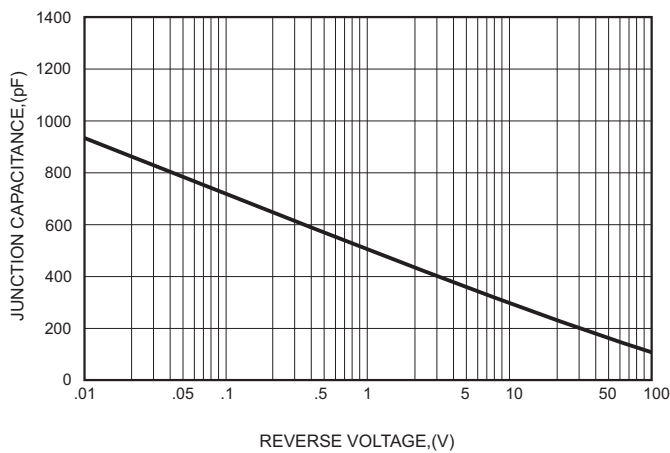




FIG.4-TYPICAL JUNCTION CAPACITANCE



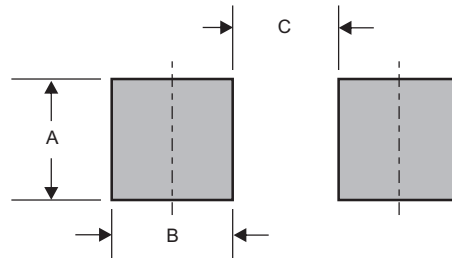
### Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

### Marking

Type number	Marking code
AS540-AL	SS54
AS560-AL	SS56
AS5100-AL	S510

### Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMA-L	0.110 (2.80)	0.059 (1.50)	0.110 (2.80)

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