

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-60V	120mΩ@-10V	-4A
	170mΩ@-4.5V	

Feature

- Advanced trench process technology
- High density cell design for ultra low on-resistance

Application

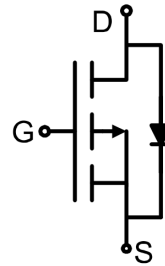
- Load Switch for Portable Devices
- DC/DC Converter

Package

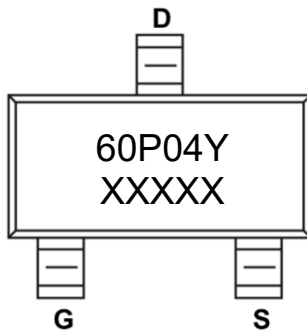


SOT-23-3L

Circuit diagram



Marking



Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-4	A
Pulsed Drain Current	I_{DM}	-12	A
Power Dissipation	P_D	1.5	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	83.3	$^{\circ}C/W$
Junction Temperature	T_J	150	$^{\circ}C$
Storage Temperature	T_{STG}	-55 ~ +150	$^{\circ}C$

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-60			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -60V, V_{GS} = 0V$			-1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1		-3	V
Drain-source on-resistance ¹⁾	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -4A$		107	120	m Ω
		$V_{GS} = -4.5V, I_D = -3A$		138	170	
Dynamic characteristics²⁾						
Input Capacitance	C_{iss}	$V_{DS} = -30V, V_{GS} = 0V, f = 1MHz$		930		pF
Output Capacitance	C_{oss}			85		
Reverse Transfer Capacitance	C_{rss}			35		
Total Gate Charge	Q_g	$V_{DS} = -30V, V_{GS} = -10V, I_D = -4A$		25		nC
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			7		
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -30V, V_{GS} = -10V, R_L = 7.5\Omega, R_{GEN} = 3\Omega$		8		nS
Turn-on rise time	t_r			4		
Turn-off delay time	$t_{d(off)}$			32		
Turn-off fall time	t_f			7		
Source-Drain Diode characteristics						
Diode Forward voltage	V_{DS}	$V_{GS} = 0V, I_S = -4A$			-1.2	V

Notes:

- 1) Pulse Test: Pulse Width < 300 μs , Duty Cycle $\leq 2\%$.
- 2) Guaranteed by design, not subject to production testing.

Typical Characteristics

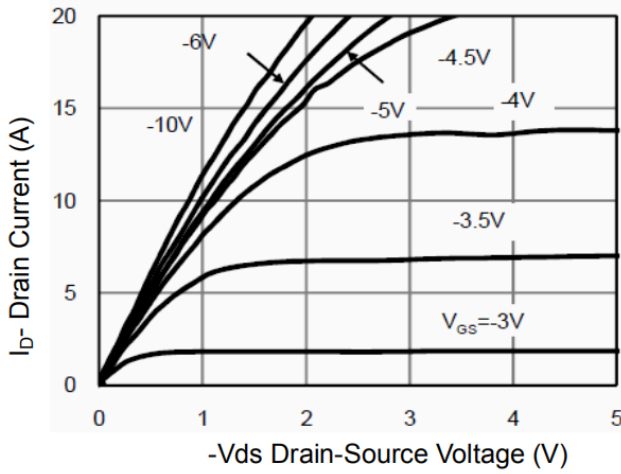


Figure 1 Output Characteristics

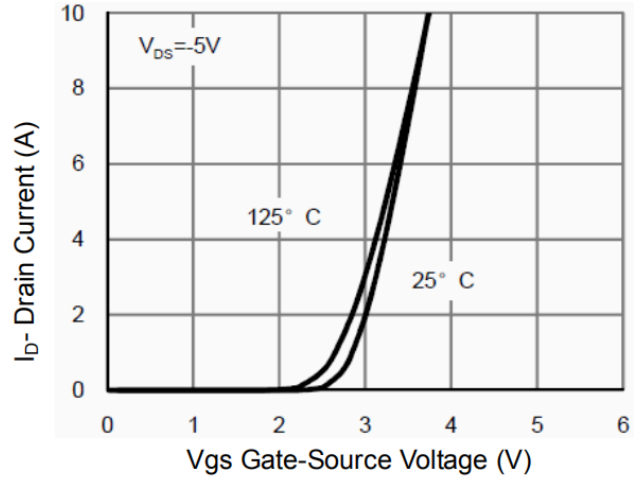


Figure 2 Transfer Characteristics

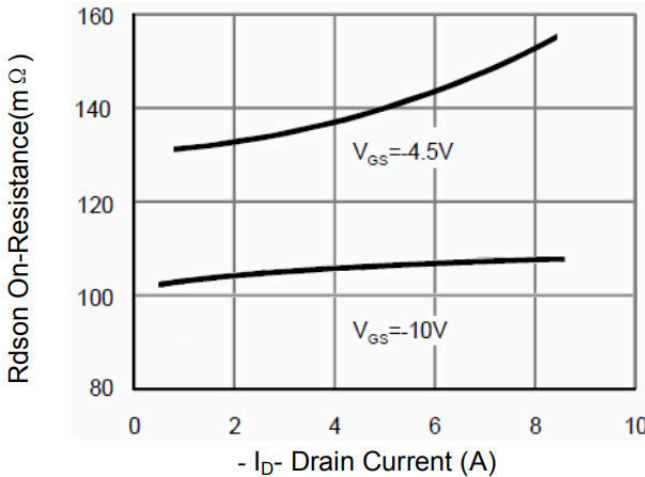


Figure 3 Rdson- Drain Current

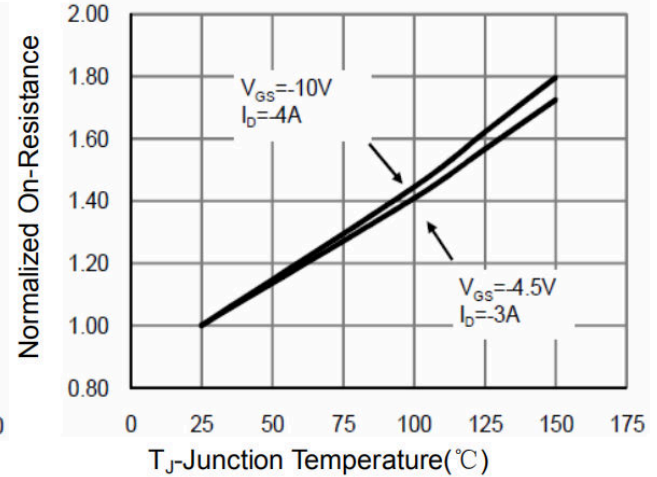


Figure 4 Rdson-Junction Temperature

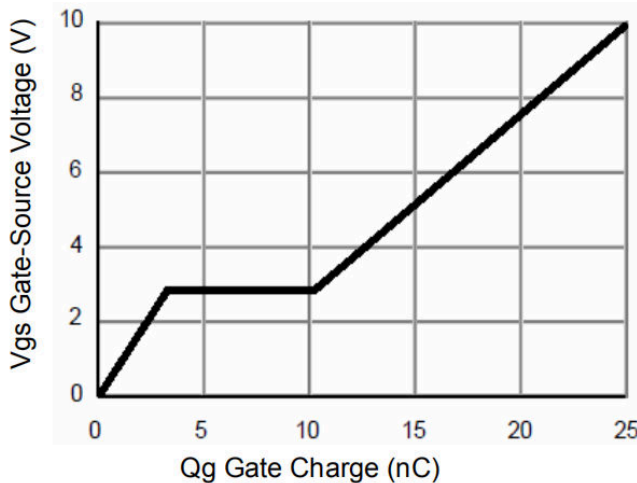


Figure 5 Gate Charge

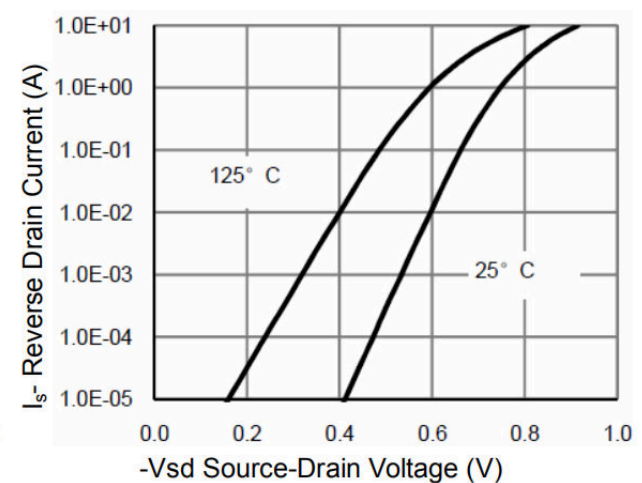


Figure 6 Source- Drain Diode Forward

Typical Characteristics

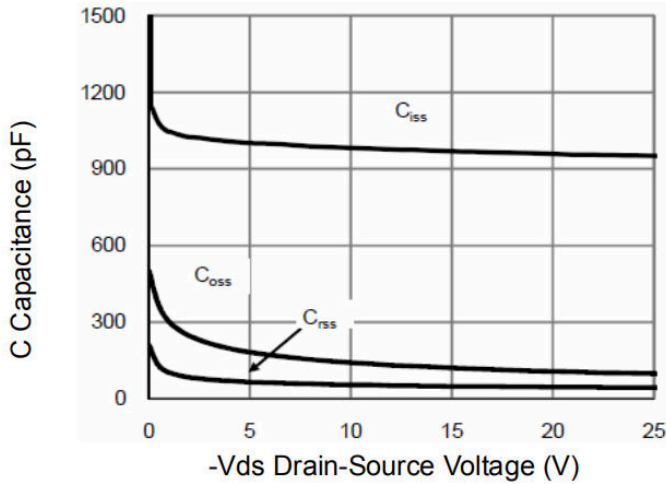


Figure 7 Capacitance vs Vds

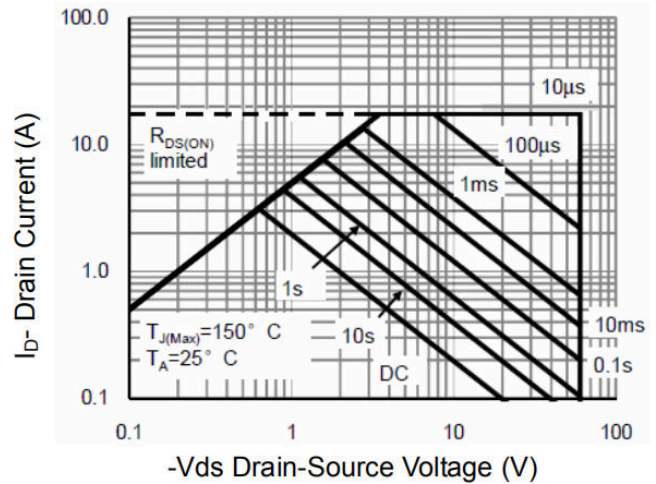


Figure 8 Safe Operation Area

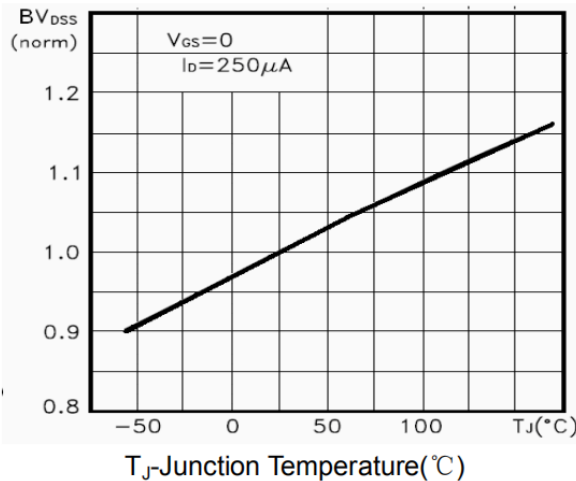


Figure 9 BV_{DSS} vs Junction Temperature

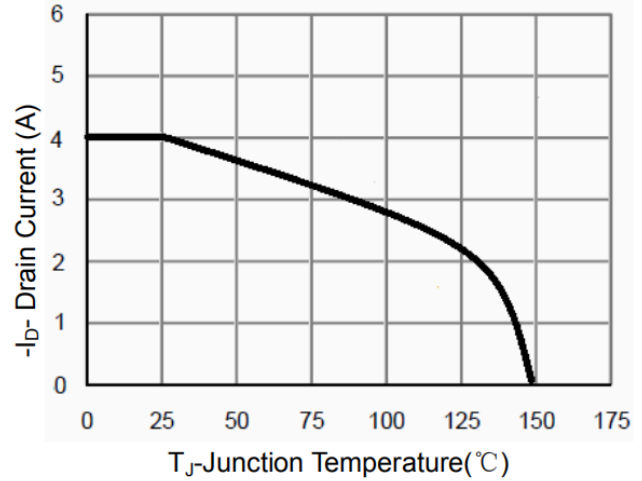


Figure 10 ID Current De-rating

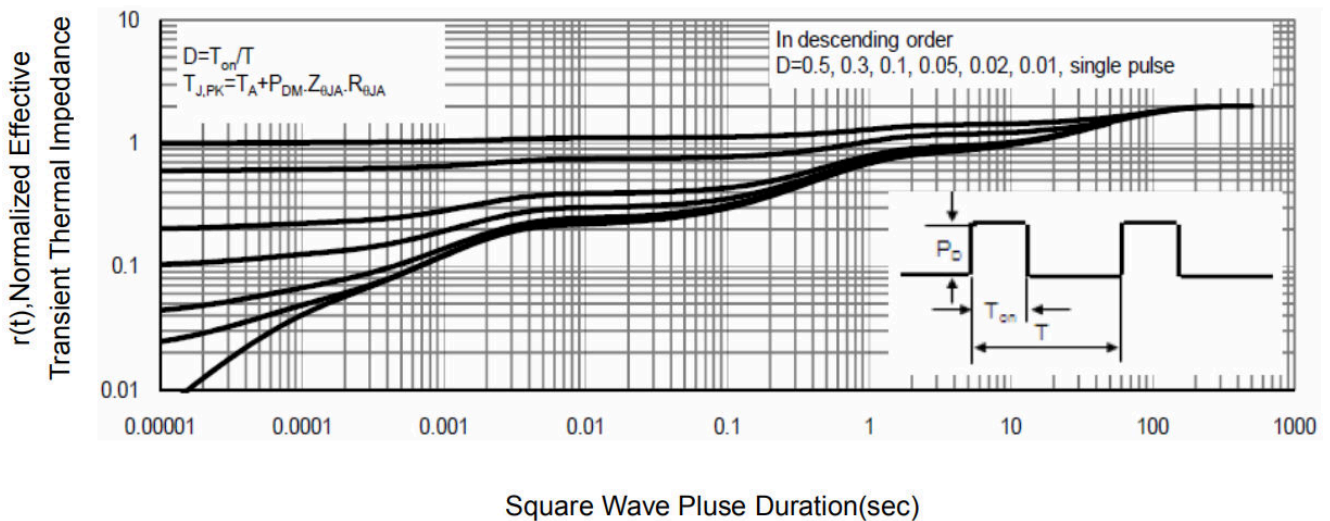
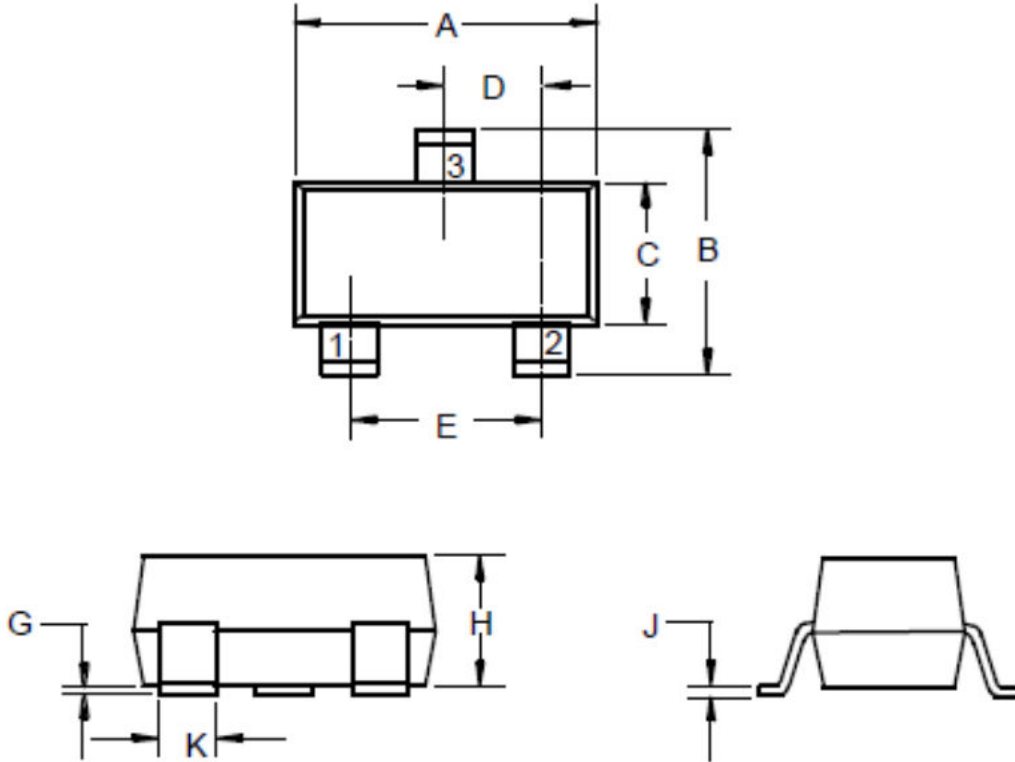


Figure 11 Normalized Maximum Transient Thermal Impedance

SOT-23-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.820	3.020	0.111	0.119
B	2.650	2.950	0.104	0.116
C	1.500	1.700	0.059	0.067
D	0.865	1.015	0.034	0.040
E	1.800	2.000	0.071	0.079
G	0.040	0.100	0.002	0.004
H	1.050	1.250	0.041	0.049
J	0.100	0.200	0.004	0.008
K	0.300	0.500	0.012	0.020

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