

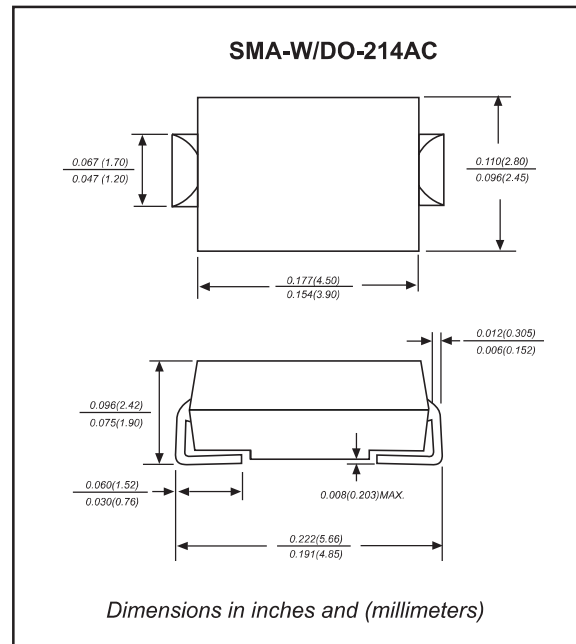
Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Super fast switching for high efficiency
- ◆ Low reverse leakage
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 250°C/10 seconds at terminals
- ◆ Compliant to RoHS Directive 2011/65/EU
- ◆ Suffix "-H" indicates Halogen-free part, ex. ES1J-W-H

Mechanical data

- ◆ **Case:** JEDEC DO-214AC molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

Package outline



Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

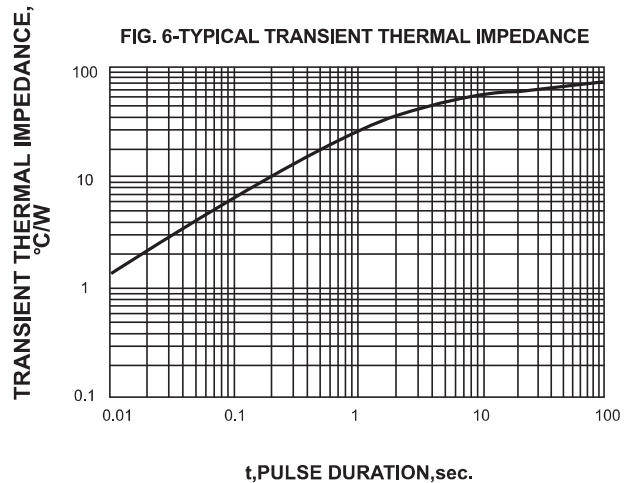
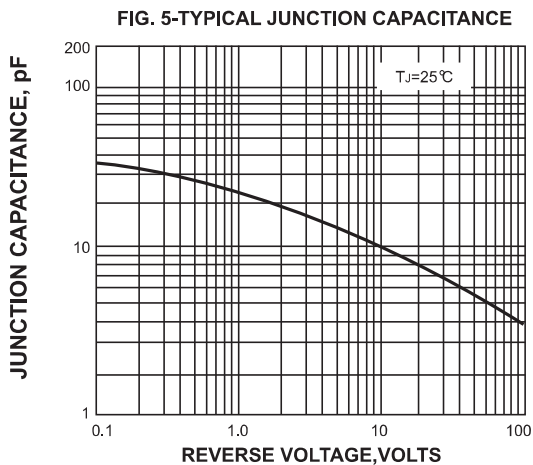
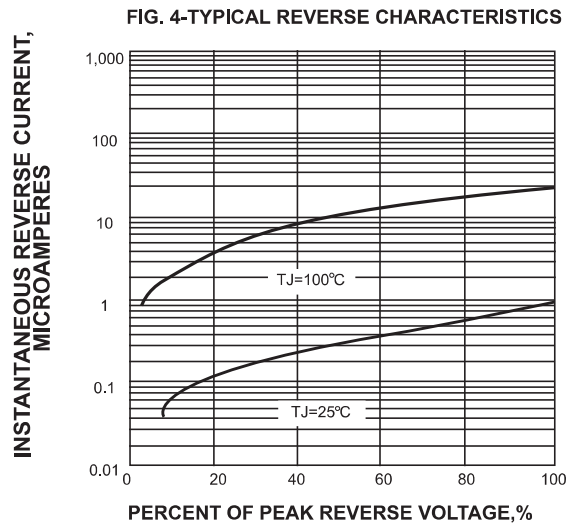
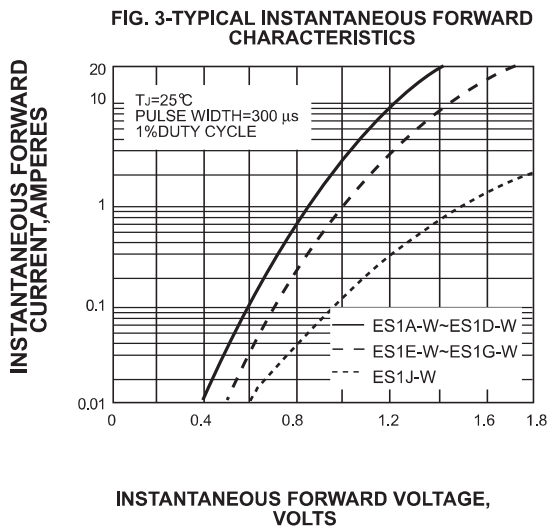
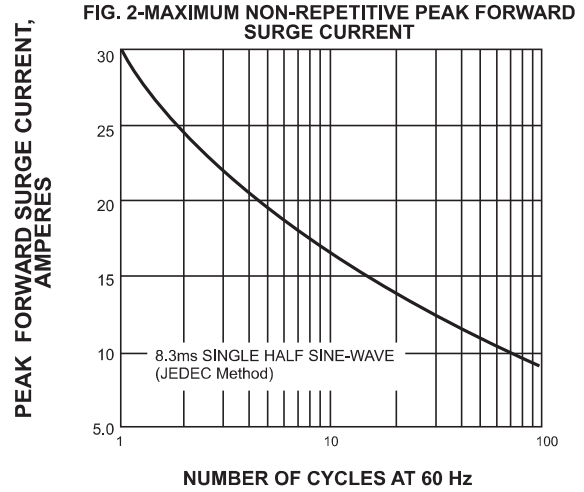
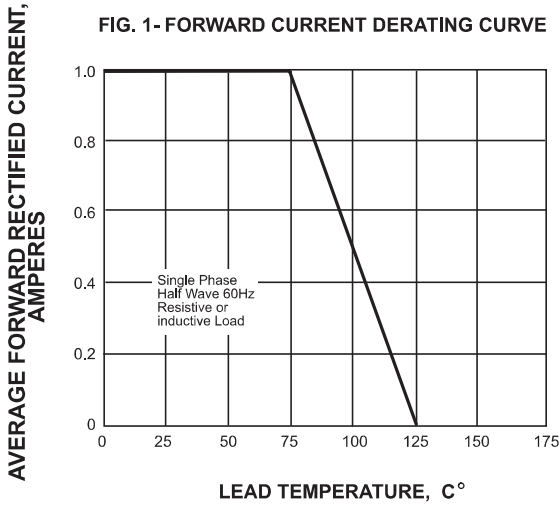
PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I_O			1.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I_{FSM}			30	A
Reverse current	$V_R = V_{RRM}$ $T_A = 25^\circ\text{C}$	I_R			5.0	μA
	$V_R = V_{RRM}$ $T_A = 100^\circ\text{C}$				50	
Thermal resistance	Junction to ambient NOTE 1	$R_{\theta JA}$		60		$^\circ\text{C/W}$
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C_J		15		pF
Storage temperature		T_{STG}	-65		+125	$^\circ\text{C}$

SYMBOLS	V_{RM}^{*1} (V)	V_{RMS}^{*2} (V)	V_R^{*3} (V)	V_F^{*4} (V)	t_{rr}^{*5} (ns)	Operating temperature T_{Jr} ($^\circ\text{C}$)
ES1A-W	50	35	50	0.95	35	-55 to +125
ES1B-W	100	70	100			
ES1C-W	150	105	150			
ES1D-W	200	140	200			
ES1E-W	300	210	300	1.25	35	-55 to +125
ES1G-W	400	280	400			
ES1J-W	600	420	600			



- *1 Repetitive peak reverse voltage
- *2 RMS voltage
- *3 Continuous reverse voltage
- *4 Maximum forward voltage@ $I_F=1.0\text{A}$
- *5 Maximum Reverse recovery time, note 2

Note: 1.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas
2. Reverse recovery time test condition, $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{RR}=0.25\text{A}$

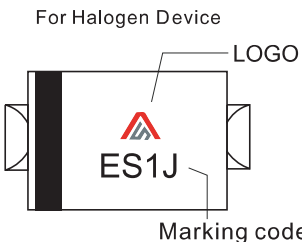
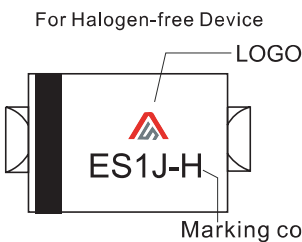
Rating and characteristic curves



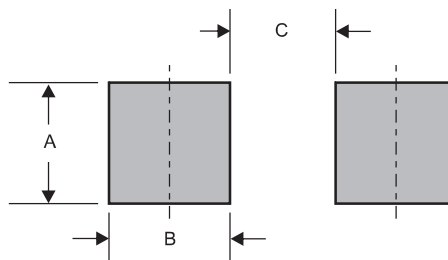
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code	Example	
ES1A-W	ES1A		
ES1B-W	ES1B		
ES1C-W	ES1C		
ES1D-W	ES1D		
ES1E-W	ES1E		
ES1G-W	ES1G		
ES1J-W	ES1J		

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMA-W	0.110 (2.80)	0.063 (1.60)	0.087 (2.20)

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