

US2A-B THRU US2M-B

2.0A Surface Mount High Efficiency Rectifiers-50-1000V

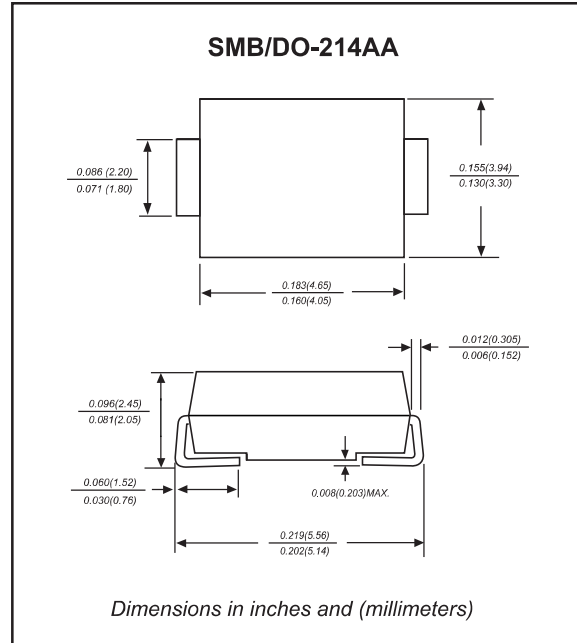
Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Ultra fast switching for high efficiency
- ◆ Low reverse leakage
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 250°C/10 seconds at terminals
- ◆ Glass passivated chip junction
- ◆ Compliant to RoHS Directive 2011/65/EU
- ◆ Suffix "-H" indicates Halogen-free part, ex. US2M-B-H

Mechanical data

- ◆ **Case:** JEDEC DO-214AA molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

Package outline



Maximum ratings and Electrical Characteristics (AT T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I _O			2.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I _{FSM}			50	A
Reverse current	V _R = V _{RRM} T _A = 25°C	I _R			5.0	μA
	V _R = V _{RRM} T _A = 100°C				50	
Thermal resistance	Junction to ambient NOTE 1	R _{θJA}		50		°C/W
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C _J		20		pF
Storage temperature		T _{STG}	-65		+150	°C

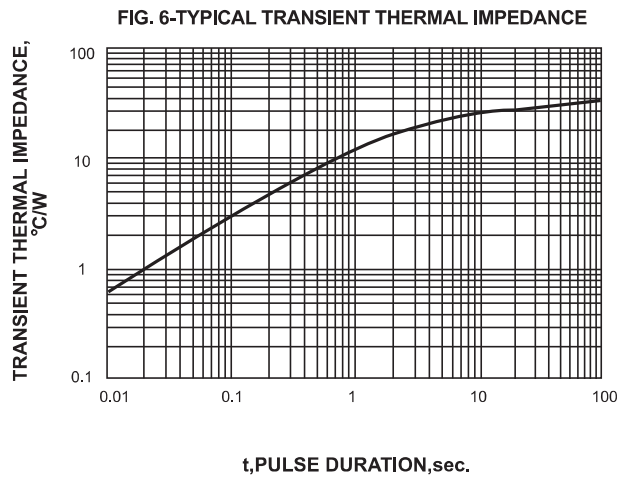
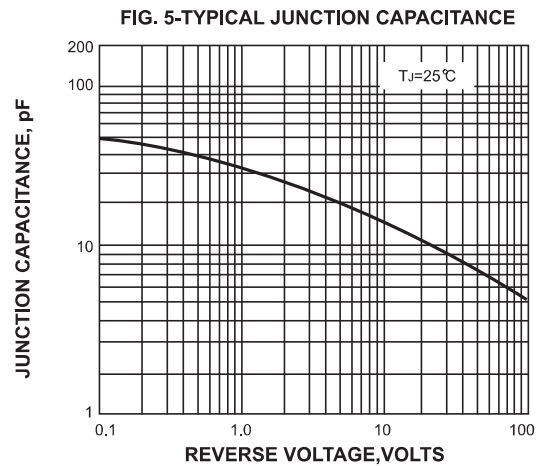
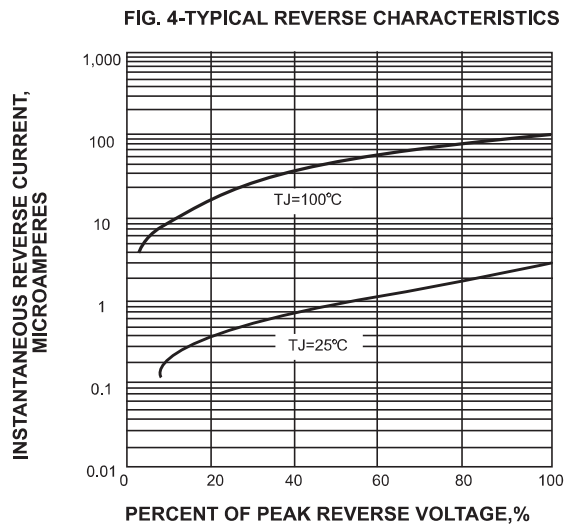
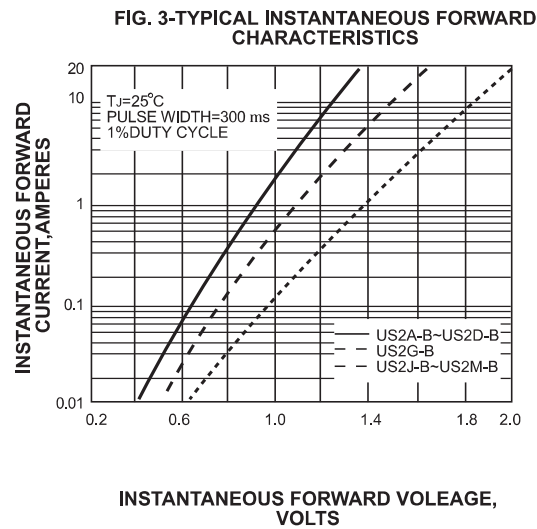
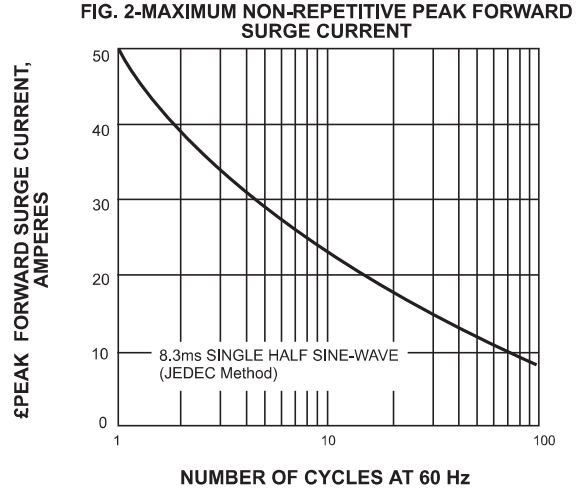
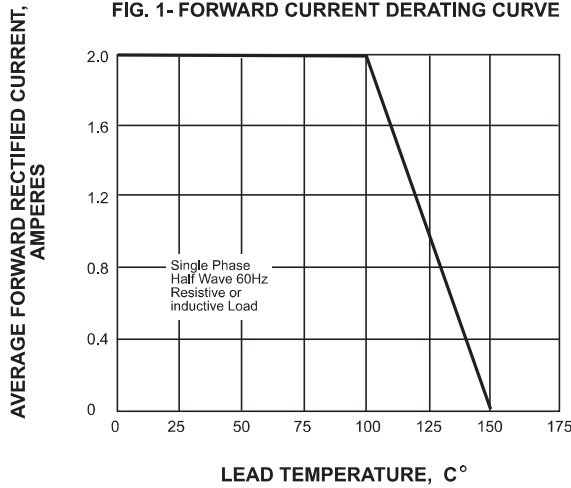
SYMBOLS	V _{RRM} ^{*1} (V)	V _{RMS} ^{*2} (V)	V _R ^{*3} (V)	V _F ^{*4} (V)	t _{rr} ^{*5} (ns)	Operating temperature T _J (°C)
US2A-B	50	35	50	1.00	50	
US2B-B	100	70	100			
US2D-B	200	140	200			
US2G-B	400	280	400	1.40		
US2J-B	600	420	600	1.70	75	
US2K-B	800	560	800			
US2M-B	1000	700	1000			

- *1 Repetitive peak reverse voltage
- *2 RMS voltage
- *3 Continuous reverse voltage
- *4 Maximum forward voltage@I_F=2.0A
- *5 Maximum Reverse recovery time, note 2

Note: 1.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas
2. Reverse recovery time test condition, I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

US2A-B THRU US2M-B

Rating and characteristic curves



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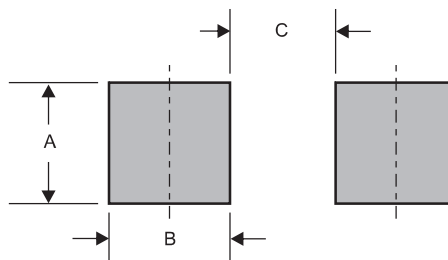
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code	Example	
US2A-B	US2A	For Halogen Device	For Halogen-free Device
US2B-B	US2B		
US2D-B	US2D		
US2G-B	US2G		
US2J-B	US2J		
US2K-B	US2K		
US2M-B	US2M		

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SMB	0.078 (2.00)	0.059 (1.50)	0.110 (2.80)

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