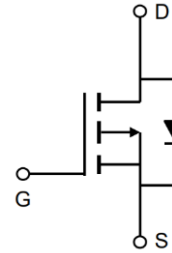


-20V P-Channel Enhancement Mode MOSFET

Description

The AP70P02D uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

$V_{DS} = -20V$ $I_D = -70A$

$R_{DS(ON)} < 9m\Omega$ @ $V_{GS} = -4.5V$

Application

Battery protection

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP70P02D	TO-252-3L	AP70P02D XXX YYYY	2500

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-70	A
$I_D @ T_C = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-35	A
IDM	Pulsed Drain Current ²	-210	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ³	29	W
$P_D @ T_C = 70^\circ C$	Total Power Dissipation ³	19	W
TSTG	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	75	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ ($t \leq 10s$)	40	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	4.2	$^\circ C/W$



-20V P-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	-22	---	V
ΔBVDSS/ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.012	---	V/°C
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-15A	---	6.8	9	mΩ
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =-2.5V, I _D =-10A	---	8.2	11	
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.3	-0.6	-1.0	V
ΔVGS(th)	V _{GS(th)} Temperature Coefficient		---	2.94	---	mV/°C
IDSS	Drain-Source Leakage Current	V _{DS} =-20V, V _{GS} =0V, T _J =25°C	---	---	1	uA
IGSS	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
gfs	Forward Transconductance	V _{DS} =-5V, I _D =-10A	---	43	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-15V, V _{GS} =-4.5V, I _D =-10A	---	63	---	nC
Q _{gs}	Gate-Source Charge		---	9.1	---	
Q _{gd}	Gate-Drain Charge		---	13	---	
Td(on)	Turn-On Delay Time	V _{DD} =-10V, V _{GS} =-4.5V, R _G =3.3Ω, I _D =-10A	---	15.8	---	ns
T _r	Rise Time		---	76.8	---	
Td(off)	Turn-Off Delay Time		---	193	---	
T _f	Fall Time		---	186.4	---	
Ciss	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	---	5783	---	pF
Coss	Output Capacitance		---	509	---	
Crss	Reverse Transfer Capacitance		---	431	---	
IS	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-10.7	A
ISM	Pulsed Source Current ^{2,4}		---	---	-60	A
VSD	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.2	V
trr	Reverse Recovery Time	I _F =-10A, dI/dt=100A/μs, T _J =25°C	---	27	---	nS
Q _{rr}	Reverse Recovery Charge		---	17.8	---	nC



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-20V P-Channel Enhancement Mode MOSFET



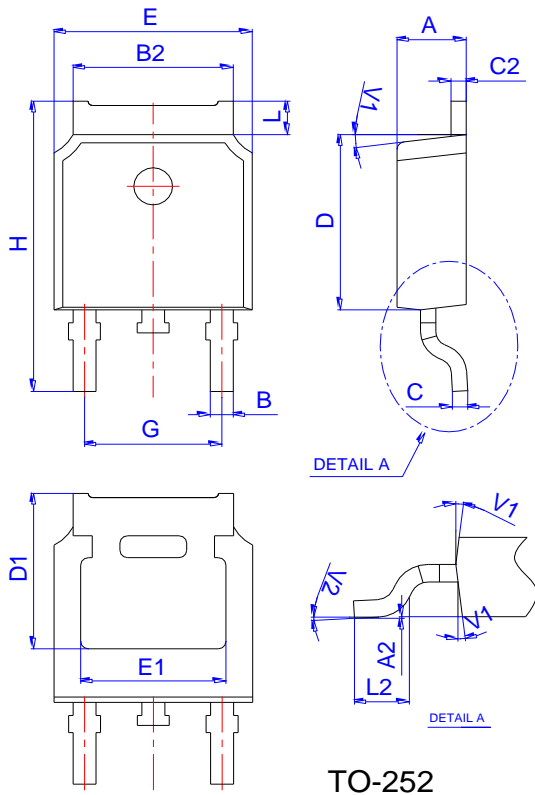
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-20V P-Channel Enhancement Mode MOSFET

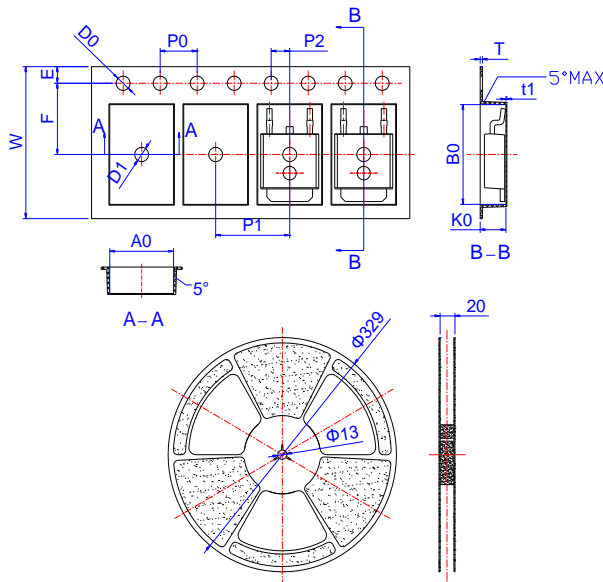
Package Mechanical Data



TO-252

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2		0°	6°	0°	7°	6°

Reel Specification-TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583

-20V P-Channel Enhancement Mode MOSFET**Attention**

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Edition	Date	Change
Rve1.0	2018/1/31	Initial release
Rve1.2	2020/2/01	Reduce RDS(on)

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