DS3000TE-3

3000 W
Distributed Power System

Total Power: 3000 W
Input Voltage: 180 to 264 Vac
# of Outputs: Main and Standby

Special Features
- 3000 W output power
- High-power and narrow form factor
- 6 units can fit in a 19 inch rack for a total of 18 KW
- High-density design: 24 W/in^3
- Active Power Factor Correction
- 80 plus Titanium Efficiency
- EN61000-3-2 harmonic compliance
- Inrush current control
- Hot-pluggable
- N+1 or N+N Redundant
- Active current sharing
- PMBus compliant
- Full digital control
- Accurate input power reporting
- Compatible with Artesyn's Universal PMBus GUI
- Two year warranty
- Conducted/Radiated EMI EN55022 Class A Limits + 6 dB margin
- RoHS Compliance

Safety
UL/cUL 60950-1 2nd Edit.
CSA
Demko+ CB Report EN60950
80 Plus (Titanium)
CE Mark
CHINA CQC
BSMI thru ETC
KC
MSIP (KC EMC)

Product Descriptions

The DS3000TE series bulk front end power AC-DC supply is designed to fully utilize the current capacity of standard IEC C19/C20 AC inlet distribution connectors. It accepts 180–264 Vac input and provides a main 12.1 Vdc output plus a 12 Vdc standby output. Rated at 3000 W when operating from a 208-264 Vac input, the DS3000TE is an 80 Plus Titanium power supply with a very high conversion efficiency of more than 96% at 50% load. Housed in a compact enclosure measuring just 4.15 x 2.78 x 11 inches, the power supply has a power density of 24 W per cubic inch, six units can fit on a standard 19 inch rack shelf to provide a total of 18 KW.

DTS3000TE series power supplies are designed for server, networking, telecom and other redundant power applications. The main 12.1 Vdc output can deliver up to 248 A for DS3000TE-3 and 244 A for DS3000TE-3-001 (while the standby power limited to 54 W max) meanwhile stays within regulation down to zero load, making it perfect for feeding downstream DC-DC converters in systems that use distributed power architectures (DPA). Active current sharing enables multiple supplies to be paralleled for very high current applications without need of additional components. The DS3000TE power supply is hot pluggable and ideal for fault tolerant, N+1 and N+N redundant applications.

This digitally-controlled power supply is PMBus™ compliant. A built-in I^{2}C serial interface allows set-up, monitoring and control to be performed using Artesyn Embedded Technologies' universal PMBus™ graphical user interface.

ARTESYN™
EMBEDDED TECHNOLOGIES

Model Numbers

Standard	Output Voltage	Minimum Load[1]	Maximum Load	Standby Supply	Air Flow Direction
DS3000TE-3	12.1Vdc	1A	248A	12.0Vdc @ 4.5A	Forward (DC Connector to Red Handle)
DS3000TE-3-001	12.1Vdc	1A	244A	12.0Vdc @ 4.5A	Reverse (Blue Handle to DC Connector)

Note 1 - Minimum current for transient load response testing only. Unit is designed to operate and be within output regulation range at zero load.

Options

None

Electrical Specifications

Absolute Maximum Ratings

Stress in excess of those listed in the "Absolute Maximum Ratings" may cause permanent damage to the power supply. These are stress ratings only and functional operation of the unit is not implied at these or any other conditions above those given in the operational sections of this TRN. Exposure to any absolute maximum rated condition for extended periods may adversely affect the power supply's reliability.

Table 1. Absolute Maximum Ratings:

Parameter	Model	Symbol	Min	Typ	Max	Unit
Input Voltage AC continuous operation	All models	$V_{IN,AC}$	180	-	264	Vac
Maximum Output Power (Main + Stand-by)	DS3000TE-3	$P_{O,max}$	-	-	3000	W
	DS3000TE-3-001		-	-	2960	W
Isolation Voltage Input to outputs Input to safety ground	 All models All models		 3000 1500	 - -	 - -	 Vac Vac
Ambient Operating Temperature	All models	T_A	0	-	+40[1]	^{O}C
Storage Temperature	All models	T_{STG}	-40	-	+90	^{O}C
Operating Relative Humidity (non-condensing) Operating Non-operating	 All models All models		 5 5	 - -	 95 95	 % %
Altitude Operating Storage	 All models All models		 - -	 - -	 13200 50000	 feet feet

Note 1 - DS3000TE-3: 3000W from 0 to 40^{O}C, derate output power by 2.5% per ^{O}C from 40^{O}C to 50^{O}C.
DS3000TE-3-001: 2960W from 0 to 40^{O}C, derate output power by 2.5% per ^{O}C from 40^{O}C to 50^{O}C.

Input Specifications

Table 2. Input Specifications:

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Operating AC Input Voltage[1]	World wide	$V_{IN,AC}$	187	230	264[2]	Vac
	Japan only		180	230	264[2]	Vac
Input AC Frequency	All	$f_{IN,AC}$	47	50/60	63	Hz
Turn-on Voltage	All		166	-	180	Vac
Turn-off Voltage	All		160	-	174	Vac
Input Under Voltage	All		170	-	179	Vac
Turn-on Delay (AC to main output)	All		1	-	2.5	Sec
Hold-up Time	$I_O = I_{O,max}$, $I_{SB} = I_{SB,max}$		11	-	-	mS
Maximum Input Current ($I_O = I_{O,max}$, $I_{SB} = I_{SB,max}$)	$V_{IN,AC}$ = 200Vac	$I_{IN,max}$	-	-	16	A_{RMS}
Standby Input Current (V_O = Off, I_{SB} = 0A)	All	$I_{IN,standby}$	-	-	410	mA_{RMS}
Standby Input Power (V_O = Off, I_{SB} = 0A)	All	$P_{IN,standby}$	-	-	25	W
No Load Input Current (V_O = On, I_O = 0A, I_{SB} = 0A)	All	$I_{IN,no-load}$	-	-	500	mA_{RMS}
Harmonic Line Currents	All	THD	Per EN61000-3-2			
Power Factor	90% load and above	PF	-	0.97	-	
Startup Surge Current (Inrush) @ 25°C	$V_{IN,AC}$ = 264Vac	$I_{IN,surge}$	-	-	55	A_{PK}
Input Fuse	Internal, 6.99 x 32.72 mm, Quick Acting 250Vac		-	20	-	A
Leakage Current to Earth Ground (Touch current per unit)	$V_{IN,AC}$ = 240Vac $f_{IN,AC}$ = 60Hz		-	-	0.58	mA
Operating Efficiency at 0°C to 40°C	$V_{IN,AC}$ = 230Vac I_O = 10% of $I_{O,max}$ I_O = 20% of $I_{O,max}$ I_O = 50% of $I_{O,max}$ I_O = 100% of $I_{O,max}$	η	 - - - -	 90 94 96 91	 - - - -	 % % % %
System Stability Phase Margin Gain Margin			 45 -	 - -	 - 12	 Ø dB

Note 1 - The power supply will operate over the entire input voltage range. Voltages are measured at the power supply AC inlet.
Note 2 - The power supply will continue to work when 264Vac < VIN ≤ 276Vac). The ACOK signal and PMBus registers will set to indicate over voltage. The power supply will shutdown when input over voltage.

Output Specifications

Table 3. Output Specifications:

Parameter		Conditions	Symbol	Min	Typ	Max	Unit
Factory Set Voltage		All	$\%V_O$	-0.2	-	+0.2	%
			$\%V_{SB}$	-2.0	-	+2.0	
Output Regulation		Inclusive of set-point, temperature change, warm-up drift and dynamic load	V_O	11.5	12.1	12.7	Vdc
			V_{SB}	11.1	12.0	12.9	
Output Ripple, pk-pk		Measure with a 0.1uF ceramic capacitor in parallel with a 10uF tantalum capacitor, 0 to 20MHz bandwidth	V_O	-	-	150	mV_{PK-PK}
			V_{SB}	-	-	120	
Output Current	DS3000TE-3	$V_{IN,AC}$ = 187-264Vac $V_{IN,AC}$ = 180-264Vac	I_O	0 0	- -	248 223	A
	DS3000TE-3-001	$V_{IN,AC}$ = 187-264Vac $V_{IN,AC}$ = 180-264Vac	I_O	0 0	- -	244 220	
	All models		I_{SB}	0.5	-	4	
V_O Current Share Accuracy[1] (per unit)		10% to 100% of $I_{O,max}$		-	-	12	A
V_O Minimum Current Share Loading (per unit)		All		10	-	-	$\%I_{O,max}$
Number of Parallel Units		Main Output "ISHARE" Connected		-	-	12	
Load Capacitance		Start up	V_O	1000	-	17000	uF
			V_{SB}	27	-	620	uF
V_O Dynamic Response Peak Deviation		50% load change, Slew rate = 0.5A/uS, Minimum allowable output capacitance of 2000uF	$\%V_O$	-4.2	-	+4.2	%
Predicted MTBF		Telcordia SR232 at 40 ^OC, nominal input, full load		400	-	-	K Hrs
Electrolytic Capacitor Life		40 ^OC ambient temperature, nominal input, 80% load		5	-	-	Years
Fan L10 Life[2]		40 ^OC ambient temperature		50	-	-	K Hrs

Note 1 - maximum difference between any two supplies.
Note 2 - a failure rate measure given in time period before 10% failure.

Output Specifications

Table 3. Output Specifications, cont's:

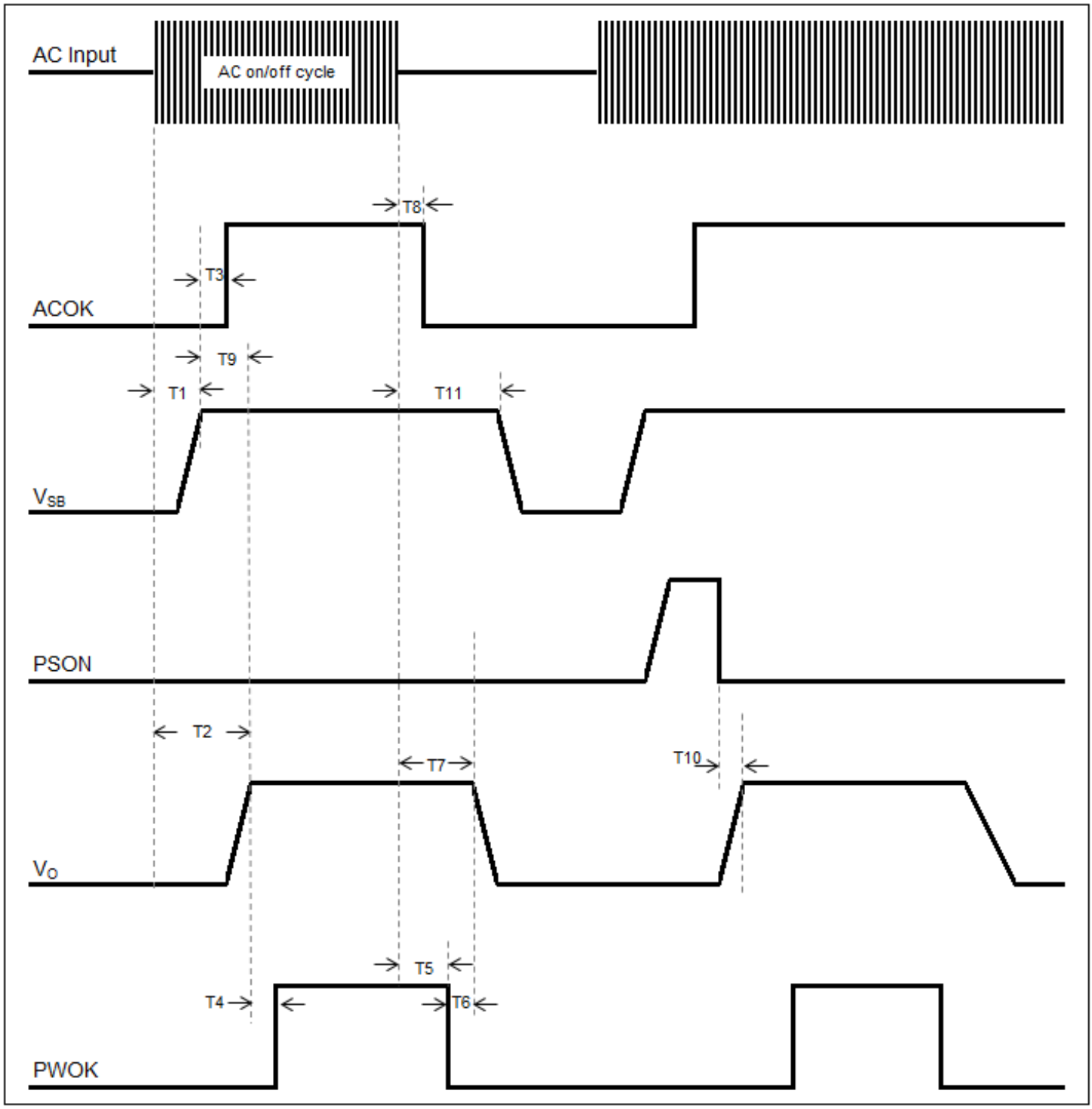
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Minimum Life Expectancy	40 ^{O}C ambient temperature, nominal input, 80% load, excluding fan		5	-	-	Years
Acoustic Noise	230Vac input, half load, 40 ^{O}C ambient temperature, 6000ft		-	-	54	dB
	All		-	-	76	dB

System Timing Specifications

Table 4. System Timing Specifications:

Label	Parameter	Min	Typ	Max	Unit
T1	Delay from AC being applied to Standby output being within regulation.	-	-	2000	mSec
T2	Delay from AC being applied to main output voltage being within regulation.	-	-	2500	mSec
T3	Delay from Standby output to ACOK assertion.	-	-	20	mSec
T4	Delay from main output voltage within regulation limits to PWOK asserted.	100	-	1000	mSec
T5	Delay from loss of AC to deassertion of PWOK	10	-	-	mSec
T6	Delay from deassertion of PWOK to main output voltage falling out of regulation due to AC loss.	1	-	990	mSec
T7	Delay from loss of AC to main output voltage falling out of regulation.	11	-	1000	mSec
T8	Delay from loss of AC to assertion of ACOK.	-	-	100	mSec
T9	Delay from Standby output to main output voltage being within regulation.	-	-	300	mSec
T10	Delay from PSON assertion to main output voltage being within regulation.	-	-	150	mSec
T11	Delay from loss of AC to Standby output voltage going out of regulation.	150	-	1200	mSec

System Timing Specifications



DS3000TE-3 Performance Curves

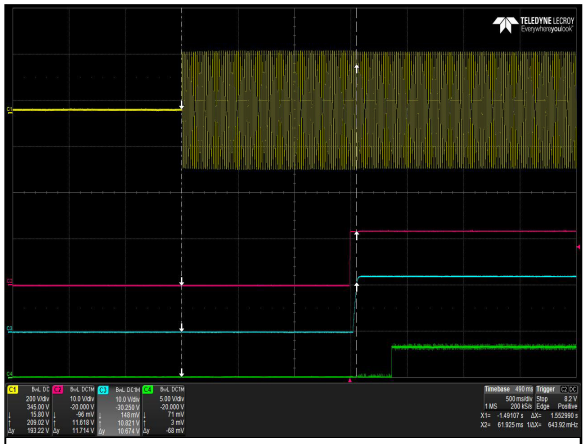


Figure 1: DS3000TE-3 Turn-on delay via AC mains – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: V_{SB} Ch 3: V_O Ch 4: PWOK

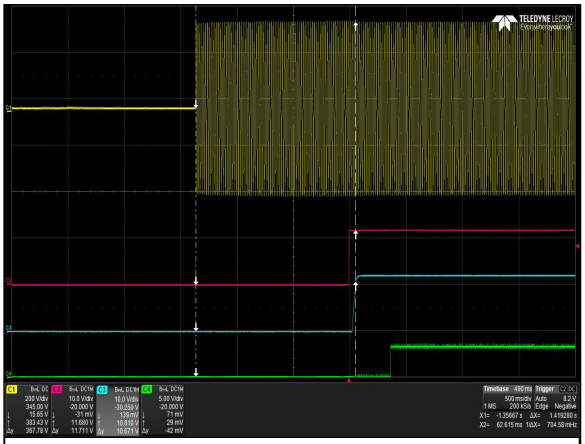


Figure 2: DS3000TE-3 Turn-on delay via AC mains – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: V_{SB} Ch 3: V_O Ch 4: PWOK

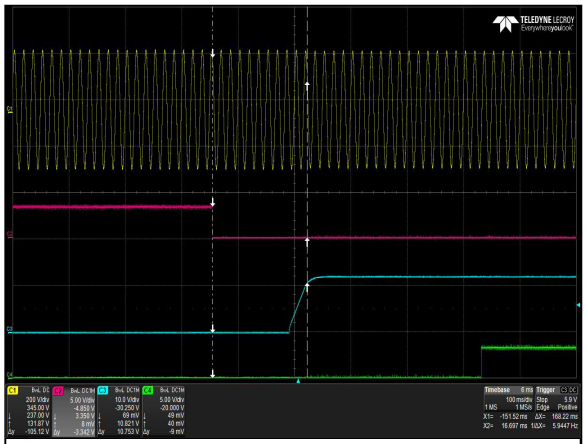


Figure 3: DS3000TE-3 Turn-on delay via PSON – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: PSON Ch 3: V_O Ch 4: PWOK

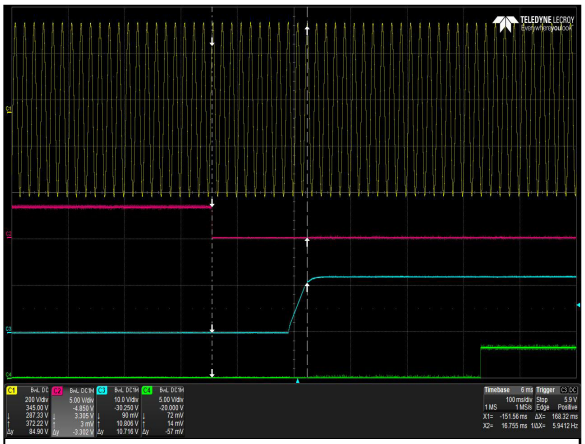


Figure 4: DS3000TE-3 Turn-on delay via PSON – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: PSON Ch 3: V_O Ch 4: PWOK

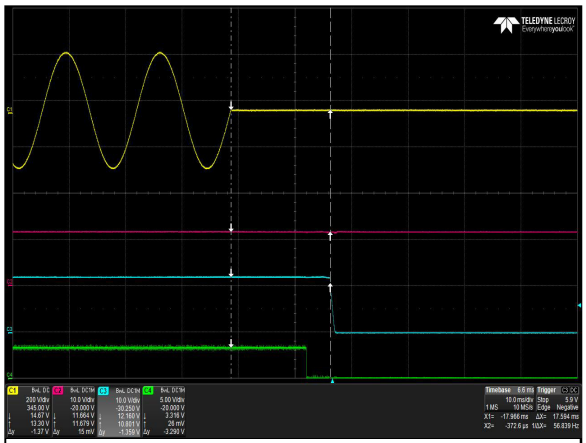


Figure 5: DS3000TE-3 Hold-up Time – Vin = 180Vac / 63Hz / 0°
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: V_{SB} Ch 3: V_O Ch 4: PWOK

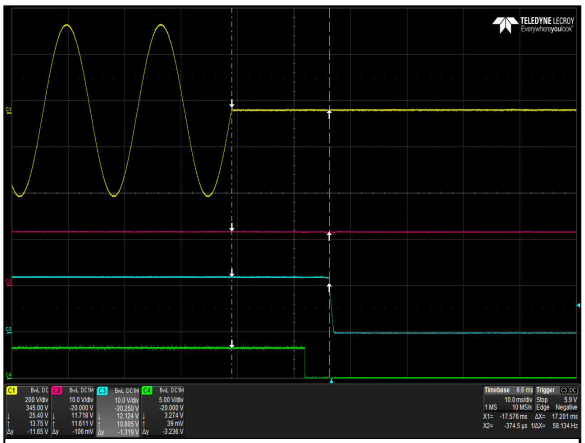


Figure 6: DS3000TE-3 Hold-up Time – Vin = 264Vac / 47Hz / 0°
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: AC Mains Ch 2: V_{SB} Ch 3: V_O Ch 4: PWOK

DS3000TE-3 Performance Curves

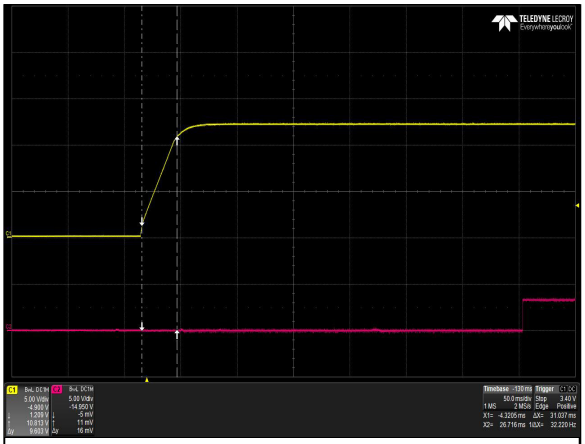


Figure 7: DS3000TE-3 Output Voltage Startup – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: V_O Ch 2: PWOK

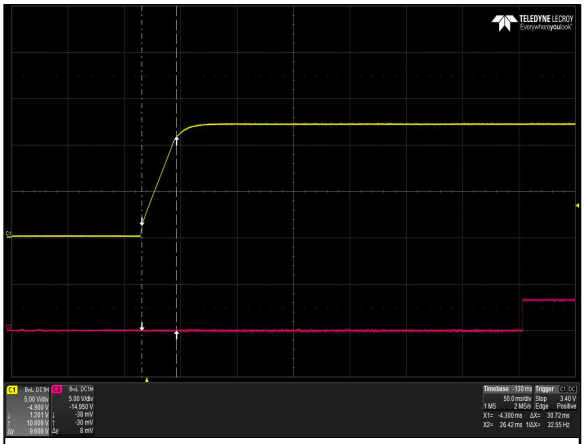


Figure 8: DS3000TE-3 Output Voltage Startup – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: V_O Ch 2: PWOK

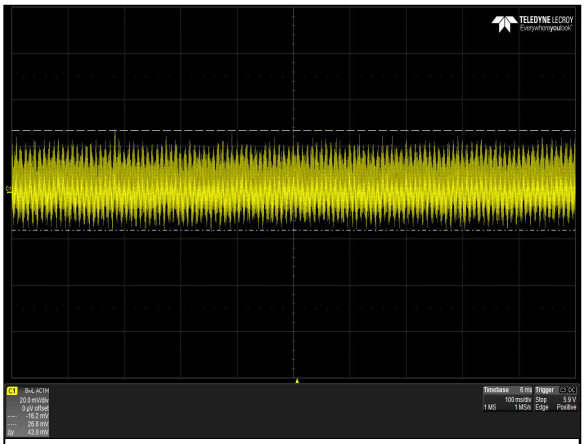


Figure 9: DS3000TE-3 Ripple and Noise Measurement – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: V_O

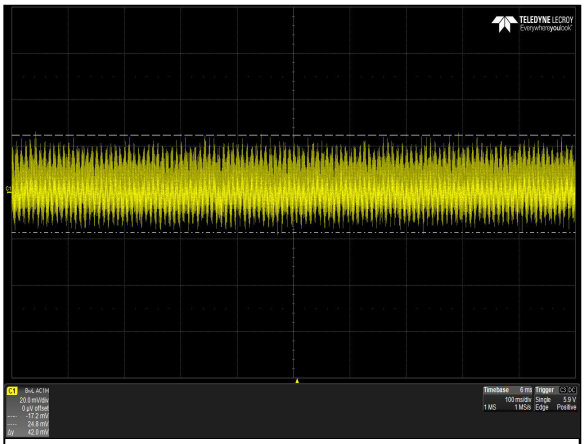


Figure 10: DS3000TE-3 Ripple and Noise Measurement – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: V_O

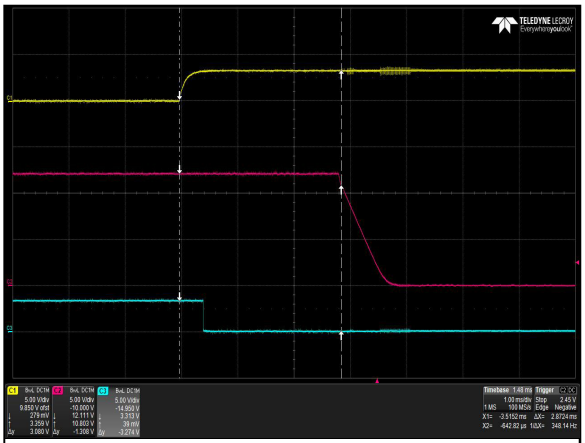


Figure 11: DS3000TE-3 Turn Off Characteristic via PSON – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: PSON Ch 2: V_O Ch 3: PWOK

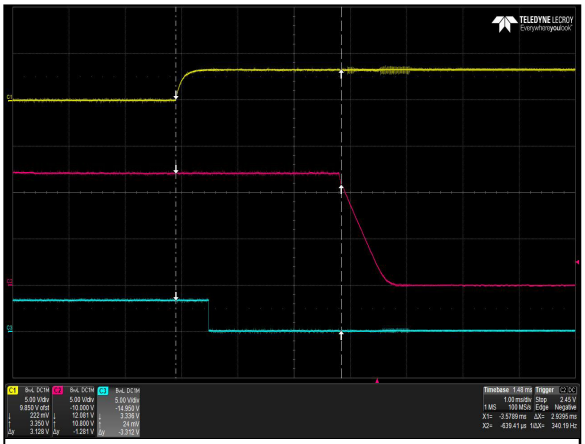


Figure 12: DS3000TE-3 Turn Off Characteristic via PSON – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: PSON Ch 2: V_O Ch 3: PWOK

DS3000TE-3 Performance Curves

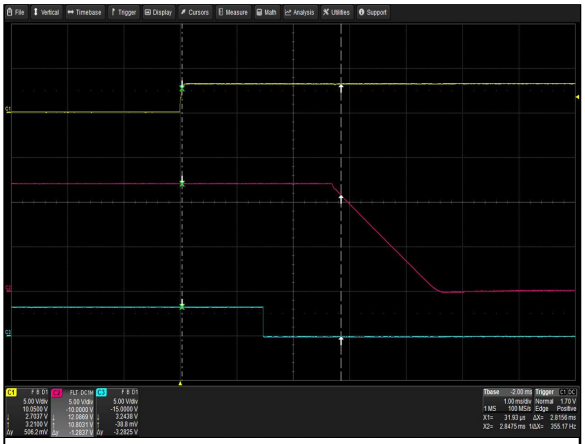


Figure 13: DS3000TE-3 Turn Off Characteristic via PSKILL – Vin = 180Vac
Full Load: I_O = 223A, I_{SB} = 4.5A
Ch 1: PSKILL Ch 2: V_O Ch 3: PWOK

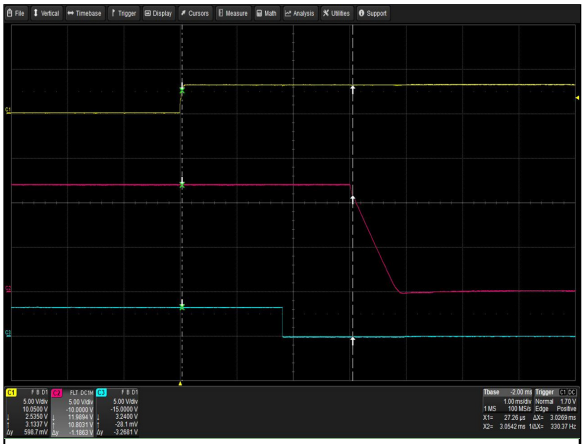


Figure 14: DS3000TE-3 Turn Off Characteristic via PSKILL – Vin = 264Vac
Full Load: I_O = 248A, I_{SB} = 4.5A
Ch 1: PSKILL Ch 2: V_O Ch 3: PWOK

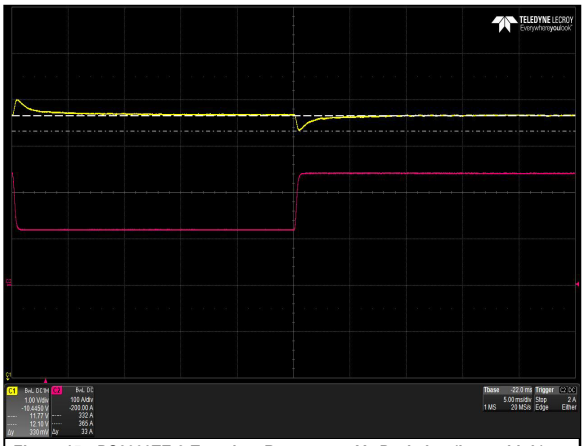


Figure 15: DS3000TE-3 Transient Response – Vo Deviation (low to high)
50% to 100% load change, 0.5A/uS slew rate, Vin = 230Vac
Ch 1: V_O Ch 2: I_O Output capacitance=1000uF

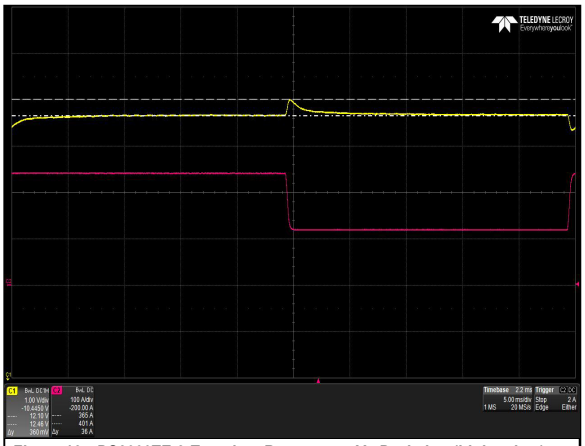


Figure 16: DS3000TE-3 Transient Response – Vo Deviation (high to low)
100% to 50% load change, 0.5A/uS slew rate, Vin = 230Vac
Ch 1: V_O Ch 2: I_O Output capacitance=1000uF

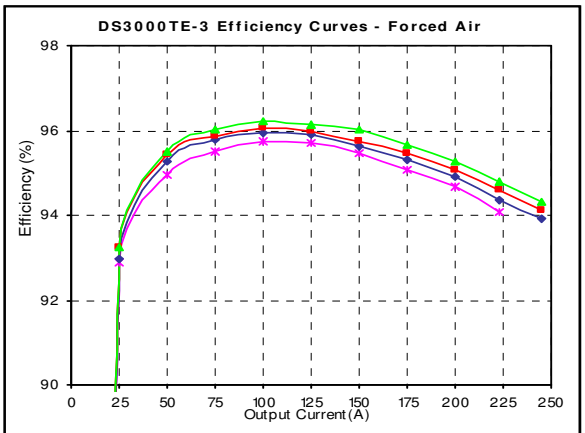


Figure 17: DS3000TE-3 Efficiency Curves @ 25°C
180Vac 220Vac 240Vac 264Vac
Loading: I_O = 10% of Io,max increment to 248A

Protection Function Specification

Input Fusing

DS3000TE-3 series are equipped with an internal non user serviceable 20A Fast Acting 250Vac fuse to IEC 127 for fault protection in the L line input.

Over Voltage / Under Voltage Protection (OVP / UVP)

The power supply will provide latch mode over and under voltage protection as defined by the output under voltage and output over voltage parameters for each output. A fault on the main output will not cause the Standby Output to shutdown.

OVP

Parameter	Min	Nom	Max	Unit
V_O Output Overvoltage	14.0	/	15.0	V
V_{SB} Output Overvoltage	14.0	/	15.0	V

UVP

Parameter	Min	Nom	Max	Unit
V_O Output Undervoltage	9.6	/	10.5	V
V_{SB} Output Overvoltage	9.2	/	10.1	V

Over Current Protection (OCP)

DS3000TE-3 series include internal current limit circuitry to prevent damage in the event of overload or short circuit. Recovery must be automatic when the overload is removed if the overload lasts for 200ms or less, and if it is less than or equal to 270A, otherwise it will latch. If the overload is over 270A, or lasts over 200ms, the power supply will shutdown within 10ms. The supply will continue to auto retry to power up under the following conditions:
a) The off time in between retries will be at least 15 seconds.
b) The off time in between retires if necessary can be set longer by the supply microchip to avoid heating up the power supply.

A fault in the main output will not cause the standby output to shut down. No damage will happen to the supply as the result of either short term or long term overload of the outputs.

The standby output will have an OCP limit from 111% to 155% (5A-7A) and will auto-recover when the overload is removed. A fault in the standby output will shutdown the main output and will auto-recover as well when the overload on the standby is removed.

Short Circuit Protection (SCP)

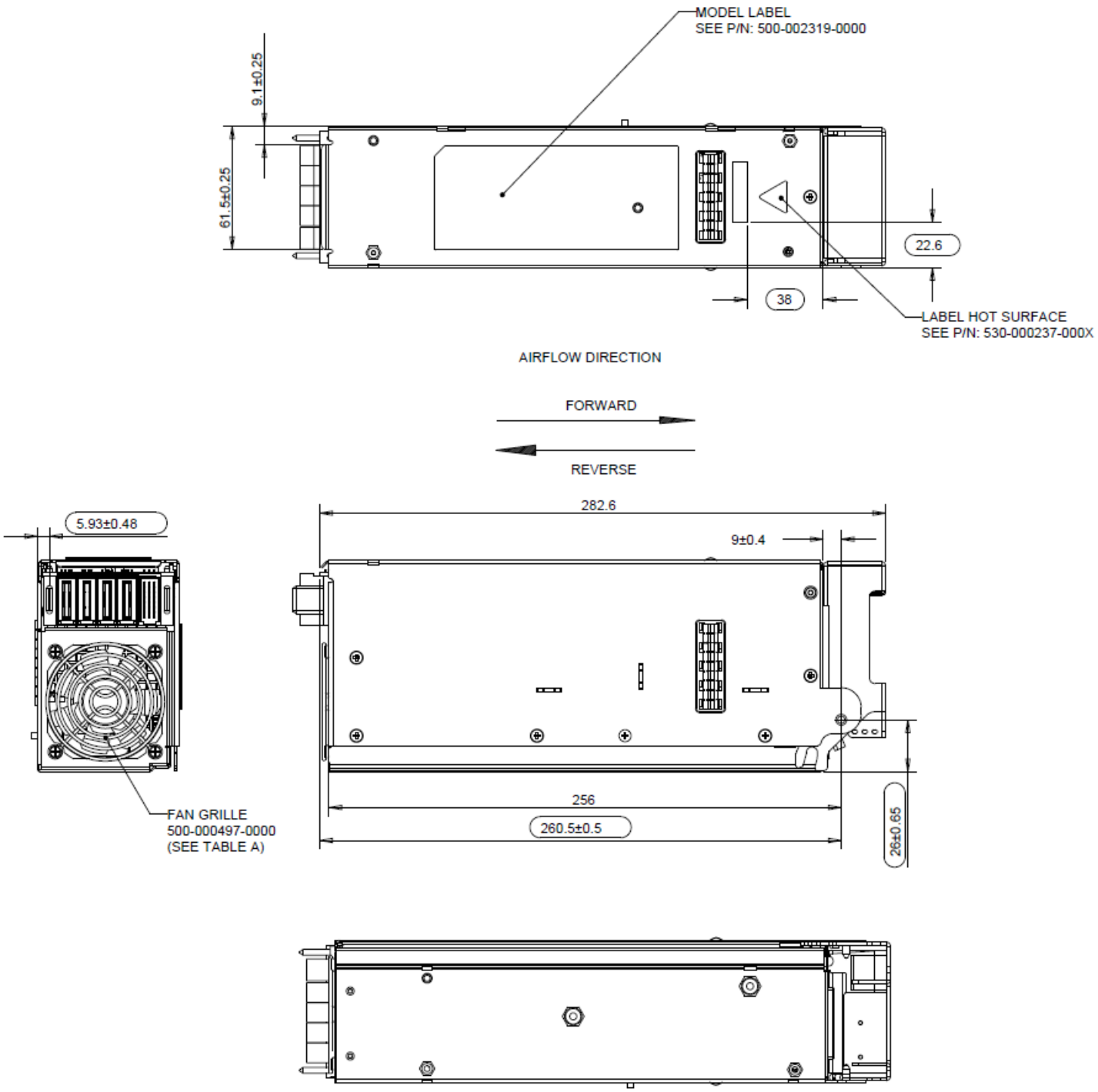
The DS3000TE-3 series power supply will withstand a continuous short circuit applied to any output during start-up or while running, and the short circuit will not cause any damage to the power supply (connectors, components, PCB traces, etc.). A short circuit is defined as an impedance on main output of 0.01 ohm or less. When the standby output is shorted, the output will go into “hiccup mode”. When the Standby output attempts to restart, the maximum peak current from the Standby output will be less than 10.0A. The maximum average current, taking into account the “hiccup” duty cycle, must be less than rated output current. The power supply will recover automatically when the error condition is removed and never latch off requiring a manual AC power cycle. Excessive peak currents due to the discharge of output capacitors are not controllable in the event of short circuit at the output.

Over Temperature Protection (OTP)

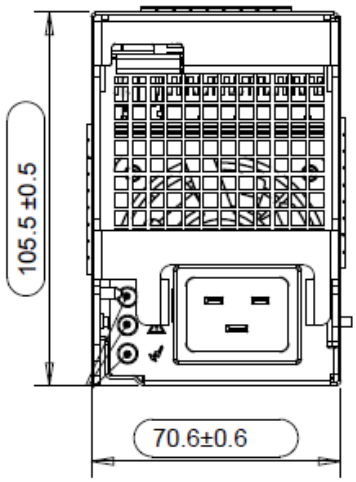
The DS3000TE-3 series power supply will internally protect itself against over temperature conditions. The shutdown temperature will be greater than the maximum specified chassis temperature under all conditions and no components will be over stressed at the shutdown temperature. There are three over-temperature protection sensors for the main output, the PFC circuit and on the Standby output. When one of the sensing circuits has reached the OTP limit, all outputs will shut-down and remain off until the over-temperature condition no longer exists. A suitable hysteresis point between the OTP threshold and the recovery point will be set to ensure there is no frequent on-off cycling of the outputs. The temperature recovery point will be set well within the supply operating temperature range. Upon reaching the temperature recovery point, all outputs will auto-recover. It is desirable that the over temperature warning alarm is asserted about more than 1 second before the supply shut down. Any OTP fault will be reported in the PMBus™ status flag, without discriminating on which OTP sensing circuit was triggered.

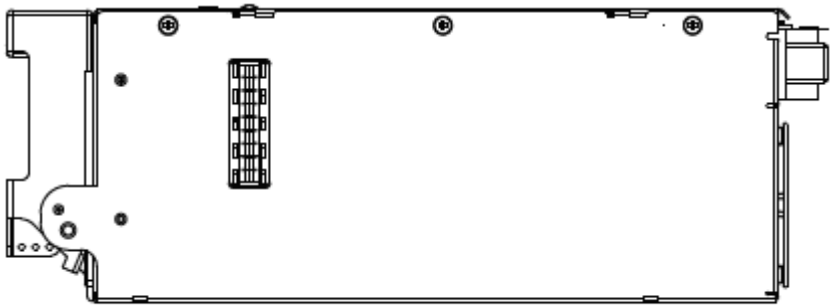
Mechanical Specifications

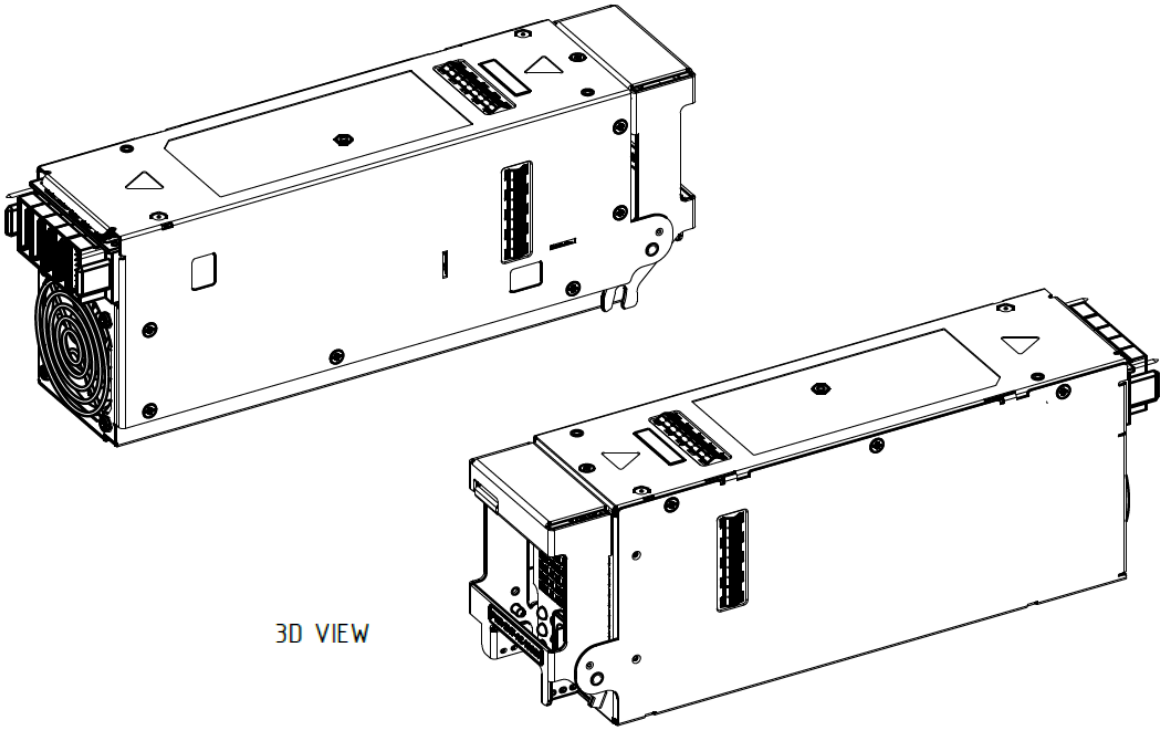
Mechanical Outlines (Unit:mm)



Mechanical Outlines (Unit:mm)



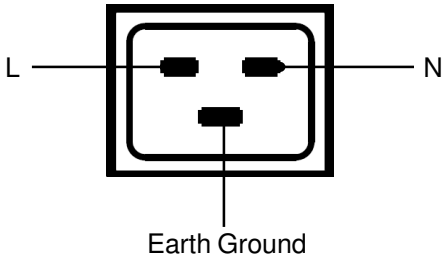




Connector Definitions

AC Input Connector

Pin 1 – L
Pin 2 – N
Pin 3 – Earth Ground



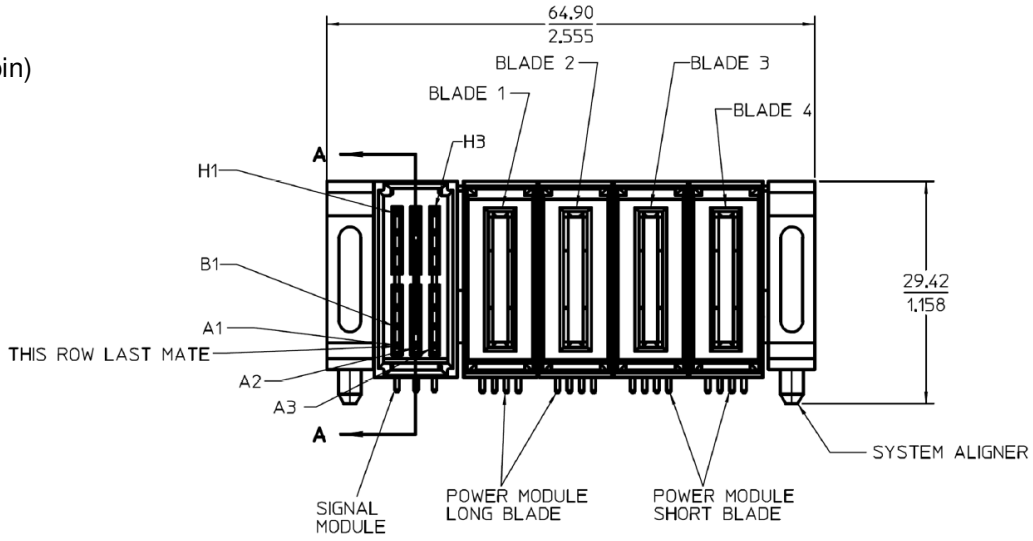
Output Connector – Power Blades

PB1 – Return (Max 150Amp)
PB2 – Return (Max 150Amp)
PB3 – + Main Output ($12.1V_{O,}$ Max 150Amp)
PB4 – + Main Output ($12.1V_{O,}$ Max 150Amp)

Power Supply Output Card Edge

Output Connector – Control Signals

A1 – PWOK (short pin)
A2 – PS_KILL (short pin)
A3 – PS_PRESENT (short pin)
B1 – RETURN
B2 – ISHARE
B3 – RETURN
C1 – PS_INTERRUPT
C2 – RETURN
C3 – ACOK
D1 – RETURN
D2 – PSON
D3 – RESERVED
E1 – SDA
E2 – SCL
E3 – A0
F1 – RESERVED
F2 – A1
F3 – A2
G1 – RESERVED
G2 – RESERVED
G3 – RESERVED
H1 – 12 VSB
H2 – 12 VSB
H3 – 12 VSB

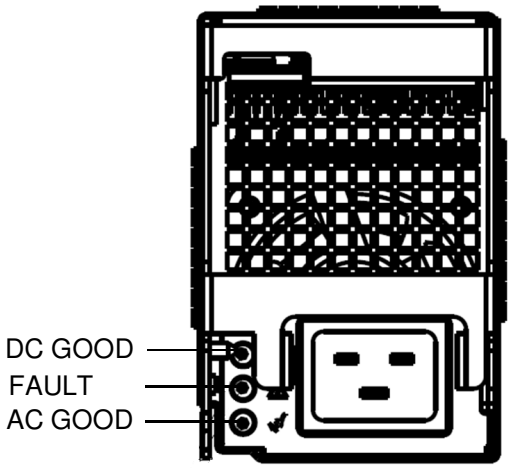


Power / Signal Mating Connectors and Pin Types

Table 5. Mating Connectors for DS3000TE-3 series

Reference	On Power Supply	Mating Connector or Equivalent
AC Input Connector	IEC320-C20	IEC320-C19
Output Connector	75555-104	75541-104REVB1

LED indicator Definition



Three user-friendly LEDs for status and diagnostics shows status of input power, output power and alarm condition valuable troubleshooting aid to reduce system. downtime. The status LED conditions is shown on the below table.

Condition	AC GOOD LED	DC GOOD LED	FAULT LED
Color	Green	Green	Amber
Symbol			
AC Input = OFF	Off	Off	Off
AC Input = ON, V_{SB} = ON, V_O = OFF	On	Off	Off
AC Input = ON, V_{SB} = ON, V_O = ON	On	On	Off
V_O or V_{SB} = OCP / OVP / OTP / FAN FAULT (Fault of any kind; supply in chassis)	Off	Off	Blinking (1.5 Sec on, 1.5 Sec off)
Power supply plugged in to a live chassis, with no AC cord	Off	Off	Off
Power supply has AC power but not plugged in to the chassis	Blinking (1.5 Sec on, 1.5 Sec off)	Off	Off

Weight

The DS3000TE-3 series maximum weight is 6.615 lbs / 3000 g.

Environmental Specifications

EMC Immunity

DS3000TE-3 series power supply is designed to meet the following EMC immunity specifications:

Table 6. Environmental Specifications:

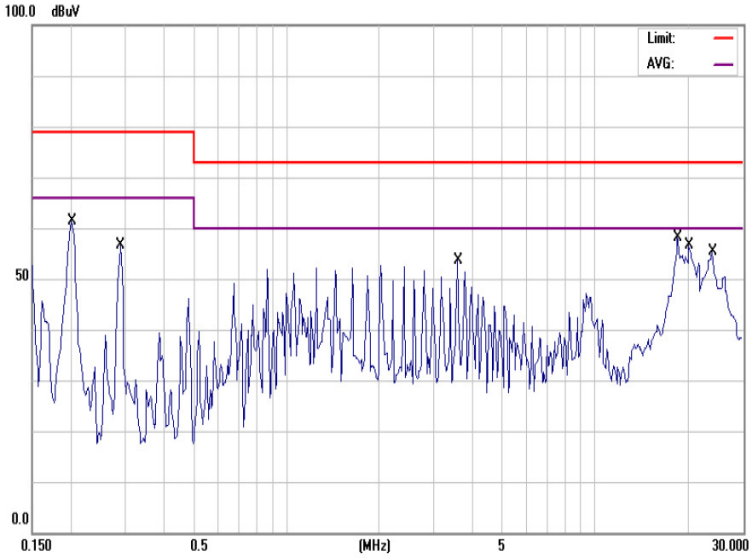
Document	Description
FCC 47CFR Part 15 Subpart B / CISPR 22/ EN55022, Class A	Conducted and Radiated EMI Limits
EN61000-3-2	Harmonic Currents <16 Amps per phase
EN61000-3-3	Voltage Fluctuations <16 Amps per phase
EN61000-4-2	Electromagnetic Compatibility (EMC) - Testing and measurement techniques – Electrostatic discharge immunity test. +/-15KV air, +/-8KV contact discharge, performance Criteria A
EN61000-4-4	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Electrical Fast Transient/Burst Immunity Test. +/-2KV for AC power port, performance Criteria B; +/-0.5KV for DC power, I/O and signal ports, performance Criteria A
EN61000-4-5	Electromagnetic Compatibility (EMC) - Testing and measurement techniques: Surge AC – 2KV common mode and 1KV differential mode for AC ports, performance Criteria A
GR-1089	Electromagnetic Compatibility (EMC) - Testing and measurement techniques: Surge AC – 2KV common mode and 1KV differential mode for AC ports, performance Criteria A
EN61000-4-11	Electromagnetic Compatibility (EMC) - Testing and measurement techniques : Voltage Dips and Interruptions: >30% reduction for 500ms, Criteria C; >95% reduction for 10mS, Criteria A (Self-recoverable only); >95% reduction for 500mS, Criteria C (Self-recoverable only)
EN55022	Information Technology Equipment-Immunity Characteristics, Limits and Method of Measurements

EMI Emissions

The DS3000TE-3 series have been designed to comply with the Class A limits of EMI requirements of EN55022 (FCC 47CFR Part 15) and CISPR 22 (EN55022) for emissions and relevant sections of EN61000 (IEC 61000) for immunity. The unit is enclosed inside a metal box, tested at 3000W using resistive load with cooling fan.

Conducted Emissions

The applicable standard for conducted emissions is EN55022 (FCC 47CFR Part 15). Conducted noise can appear as both differential mode and common mode noise currents. Differential mode noise is measured between the two input lines, with the major components occurring at the supply fundamental switching frequency and its harmonics. Common mode noise, a contributor to both radiated emissions and input conducted emissions, is measured between the input lines and system ground and can be broadband in nature.



The DS3000TE-3 power supplies have internal EMI filters to ensure the convertors' conducted EMI levels comply with EN55022 (FCC 47CFR Part 15) Class A EN55022 (CISPR 22) Class A limits. The EMI measurements are performed with resistive loads at maximum rated loading.

Sample of EN55022 Conducted EMI Measurement at 220Vac input

Note: The first red Line refers to Artesyn Quasi Peak margin, which is 6dB below the CISPR 22 international limit. The second purple Line refers to the Artesyn Average margin, which is 6dB below the CISPR international limit. The scan curve indicates peak detector measurement.

Conducted Emissions

Parameter	Model	Symbol	Min	Typ	Max	Unit
FCC 47CFR Part 15, class A	All	Margin	-	-	6	dB
CISPR 22 (EN55022) class A	All	Margin	-	-	6	dB

Radiated Emissions

Unlike conducted EMI, radiated EMI performance in a system environment may differ drastically from that in a stand-alone power supply. It is thus recommended that radiated EMI be evaluated in a system environment. The applicable standard is specified in EN55022 (FCC 47 CFR15 Subpart B and EN300 386) Class A with 6dB margin. Testing AC-DC convertors as a stand-alone component to the exact requirements of EN55022 can be difficult, because the standard calls for 1m leads to be attached to the input and outputs and aligned such as to maximize the disturbance. In such a set-up, it is possible to form a perfect dipole antenna that very few AC-DC convertors could pass. However, the standard also states that an attempt will be made to maximize the disturbance consistent with the typical application by varying the configuration of the test sample.

Safety Certifications

The DS3000TE-3 power supply is intended for inclusion in other equipment and the installer must ensure that it is in compliance with all the requirements of the end application. This product is only for inclusion by professional installers within other equipment and must not be operated as a stand alone product.

Table 7. Safety Certifications for DS3000TE-3 series power supply system .

Document	File #	Description
UL/cUL 60950	E186249-A292-UL-X6	US and Canada Requirements
EN60950		European Requirements
CB Certificate and Report	DK-45042-A2-UL	(All CENELEC Countries)
DEMKO	D-04128-A2	
CHINA CQC Approval	CQC15001130760	China Requirements
KC (K60950-1)	YU10485-16003	Korea Requirements
BSMI thru ETC	CI33416160276700	Taiwan Requirements
CE Mark (LVD+RoHS)	16290	

Operating Temperature

The DS3000TE-3 series power supplies will start and operate within stated specifications at an ambient temperature from 0^{O}C to 40^{O}C under all load conditions with internal fan.

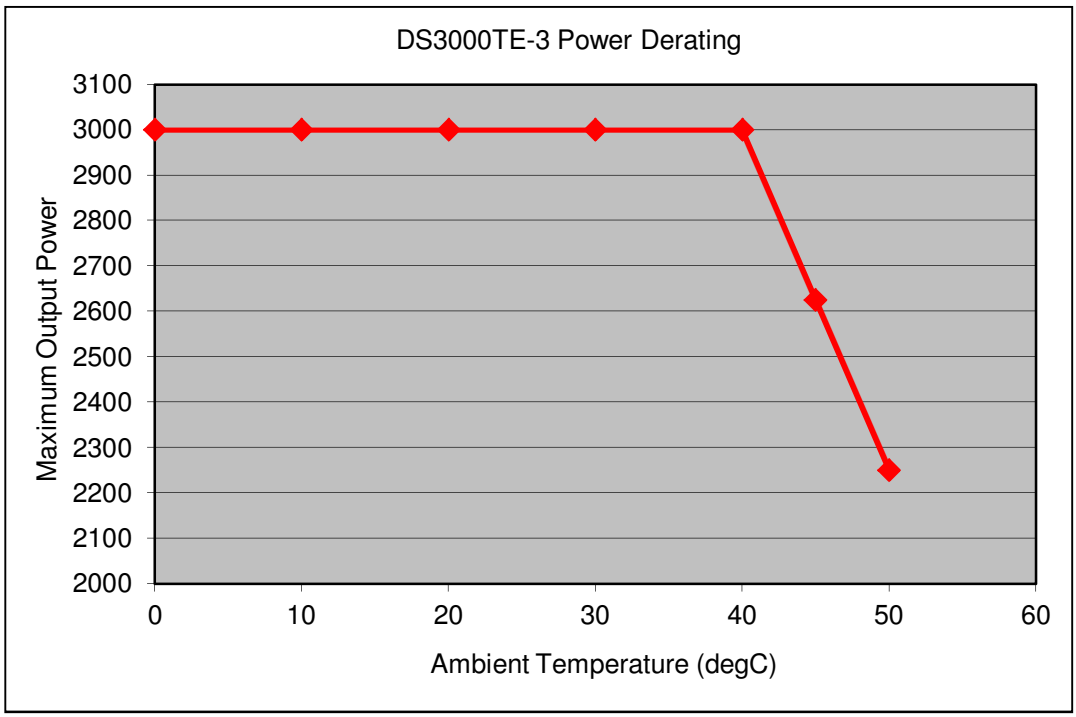
Forced Air Cooling

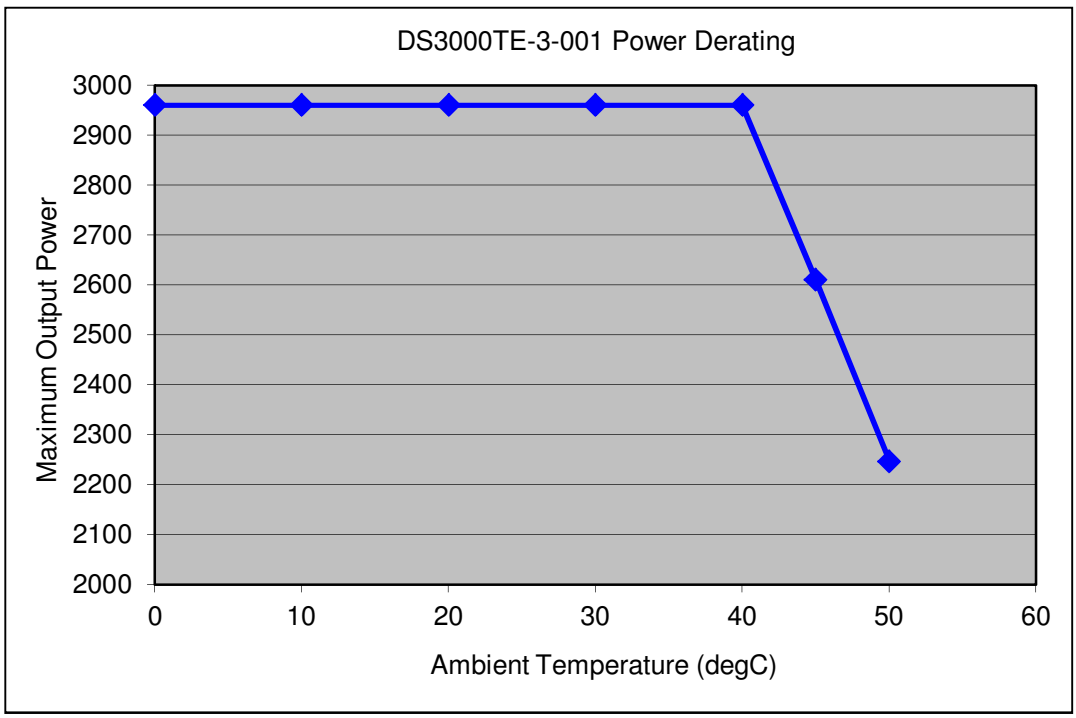
The DS3000TE-3 series power supplies included internal cooling fans as part of the power supply assembly to provide forced air-cooling to maintain and control temperature of devices and ambient temperature in the power supply to appropriate levels. The standard direction of airflow is from the DC connector end to the AC connector end of the power supply.

The cooling fan is a variable speed fan and the fan speed will be controlled by power supply. In Standby mode power supply fan will operate at minimum speed to maintain component reliability at all load, line and ambient conditions. When 12V output is enabled, power supply fan will operate at minimum achievable fan speed. Power supply fan speed control algorithms will vary the speed so that the critical component temperatures do not exceed safe operating levels. Fan will be powered from voltage source inside the power supply and from system side voltage source.

Power Derating Curves

DS3000TE-3 series total output power will be derated according to the curve shown below. DS3000TE-3 can provide derated output power from 40^{O}C up 50^{O}C ambient temperature max. DS3000TE-3-001 can provide derated output power from 40^{O}C up to 50^{O}C ambient temperature max.





Storage and Shipping Temperature / Humidity

The DS3000TE-3 series power supplies can be stored or shipped at temperatures between -40^{O}C to +90^{O}C and relative humidity from 5% to 95% non-condensing.

Altitude

The DS3000TE-3 series will operate within specifications at altitudes up to 13200 feet (4000m) above sea level. And the power supply will not be damaged when stored at altitudes of up to 50000 feet (15000m) above sea level.

Altitude	Models	Input Voltage Range	Maximum Output Power (W)	Minimum Air Inlet Temperature (degC)	Maximum Air Inlet Temperature (degC)
6600 feet (2000m)	DS3000TE-3	208-240Vac	3000	0	40
		180-208Vac	2700		
	DS3000TE-3-001	208-240Vac	2960	0	30
		180-208Vac	2664		
13200 feet (4000m)	DS3000TE-3	208-240Vac	3000	0	40
		180-208Vac	2700		
	DS3000TE-3-001	208-240Vac	2770	0	30
		180-208Vac	2500		

Humidity

Operating: Power supply will be designed to operate with no degradation of performance while operating in range of 5%RH to 95%RH non-condensing.

Non-Operating: Power supply will be designed to stored with no degradation of performance while non-operating in range of 5%RH to 95%RH non-condensing.

Vibration

The DS3000TE-3 series power supply will be subjected to random non-operational and non-operation vibration at levels according to the NEBS GR-63 and IPC9592 requirements. The power supply module will also be subjected to a swept sinusoidal vibration test at acceleration amplitude of 0.5 g from 5 to 200 Hz and back to 5 Hz at 0.1 octave/minute, performed for 90 minutes for each of the three mutually perpendicular axes.

Operating Random Vibration

Acceleration	0.5	gRMS
Frequency Range	5-200	Hz
Duration	90	mins
Direction	Rotating each axis on vertical vibration	
PSD Profile	**FREQ** / **SLOPE dB/oct** / **PSD g^2/Hz** 5Hz — 0.00003 g^2/Hz 10-50Hz — 0.0004 g^2/Hz 100Hz — 0.00003 g^2/Hz	

Shock

The DS3000TE-3 power supply will pass the following shock specifications. All components within the power supply will be appropriately secured to prevent failure resulting from these tests. At the conclusion of any of the below referenced Shock tests listed, the power supply will be powered up under maximum rated load and will perform within specification.

Non-Operating Half-Sine Shock

Acceleration	30	G
Duration	18	msec
Pulse	Half-Sine	
No. of Shock	3 shocks on each of 6 faces	

Operating Half-Sine Shock

Acceleration	15	G
Duration	11	msec
Pulse	Half-Sine	
No. of Shock	3 shocks on each of 6 faces	

Power and Control Signal Descriptions

AC Input Connector

This connector supplies the AC Mains to the DS3000TE-3 power supply.

Pin 1 - L
Pin 2 - N
Pin 3 - Earth Ground

Output Connector – Power Blades

These pins provide the main output for the DS3000TE-3. The + Main Output (V_O) and Return pins are the positive and negative rails, respectively, of the V_O main output of the DS3000TE-3 power supply. The Main Output (V_O) is electrically isolated from the power supply chassis.

PB1 – Return
PB2 – Return
PB3 – + Main Output (V_O)
PB4 – + Main Output (V_O)

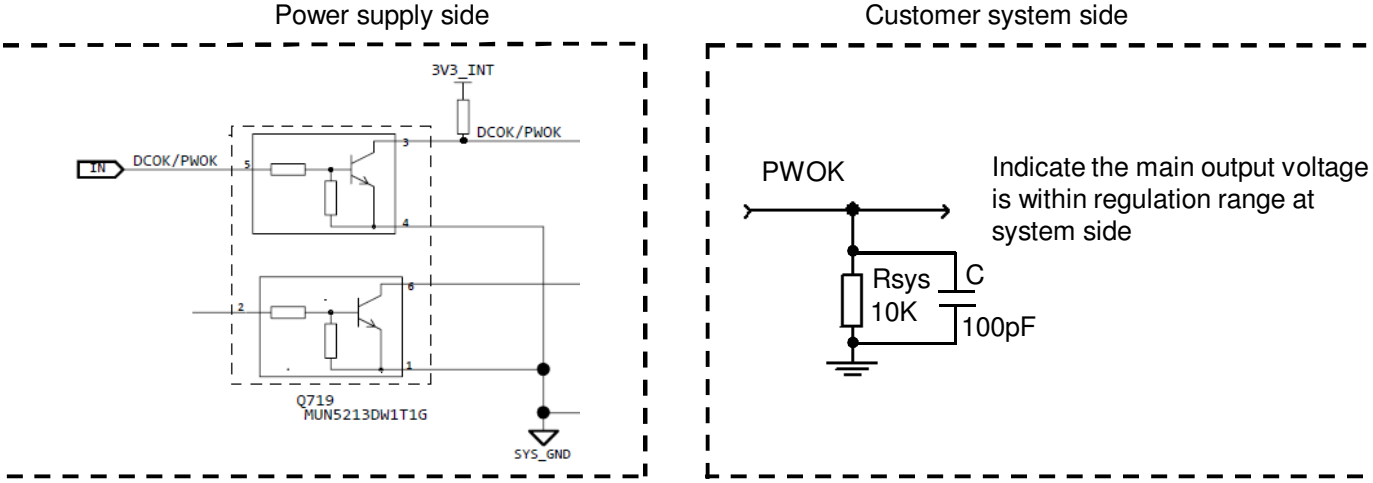
Output Connector - Control Signals

The DS3000TE-3 series contain a 24 pins control signal header providing an analogue control interface and I^2C interface signal connections.

PWOK – (A1)

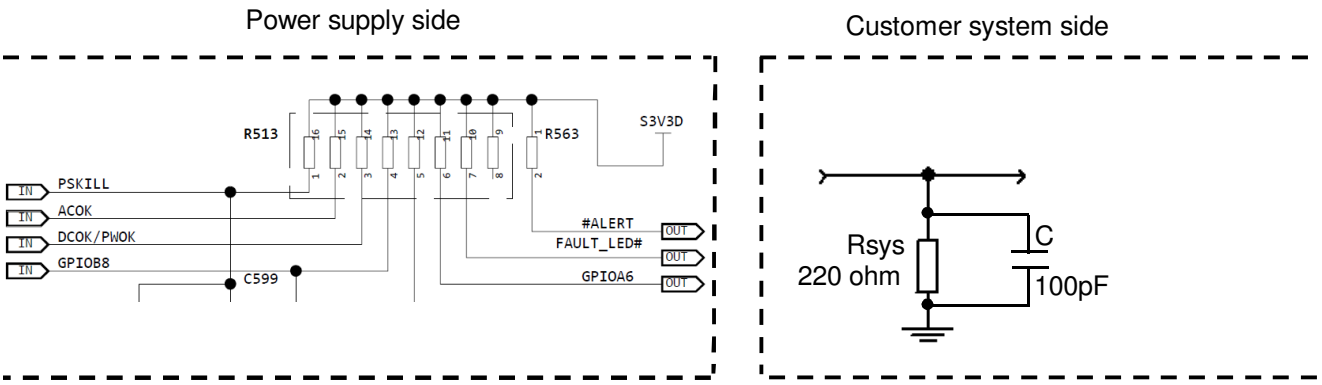
The PWOK signal used to indicate that main output voltage is within regulation range. The PWOK signal will be driven HIGH when the output voltage is valid and will be driven LOW when the output falls below the under-voltage threshold. This signal also gives an advance warning when there is an impending power loss due to loss of AC input or system shutdown request. If the AC power is lost, this signal is driven low at least 20ms before the standby output goes below regulation range.

This is an open collector/drain output. This pin is pulled high by a 1.0K ohm resistor connected to 3.3V inside the power supply. It is recommended that this pin be connected to a 100pF decoupling capacitor and pulled down by a 10K ohm resistor.



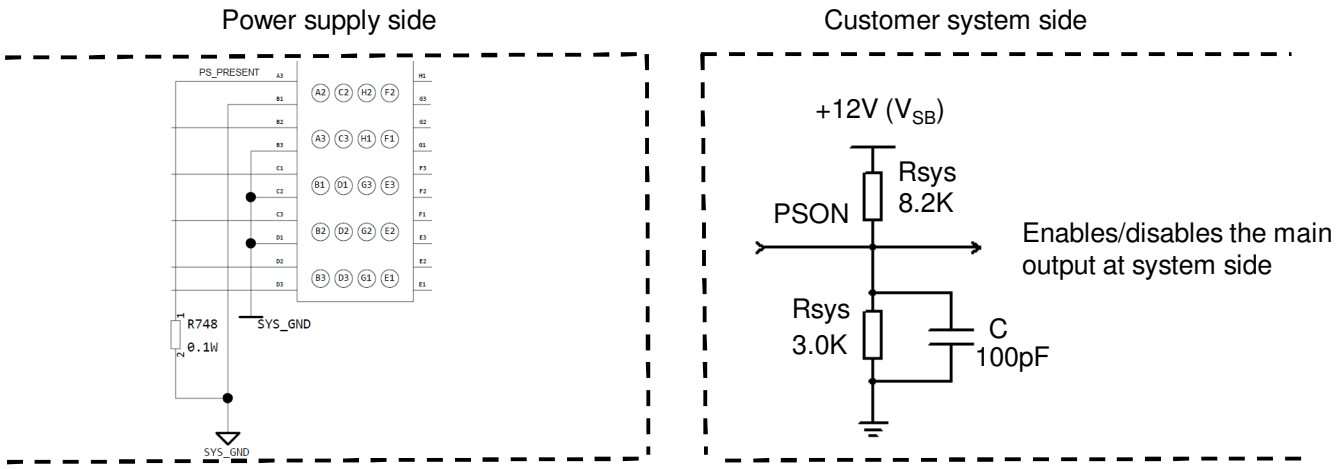
PSKILL – (pin A2)

The PSKILL input signal has a short pin in the output connector, ANDed with PSON. It functions as the first break and last mate pin, thus, supports hot-swap capability. This enables or disables the main +12V output of the power supply. When this signal is shorted to ground by the system, the main +12V output will be enabled. Power supply has 1 K ohm pull-up resistor to internal 3.3V bias. This signal is pulled to ground at the system side with a 220 ohm resistor. A 100pF decoupling capacitor is also recommended. Standby output will remain on after the main +12V output disabled.



PRESENT – (pin A3)

This input signal pin is grounded inside the power supply via 50 Ohm resistor. It can be used to sense PSU seated by using a suitable pull-up to standby with a noise filter capacitor connected to standby return. Signal used to indicate to the system that a power supply is inserted in the power bay. Recommended pull-up resistor to 12 V_{SB} is 8.2 K ohm with a 3.0 K ohm pull-down to ground. A 100 pF decoupling capacitor is also recommended.



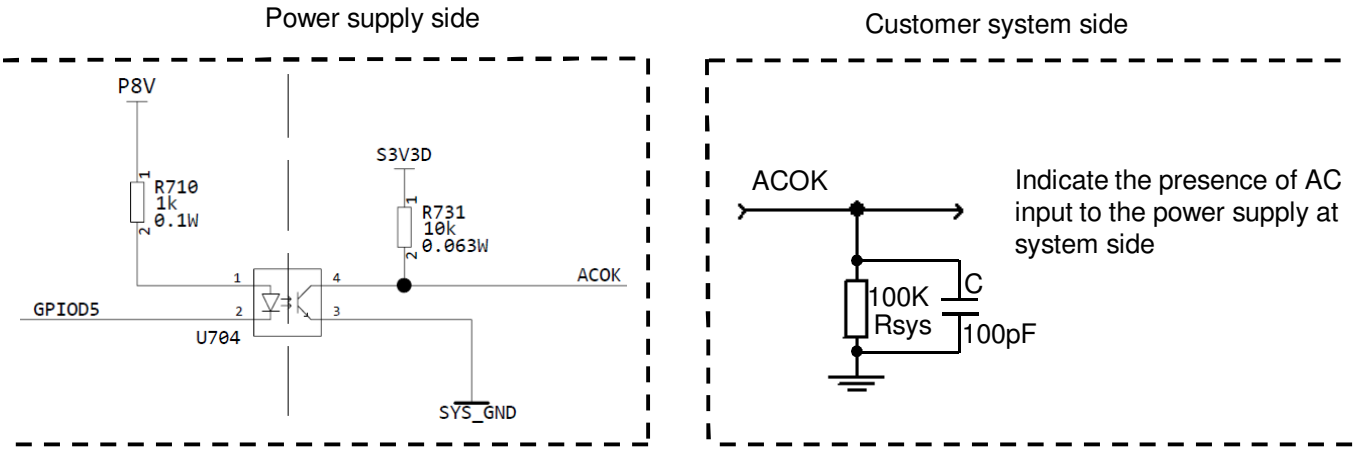
ISHARE – (pin B2)

The DS3000TE-3 supports active current sharing through a single wire connection between the power supplies. This input/output signal pin allows two or more power supplies to share the main output load current to increase the overall power capability or to operate the units in a N+N configuration for redundancy purposes. This signal is an analog bus which will allow two or more power supplies to share the system load current. It will have a voltage which is directly proportional to supplied current. A linear slope from minimum load to full load is expected. The system will continue to work with the I-Share signal wrongly driven high or shorted to ground. The chassis must supporting routing this signal to all chassis power supply. If one of the supplies fails the remaining supply must pick up the entire load without any of the outputs dropping out of regulation.

ACOK – (pin C3)

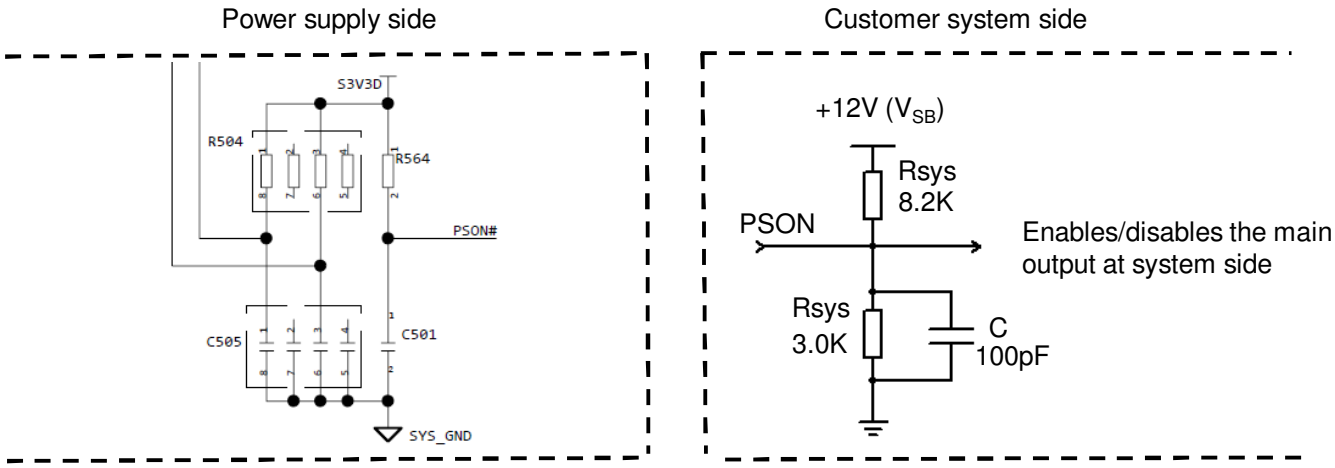
The ACOK is an open collector output signal which is normally LOW (<0.4V) whenever input AC voltage is within allowable limits. This signal will go HIGH (>2.0V) within 6ms from loss of AC. Power supply has internal 10 K ohm pull-up resistor to internal 3.3V bias. Additional pull-up on system side may be added but current-limited to 0.7mA. Suitable noise filter capacitor connected to standby return line is recommended on system side.

It is recommended that this pin be connected to a 100pF decoupling capacitor and pulled down by a 100K ohm resistor.



PSON – (pin D2)

This signal input pin controls the normal turning ON and Off of the Main Output of the DS3000TE-3 power supply, ANDed with PSKILL. The power supply main output will be enabled when this signal is pulled low to below 0.8V. The power supply main output will be disabled when this input is driven higher than 2.0V, or left open. This signal can be pulled high to 3.3V maximum. The standby output is not affected by this signal. Recommended pull-up resistor to $12V_{SB}$ is 8.2K ohm with a 3.0K ohm pull-down to ground. A 100pF decoupling capacitor is also recommended.



SDA, SCL and PS_INTERRUPT – (pin E1, E2 and C1)

Please refer to "Communication Bus Descriptions" section.

A0, A1 and A2– (pins E3, F2 and F3)

Please refer to “Communication Bus Descriptions” section.

Communication Bus Descriptions

I²C Bus Signals

The DS3000TE-3 power supply contains enhanced monitor and control functions implemented via the I²C bus. The DS3000TE-3 I²C functionality (PMBus™ and FRU data) can be accessed via the output connector control signals. The communication bus is powered either by the internal 3.3V supply or from an external power source connected to the Standby Output (i.e. accessing an unpowered power supply as long as the Standby Output of another power supply connected in parallel is on).

If units are connected in parallel or in redundant mode, the Standby Outputs must be connected together in the system. Otherwise, the I²C bus will not work properly when a unit is inserted into the system without the AC source connected.

Note: PMBus™ functionality can be accessed only when the PSU is powered-up.
Guaranteed communication I²C speed is 100K Hz.

SDA, SCL (I²C Data and Clock Signals) – (pins E1, E2)

I²C serial data and clock bus - these pins are internally pulled up to internal 3.3V supply with a 22K ohm resistor. These pins must be pulled-up in the system by an 2.2K ohm resistor to 3.3V and a 200pF decoupling capacitor at the system side.

Refer to the communication interface specifications for more details.

PS_INTERRUPT – (pin C1)

PS_INTERRUPT is used to indicate to the system that a change in power supply status has occurred. This signal is normally logic level HIGH. It will go to a LOW logic level when a fault bit has been set in the power supply's status register. This event can be triggered by faults such as OVP, OCP, OTP and fan fault. This signal can be cleared by a CLEAR_FAULT command. Recommended pull-up resistor to $12V_{SB}$ is 8.2 K ohm with a 3.0 K ohm pull-down to ground. A 100pF decoupling capacitor is also recommended. Three conditions can deassert this signal: AC re-cycle, PSON re-cycle or issuance of a CLEAR_FAULTS PMBus command.

A0, A1 and A2 (I²C Address) – (pin E3, F2 and F3)

These three input pins are the address lines A0, A1 and A2 to indicate the slot position the power supply occupies in the power bay and define the power supply addresses for FRU data and PMBus™ data communication. This allows the system to assign different addresses for each power supply. During I²C communication between system and power supplies, the system will be the master and power supplies will be slave.

They are internally pulled up to internal 3.3V supply with a 1K ohm resistor.

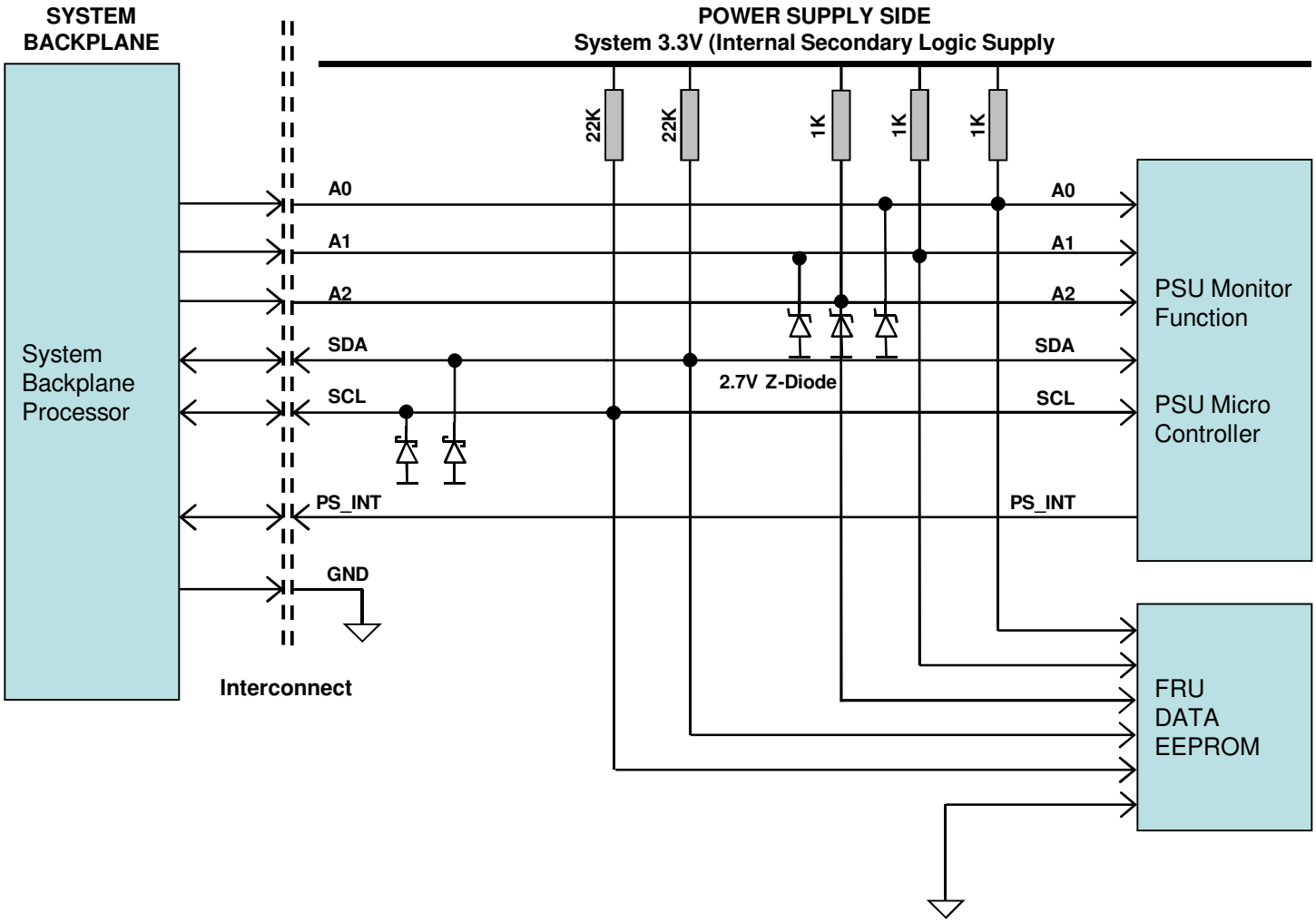
I²C Bus Communication Interval

The interval between two consecutive I²C communications to the power supply will be at least 15ms to ensure proper monitoring functionality.

I²C Bus Signal Integrity

The noise on the I²C bus (SDA, SCL lines) due to the power supply will be less than 400mV peak-to-peak. This noise measurement will be made with an oscilloscope bandwidth limited to 100MHz. Measurements will be make at the power supply output connector with 22K ohm resistors pulled up to Standby Output and 47pF ceramic capacitors to Standby Output Return.

I²C Bus Internal Implementation, Pull-ups and Bus Capacitances



I²C Bus - Recommended external pull-ups:

Electrical and Interface specifications of I²C signals (referenced to Standby Output Return pin, unless otherwise indicated):

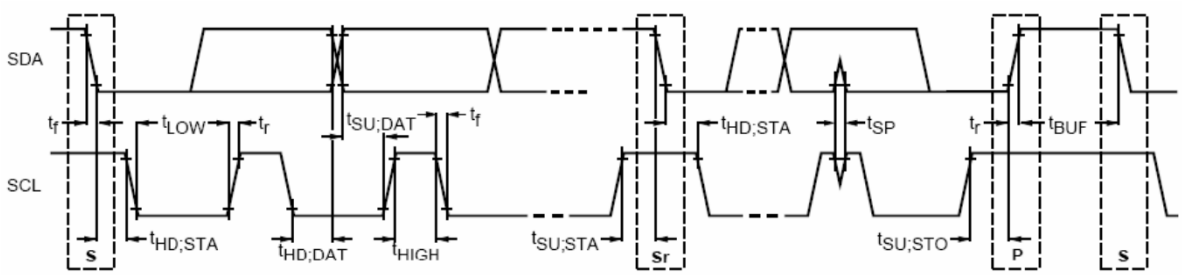
Parameter	Condition	Symbol	Min	Typ	Max	Unit
SDA, SCL internal pull-up resistor		R_{int}	-	22	-	K ohm
SDA, SCL internal bus capacitance		C_{int}	-	33	-	pF
Recommended external pull-up resistor	1 to12 PSU	R_{ext}	-	2.2	-	K ohm

Logic Levels

DS3000TE-3 series power supply I²C Communication Bus will respond to logic levels as per below:

Logic High: 3.3V Nominal (Specs is 2.1V to 5.5V)**
Logic Low: 500mV nominal (Specs is 800mV max)**

Timings



Parameter	Symbol	Standard-Mode Specs		Actual		Unit
		Min	Max			
SCL Clock Frequency	f_{SCL}	0	100	100		KHz
Hold time (repeated) START condition	$t_{HD;STA}$	4.0	-	4.7		us
LOW period of SCL clock	t_{LOW}	4.7	-	15.3		us
HIGH period of SCL clock	t_{HIGH}	4.0	-	4.8		us
Setup time for repeated START condition	$t_{SU;STA}$	4.7	-	4.9		us
Data hold time	$t_{HD;DAT}$	0	3.45	230		ns
Data setup time	$t_{SU;DAT}$	250	-	4680		ns
Rise time	t_r	-	1000	SCL = 867	SDA = 830	ns
Fall time	t_f	-	300	SCL = 175	SDA = 151	ns
Setup time for STOP condition	$t_{SU;STO}$	4.0	-	5.5		us
Bus free time between a STOP and START condition	t_{BUF}	4.7	-	30.8		msec

Device Addressing

The DS3000TE-3 series will respond to supported commands on the I²C bus that are addressed according to pins A2, A1 and A0 pins of output connector.

Address pins are held HIGH by default via pulled up to internal 3.3V supply with a 1.0 K ohm resistor. To set the address as "0", the corresponding address line will be pulled down to logic ground level. Below tables show the address of the power supply with A0, A1 and A2 pins set to either "0" or "1".

PSU Slot	Slot ID Bits			PMBus™ Address		EEPROM (FRU) Address	
	A2	A1	A0	Write Address	Read Address	Write Address	Read Address
1	0	0	0	0xB0	0xB1	0xA0	0xA1
2	0	0	1	0xB2	0xB3	0xA2	0xA3
3	0	1	0	0xB4	0xB5	0xA4	0xA5
4	0	1	1	0xB6	0xB7	0xA6	0xA7
5	1	0	0	0xB8	0xB9	0xA8	0xA9
6	1	0	1	0xBA	0xBB	0xAA	0xAB
7	1	1	0	0xBC	0xBD	0xAC	0xAD
8	1	1	1	0xBE	0xBF	0xAE	0xAF

* Default address when A0, A1 and A2 are left open.

Reporting Functions

The power supply will have enhanced monitor and control functions implemented via the PMBus. This will use the SDA and SCL pins. The power supply monitor will operate as an PMBus slave device.
The accuracy of the report functions will be as follows:

Firmware Reporting And Monitoring			
Output loading	0% to 20%	20% to 50%	50% to 100%
Input voltage	±3%		
Input current	±0.55A	±4%	±4%
Input power	±20W at less than 100W input	±5%	±5%
Output voltage	±2%		
Output current	±2.5 A	±4%	±2%
Temperature	±2.5^{O}C		
E_{IN}	±15%	±5%	
E_{out}	±10%	±5%	
Fan Accuracy	±200 RPM		

PMBus	Yes
Remote ON/OFF	Yes

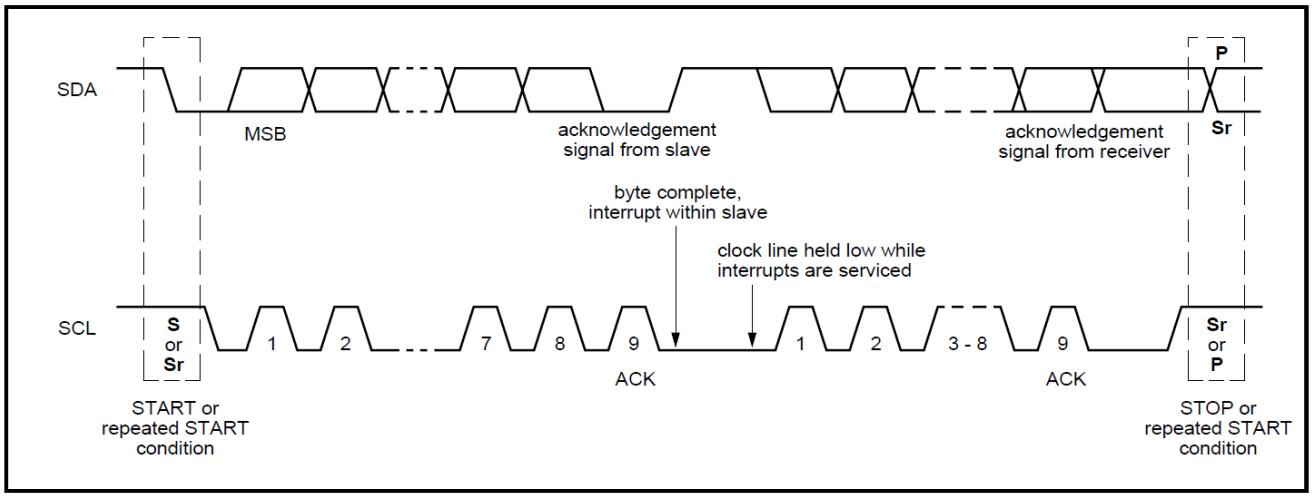
The telemetry circuit and supply controller powered either by its own AC or from the chassis 12V supply must report the same information and accuracy at all times. Unplugging or plugging the AC cord while the supply is plugged in to a live chassis must not affect reporting and/or fan operation or RPM.

I²C Clock Synchronization

The DS3000TE-3 power supply might apply clock stretching. An addressed slave power supply may hold the clock line (SCL) low after receiving (or sending) a byte, indicating that it is not yet ready to process more data.
The system master that is communicating with the power supply will attempt to raise the clock to transfer the next bit, but must verify that the clock line was actually raised. If the power supply is clock stretching, the clock line will still be low (because the connections are open-drain).

The maximum time out condition for clock stretching for DS3000TE-3 is 25 ms.

The power supply has a command completion timeout of 100 millisecond. That is, a single transaction (from START to STOP condition) must be finished within 100 millisecond.



FRU (EEPROM) Data

The FRU (Field Replaceable Unit) data format is compliant with the Intel IPMI v1.0 specification.

The DS3000TE-3 uses 1 page of EEPROM for FRU purpose. A page of EEPROM contains up to 256 byte-sized data locations.

Where: OFFSET - The OFFSET denotes the address in decimal format of a particular data byte within DS3000TE-3 EEPROM.

VALUE - The VALUE details data written to a particular memory location of the EEPROM.

DEFINITION - The contents DEFINITION refers to the definition of a particular data byte.

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		COMMON HEADER, 8 BYTES		
0	00	**FORMAT VERSION NUMBER** (Common Header) 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
1	01	**INTERNAL USE AREA OFFSET**	27	1B
2	02	**CHASSIS INFO AREA OFFSET**	1	01
3	03	**BOARD INFO AREA OFFSET**	0	00
4	04	**PRODUCT INFO AREA OFFSET**	5	05
5	05	**MULTI RECORD AREA OFFSET**	13	0D
6	06	**PAD** (reserved-Default value is 0.)	0	00
7	07	**ZERO CHECK SUM** (256 – (Sum of bytes 0 to 6))	209	D1
		CHASSIS INFO AREA(32 BYTES) This area will be filled by the Mfg. Diag. or by the OS if used		
8	08	**FORMAT VERSION NUMBER** (Default value is 0.) 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
9	09	**CHASSIS INFO AREA LENGTH** (Default value is 0.)	4	04
10	0A	**CHASSIS TYPE** (Default value is 0.)	0	00
11	0B	**CHASSIS PART NUMBER Type/Length** Type = "ASCII+LATIN1" = (11)b Length = 10 Bytes = (001010)b	202	CA
12	0C	**CHASSIS PART NUMBER BYTES** (Default value is 0.)	0	00
13	0D		0	00
14	0E		0	00
15	0F		0	00
16	10		0	00
17	11		0	00
18	12		0	00
19	13		0	00
20	14		0	00
21	15		0	00
22	16	**CHASSIS SERIAL NUMBER Type/Length** Type = "ASCII+LATIN1" = (11)b Length = 15 Bytes = (000111)b	207	CF
23	17	**CHASSIS SERIAL NUMBER BYTES,** Default value is 0.	0	00
24	18		0	00
25	19		0	00
26	1A		0	00
27	1B		0	00
28	1C		0	00
29	1D		0	00
30	1E		0	00
31	1F		0	00
32	20		0	00
33	21		0	00
34	22		0	00

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
35	23		0	00
36	24		0	00
37	25		0	00
38	26	**End Tag** (0C1h if used)	193	C1
39	27	**CHKSUM** (Bytes from 8 to 38)	161	A1
		PRODUCT INFORMATION AREA, 64 BYTES		
40	28	**FORMAT VERSION NUMBER** (Product Info Area) 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
41	29	**PRODUCT INFO AREA LENGTH** (In multiples of 8 bytes)	8	08
42	2A	**Language** (English)	25	19
43	2B	**MANUFACTURER NAME Type/Length** 7:6 - (11)b, 8-Bit ASCII + Latin 1 5:0 - (000101)b, 5-Byte Allocation	199	C7
		MANUFACTURER'S NAME 5 byte sequence		
44	2C	"A“ = 41h	65	41
45	2D	"R“ = 52h	82	52
46	2E	"T“ = 54h	84	54
47	2F	"E“ = 45h	69	45
48	30	"S“ = 53h	83	53
49	31	"Y“ = 59h	89	59
50	32	"N“ = 4Eh	78	4E
51	33	**PRODUCT NAME Type/Length** Type = "ASCII+LATIN1" = (11)b Length = 15 Bytes = (001111)b	207	CF
		PRODUCT NAME BYTES (15 Byte sequence)		
52	34	"D" = 44H	68	44
53	35	"S" = 53H	83	53
54	36	"3" = 33H	51	33
55	37	"0" = 30H	48	30
56	38	"0" = 30H	48	30
57	39	"0" = 30H	48	30
58	3A	“T" = 54H	84	54
59	3B	"E" = 45H	69	45
60	3C	"-" = 2DH	45	2D
61	3D	"3" = 33H	51	33
62	3E	**Last four characters will be based on the last four characters of the variant**	45	2D
63	3F	**Ex. -001 for DS3000TE-3-001.**	52	34
64	40	**For DS3000TE-3-SP, use only DS3000TE-3**	48	30
65	41	**For DS3000TE-3-001-SP, use only DS3000TE-3-001**	49	31
66	42		32	20
67	43	**PRODUCT PART/MODEL NUMBER Type/Length** Type = "ASCII+LATIN1" = (11)b Length = 15 Bytes = (001111)b	207	CF
		PRODUCT PART/MODEL NUMBER BYTES		
68	44	"D" = 44H	68	44
69	45	"S" = 53H	83	53
70	46	"3" = 33H	51	33
71	47	"0" = 30H	48	30
72	48	"0" = 30H	48	30
73	49	"0" = 30H	48	30
74	4A	“T" = 54H	80	54
75	4B	"E" = 45H	69	45
76	4C	"-" = 2DH	45	2D
77	4D	"3" = 33H	51	33
78	4E	**Last four characters will be based on the last four characters of the variant**	45	2D
79	4F	**Ex. -001 for DS3000TE-3-001.**	52	34
80	50	**For DS3000TE-3-SP, use only DS3000TE-3**	48	30
81	51	**For DS3000TE-3-001-SP, use only DS3000TE-3-001**	49	31
82	52		32	20

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
83	53	**PRODUCT VERSION NUMBER Type/Length (C2h)** Type = “ASCII+LATIN1” = (11)b Length = 2 bytes = (000010)b	194	C2
		PRODUCT VERSION NUMBER BYTES Refer to Bom Revision		
84	54	X	88	58
85	55	X	88	58
86	56	**PRODUCT SERIAL NUMBER Type/Length** Type = “ASCII+LATIN1” = (11)b Length = 13 bytes = (001101)b	205	CD
		PRODUCT SERIAL NUMBER BYTES Model ID = **K553 (DS3000TE-3), K554 (-001), K871 (-403), K878 (), K879 (-001), K880 (-SP), K881 (-001-SP)** Example below		
87	57	"K" = 4B	76	4C
88	58	"5" = 35	51	33
89	59	"5" = 35	48	30
90	5A	"3" = 33	52	34
		MANUFACTURING YEAR AND WEEK CODE		
91	5B	"W" = 57h (Per Unit)	87	57
92	5C	"W" = 57h (Per Unit)	87	57
		UNIQUE SERIAL NUMBER “SSSS”		
93	5D	"S" = 53 (Per Unit)	83	53
94	5E	"S" = 53 (Per Unit)	83	53
95	5F	"S" = 53 (Per Unit)	83	53
96	60	"S" = 53 (Per Unit)	83	53
		MODEL REVISION, Astec Model Rev, See Latest Model Rev Ipro Bom Revision		
97	61	X	88	58
98	62	X	88	58
		MANUFACTURING LOCATION “Z” for "Zhongshan" In Decimal = 090, In Hex = 5AH		
99	63	“L” for "Laguna" In Decimal = 760, In Hex = 4CH	76	4C
100	64	**End Tag**	193	C1
101	65	PAD (reserved), Default value is 0.	0	00
102	66		0	00
103	67	**ZERO CHECK SUM (256 – (Sum of bytes 40 to 102)) Per Unit** Zero Check Sum :will follow check sum calculation as per IPMI v1.1 specs	179	B3
Multi Record Area, 88 Bytes				
		Power Supply Record Header		
104	68	Record type = 00 for Power supply	0	00
105	69	End of List /Record Format Version Number	2	02
106	6A	Record Length of Power Supply Record	24	18
107	6B	Record CHECKSUM of Power Supply Record (Zero CHECKSUM) (256-(sum of bytes 109 to 132)	98	62
248	6C	Header CHECKSUM of Power Supply Record Header (Zero CHECKSUM) (256-(sum of bytes 104 to 107)	132	84
Power Supply Record				
		Overall Capacity of the Power Supply 2 Bytes Sequence		
109	6D	**DS3000TE-3//-SP/-403 = 3000W=0BB8H; DS3000TE-3-001/-001/-001-SP = 2960W=0B90H**	184	B8
110	6E	3000W = 0BB8H; 2730W = 0AAAH.	11	0B
		Peak VA, 4224VA = 2480H		
111	6F	2 Bytes Sequence	128	80
112	70	In Decimal = 128, 016; In Hex = B8H, 0BH	16	10
113	71	**Inrush Current, 55A** In Decimal = 055; In Hex = 37H	55	37

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
114	72	**Inrush Interval, 10mS** In Decimal = 010 In Hex = 0AH	10	0
115 116	73 74	**Low End Input Voltage Range 1(10mV),** (180V / 10mV) 18000 = 4650H 2 Bytes Sequence Stored with LSB first then MSB.	80 70	50 46
117 118	75 76	**High End Input Voltage Range 1(10mV),** (264V/10mV) 26400= 264000 2 Bytes Sequence Stored with LSB first then MSB.	32 103	20 67
119 120	77 78	**Low End Input Voltage Range 1(10mV),** 2 Bytes Sequence	0 0	00 00
121 122	79 7A	**High End Input Voltage Range 1(10mV),** 2 Bytes Sequence	0 0	00 00
123	7B	**Low End Input Frequency Range,** 47Hz = 2FH	47	2F
124	7C	**Low End Input Frequency Range,** 63Hz = 3FH	63	3F
125	7D	**AC Dropout Tolerance in ms,** 10mS= 0AH	10	0A
126	7E	**Binary Flags, 1 indicates function supported and a 0 indicates function not supported.** Bits 7-5: RESERVED, WRITE AS 000B Bit 4: Tachometer Pulses Per Rotation / Predictive Fail Polarity BIT =1 Bit 3: Hot Swap / Redundancy Support BIT = 1 Bit 2: Auto switch Support BIT = 1 Bit 1: Power Factor Correction Support BIT = 1 Bit 0: Predictive Fail Support BIT = 0	30	1E
127 128	7F 80	**Peak Wattage Capacity and Holdup Time** 15-12: (0001)b, Hold Up Time in Seconds 0.1Sec 11-0: (101010001100)b, Peak Capacity in Watts =3000W	184 11	B8 0B
129 130 131	81 82 83	**Combined Wattage,** Byte 1: 0000 0000 = 00H = 00d à (12V0 – voltage1) Byte 2 and Byte 3: 00H, 00H	0 144 11	00 B8 0B
132	84	**Predictive Fail Tachometer Lower Threshold,** Not Applicable. Predictive Failure is not Supported.	0	00
12V DC OUTPUT RECORD HEADER				
133 134 135 136 137	85 86 87 88 89	Record type = 01 for DC Output Record End of List /Record Format Version Number for 12V DC Output Record Record Length of 12V DC Output Record Record CHECKSUM of 12V DC Output Record (Zero CHECKSUM) (256-(sum of bytes 138 to 150) Header CHECKSUM of 12V DC Output Record Header (Zero CHECKSUM) (256-(sum of bytes 133 to 136)	1 2 13 38 202	01 02 0D 26 CA
12V OUTPUT RECORD				
138	8A	**Output Information, 001 = 01H** Bit 7: Standby Information = 0B Bits 6-4: Reserved, Write as 000B Bits 3-0: Output Number 1 = 001B	1	01
139 140	8B 8C	**Nominal Voltage (10mV), (12.1V / 10mV) 1210 = 04BAH** 2 Bytes Sequence In Decimal: 186, 004 In Hex: BAH, 04H	186 4	BA 04

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Maximum Negative Voltage Deviation (11.5V / 10mV), 1150 = 047EH		
		2 Bytes Sequence		
141	8D	In Decimal: 126, 004	126	7E
142	8E	In Hex: 7EH, 04H	4	04
		Maximum Positive Voltage Deviation (11.5V / 10mV), 1270 =04F6H		
		2 Bytes Sequence		
143	8F	In Decimal: 246, 004	246	F6
144	90	In Hex: F6H, 04H	4	04
		Ripple and Noise pk-pk (mV), 150 = 0078H		
		2 Bytes Sequence		
145	91	In Decimal: 150, 000	150	96
146	92	In Hex: 96H, 00H	0	00
		Minimum Current Draw (10mA), 00 = 0000H		
		2 Bytes Sequence		
147	93	In Decimal: 00, 000	0	00
148	94	In Hex: 00H, 00H	0	00
		Maximum Current Draw (10mA),		
149	95	2 Bytes Sequence	168	A8
150	96		97	61
		V_{SB} OUTPUT RECORD HEADER		
151	97	Record type = 01 for DC Output Record	1	01
152	98	End of List /Record Format Version Number for 3V3SB Output Record	2	02
153	99	Record Length of 3V3SB Output Record	13	0D
154	9A	Record CHECKSUM of 3V3SB Output Record (Zero CHECKSUM)	9	09
		(256-(sum of bytes 156 to 168)		
155	9B	Header CHECKSUM of 3V3SB Output Record Header (Zero CHECKSUM)	231	E7
		(256-(sum of bytes 151 to 154)		
		SB OUTPUT RECORD		
156	9C	**Output Information, 002 = 02H**	130	82
		Bit 7: Standby Information = 1B		
		Bits 6-4: Reserved, Write as 000B		
		Bits 3-0: Output Number 2 = 010B		
		Nominal Voltage (10mV), (12V / 10mV) 1200 = 04B0H		
		2 Bytes Sequence		
157	9D	In Decimal: 176, 004	176	B0
158	9E	In Hex: B0H, 04H	4	04
		Maximum Negative Voltage Deviation (10mV), 1140 = 0474H		
		2 Bytes Sequence		
159	9F	In Decimal: 116, 004	116	74
160	A0	In Hex: 74H, 04H	4	04
		Maximum Positive Voltage Deviation (10mV), 1260 =04ECH		
		2 Bytes Sequence		
161	A1	In Decimal: 236, 004	236	EC
162	A2	In Hex: ECH, 04H	4	04
		Ripple and Noise pk-pk (mV), 150 = 96H		
		2 Bytes Sequence		
163	A3	In Decimal: 150, 000	150	96
164	A4	In Hex: 96H, 00H	0	00
		Minimum Current Draw (10mA), (0.1A / 10mA) 10 = 000AH		
		2 Bytes Sequence		
165	A5	In Decimal: 010, 000	0	00
166	A6	In Hex: 0AH, 00H	0	00
		Maximum Current Draw (4A), (4.5A / 10mA) 450 = 01C2H		
		2 Bytes Sequence		
167	A7	In Decimal: 194, 001	194	C2
168	A8	In Hex: C2H, 01H	1	01

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		OEM RECORD HEADER		
169	A9	Record type = C0H for OEM Record	192	C0
170	AA	End of List /Record Format Version Number for 3.3Vsb output Record	130	82
171	AB	Record Length of OEM Record	42	2A
172	AC	Record CHECKSUM of OEM Record (Zero CHECKSUM)	256	00
173	AD	Header CHECKSUM of OEM Record Header (Zero CHECKSUM) (256-(sum of bytes 169 to 172)	148	94
		OEM RECORD		
174	AE	**Manufacturer ID (3 bytes, Default is 0)**	0	00
175	AF	RESERVED	0	00
176	B0	RESERVED	0	00
177	B1	RESERVED	0	00
178	B2	RESERVED	0	00
179	B3	RESERVED	0	00
180	B4	RESERVED	0	00
181	B5	RESERVED	0	00
182	B6	RESERVED	0	00
183	B7	RESERVED	0	00
184	B8	RESERVED	0	00
185	B9	RESERVED	0	00
186	BA	RESERVED	0	00
187	BB	PAD (reserved), Default value is 0.	0	00
188	BC		0	00
189	BD		0	00
190	BE		0	00
191	BF		0	00
192	C0		0	00
193	C1		0	00
194	C2		0	00
195	C3		0	00
196	C4		0	00
197	C5		0	00
198	C6		0	00
199	C7		0	00
200	C8		0	00
201	C9		0	00
202	CA		0	00
203	CB		0	00
204	CC		0	00
205	CD		0	00
206	CE		0	00
207	CF		0	00
208	D0		0	00
209	D1		0	00
210	D2		0	00
211	D3		0	00
212	D4		0	00
213	D5		0	00
214	D6		0	00
215	D7		0	00
		INTERNAL USE AREA, 40 BYTES		
216	D8	Reserved, Default value is 0.	0	00
217	D9		0	00
218	DA		0	00
219	DB		0	00
220	DC		0	00
221	DD		0	00
222	DE		0	00
223	DF		0	00
224	E0		0	00

DS3000TE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
225	E1	**Internal User Area**	0	00
226	E2	RESERVED, Zero CHECKSUM of Internal Use Area (if used). Default value is 0.	0	00
227	E3		0	00
228	E4		0	00
229	E5		0	00
230	E6		0	00
231	E7		0	00
232	E8		0	00
233	E9		0	00
234	EA		0	00
235	EB		0	00
236	EC		0	00
237	ED		0	00
238	EE		0	00
239	EF		0	00
240	F0		0	00
241	F1		0	00
242	F2		0	00
243	F3		0	00
244	F4		0	00
248	F5		0	00
246	F6		0	00
247	F7		0	00
248	F8		0	00
249	F9		0	00
250	FA		0	00
251	FB		0	00
252	FC		0	00
253	FD		0	00
254	FE		0	00
255	FF		0	00

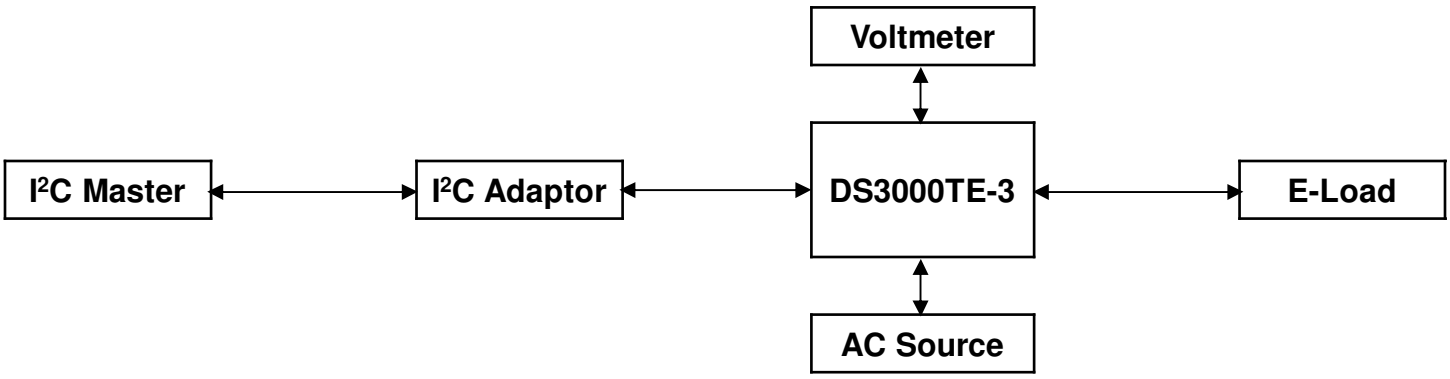
PMBus™ Interface Support

The DS3000TE-3 is compliant with the industry standard PMBus™ protocol for monitoring and control of the power supply via the I²C interface port.

DS3000TE-3 Series PMBus™ General Instructions

Equipment Setup

The following is typical I²C communication setup:



PMBus™ Writing Instructions

When writing to any PMBus™ R/W registers, ALWAYS do the following:

Disable Write Protect (command 10h) by writing any of the following accordingly:

Levels: 00h – Enable writing to all writeable commends

20h – Disables write except 10h, 01h, 00h, 02h and 21h commands

40h – Disables write except 10h, 01h, and 00h commends

80h – Disable write except 0x00h

To save changes on the USER PMBus™ Table:

Use send byte command: 15h STORE_USER_ALL

To save changes on the DEFAULT PMBus™ Table:

Use send byte command: 11h STORE_DEFAULT_ALL

Wait for 5 seconds, turn-off the PSU, wait for another 5 seconds before turning it on.

DS3000TE-3 Series Support PMBus™ Command List

The DS3000TE-3 is compliant with the industry standard PMBus™ protocol for monitoring and control of the power supply via the i²C interface port.

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
00h	Page	00	R	1		Default: 00h
01h	OPERATION	80	R/W	1		Used to Turn the unit ON/OFF in conjunction with the input CONTROL pin. It is also used to set output to upper or lower Margin Voltages.
	b7:6					00 – Immediate Turn OFF (No Sequencing) 01 – Soft Turn OFF (With Sequencing) 10 – PSU ON
	b5:4					
	b3:2					
	b1:0					Reserved
02h	ON_OFF_CONFIG	1C	R	1		Configures the combination of CONTROL pin and serial communication commands needed to turn the Unit ON/OFF.
	b7:5					Reserved
	b4 – Enable CONTROL pin and Serial communication control.	1				0 – Unit powers up any time power is present regardless of the state of CONTROL pin. 1 – Unit powers up as dictated by CONTROL pin and OPERATION command (b3:0)
	b3 – Serial communication Control	1				0 – Unit Ignores ON/OFF portion of the OPERATION command.1 – Enables Serial communication ON/OFF portion of OPERATION command. Requires CONTROL pin to be asserted for the unit to start and energize the output.
	b2 – Sets how the unit responds to CONTROL pin	1				0 – Unit ignores CONTROL pin. (ON/OFF controlled by OPERATION command). 1 – Unit requires CONTROL pin to be asserted to start the unit.
	b1 - CONTROL pin polarity	0				0 – Active Low (Pull Low to start the unit) 1 – Active high (Pull high to start the unit)
	b0 – CONTROL pin Action	0				0 – Use programmed turn ON/OFF delay 1 – Turn OFF the output and stop transferring energy to the output as fast as possible.
03h	CLEAR_FAULTS		S			
10h	WRITE_PROTECT	00	R/W	1		Used to Control Writing to the PMBus Device 80h - Disables write except 10h 40h – Disables write except 10h, 01h, 00h 20h – Disables write except 10h,01h,00h,02h and 21h commands 00 – Enables write to all writeable commands.
15h	STORE_USER_ALL		S	0		Copies the Operating memory table to the matching USER non-volatile memory.

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
19h	CAPABILITY	90	R	1		Provides a way for the hosts system to determine some key capabilities of a PMBus device.
	b7 - Packet Error Checking	1				0 - PEC not supported 1 - PEC supported
	b6 - Maximum Bus Speed	0				0 - Maximum supported bus speed, 100KHz 1 - Maximum supported bus speed, 400KHz
	b5 - SMBALERT#	0				0 – SMBus Alert Pin not supported 1 – SMBus Alert Pin supported
	b4:0					Reserved
20h	VOUT_MODE	17	R	1		Specifies the mode and parameters of Output Voltage related Data Formats
21h	VOUT_COMMAND	1801	R/W	2	Linear	Sets the Output Voltage Reference Vout command sends discreet value to change or trim output voltage. Valid range is 11.4 to 12.6V.
24h	VOUT_MAX	1933	R	2	Linear	Read Only (12.6V)
30h	COEFFICIENTS	-	BW	6		use to retrieve the m, b and R coefficients, needed for DIRECT data format
	byte 1:2					mlow Byte, m high byte
	byte 3:4					b low Byte, b high byte
	byte 5					R byte
35h	VIN_ON	EAC0	R	2	Linear	Sets the value of input, in volts, at which the unit will start. ACGOOD 88Vac
36h	VIN_OFF	EA98	R	2	Linear	Sets the value of input, in volts, at which the unit will stop power conversion. ACBAD 83Vac
3Ah	FAN_ CONFIG_1_2	90	R	1		Read only to reflect setting of Fans
	b7	1				1 – Fan is installed in position 1 0 – No Fan is installed in position 1
	b6	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC
	b5:4	01				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
	b3	0				1 – Fan is installed in position 2 0 – No Fan is installed in position 2
	b2	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC
	b1:0	00				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
3Bh	FAN_COMMAND_1	0000	R/W	2	Linear	Adjusts the operation of the Fans. The device may override the command, if it requires higher value, to maintain proper device temperature. Duty cycle Control – Commands Speeds from 0 to 100%

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
40h	VOUT_OV_FAULT_LIMIT	1B99	R/W	2	Linear	Sets Output Over voltage threshold. (13V) Valid Range: 12.8 to 14V
41h	VOUT_OV_FAULT_RESPONSE	80	R	1		Unit Latches OFF. Resets on PSON or CONTROL pin recycle or AC recycle.
42h	VOUT_OV_WARN_LIMIT	1D28	R/W	2	Linear	Sets Over-voltage Warning threshold. (14V) Valid Range: 12.8 to 14V
44h	VOUT_UV_FAULT_LIMIT	1467	R/W	2	Linear	Sets Under-voltage Fault threshold. (10V) Valid Range: 10.2 to 11V
45h	VOUT_UV_FAULT_RESPONSE	80	R	1		Turn PSU OFF
46h	IOUT_OC_FAULT_LIMIT	FA1C at High Line EBB0 at Low Line	R/W	2	Linear	Sets the Over current threshold in Amps. (270A for Hi Line and 118A for Low Line)
47h	IOUT_OC_FAULT_RESPONSE	C0	R	1		OCP ride through. If OCP persists.
4Ah	IOUT_OC_WARN_LIMIT	F3E8 at High Line EBB0 at Low Line	R/W	2	Linear	Sets the Over Current Warning threshold in Amps. (250A for Hi Line and 112A for Low Line)
4Fh	OT_FAULT_LIMIT	E320	R/W	2	Linear	Secondary ambient temperature Fault threshold, in degree C. (50degC), Valid Range: 50 to 95OC
50h	OT_FAULT_RESPONSE	B8	R	1		Turn PSU OFF and will retry indefinitely. Supported enable/disable of protection and recoverability.
51h	OT_WARN_LIMIT	E2D0	R/W	2	Linear	Secondary ambient temperature warning threshold, in degree C. Operating limit (45degC) Valid Range: 43 to 60OC
55h	VIN_OV_FAULT_LIMIT	FA26	R/W	2	Linear	Sets input over-voltage threshold. (275Vac)
56h	VIN_OV_FAULT_RESPONSE	00	R	1		
58h	VIN_UV_WARN_LIMIT	EA90	R/W	2	Linear	Default: 82Vac
59h	VIN_UV_FAULT_LIMIT	EA80	R/W	2	Linear	Default: 80Vac
5Ah	VIN_UV_FAULT_RESPONSE	C0	R	1		
5Eh	POWER_GOOD_ON	1785	R/W	2	Linear	Sets the threshold by which the Power Good Default: 11V
5Fh	POWER_GOOD_OFF	1467	R/W	2	Linear	Sets the threshold by which the Power Good Default: 10V
60h	TON_DELAY	0BE8	R/W	2	Linear	Sets the time (sec), from start condition (Power ON) until the output starts to rise. (2.2sec max) Default=2000ms
61h	TON_RISE	E320	R	2	Linear	Sets the time (ms), for the output rises from 0 to regulation. Default=50ms
62h	TON_MAX_FAULT_LIMIT	120D	R/W	2	Linear	Default: 2100 msec Min: 2000 msec Max: 3000 msec
63h	TON_MAX_FAULT_RESPONSE	C0	R			
64h	TOFF_DELAY	DAE0	R/W	2	Linear	Sets the time (ms), from a stop condition (Power OFF) until the output starts to drop (converter OFF). Default: 23mS Min: 0 msec Max: 50 msec

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
78h	STATUS_BYTE	00	R	1		Returns the summary of critical faults
	b7 – BUSY	-				Not supported
	b6 – OFF	-				Unit is OFF
	b5 – VOUT_OV	-				Output over-voltage fault has occurred
	b4 – IOUT_OC	-				Output over-current fault has occurred
	b3 - VIN_UV	-				An input under--voltage fault has occurred
	b2 - TEMPERATURE	-				A temperature fault or warning has occurred
	b1 – CML	-				A communication, memory or logic fault has occurred.
	b0 – NONE OF THE ABOVE	-				A Fault Warning not listed in bits[7:1] has occurred.
79h	STATUS_WORD	0000	R	2		Summary of units Fault and warning status.
	b15 – VOUT					An output voltage fault or warning has occurred
	b14 – IOUT/POUT					An Output current or power fault or warning has occurred.
	b13 – INPUT					An input voltage, current or power fault or warning as occurred.
	b12 – MFR					A manufacturer specific fault or warning has occurred.
	b11 – POWER_GOOD#					The POWER_GOOD signal is de-asserted
	b10 - FANS					A fan or airflow fault or warning has occurred.
	b9 – OTHER					Not supported
	b8 – UKNOWN					Not supported
	b7 – BUSY					A fault was declared because the device was busy and unable to respond.
	b6 – OFF					Unit is OFF
	b5 – VOUT_OV					Output over-voltage fault has occurred
	b4 – IOUT_OC					Output over-current fault has occurred
	b3 - VIN_UV					An input under-voltage fault has occurred
	b2 – TEMPERATURE					A temperature fault or warning has occurred
	b1 – CML					A communication, memory or logic fault has occurred.
	b0 – NONE_OF_THE_ABOVE					A fault or warning not listed in bits[7:1] of this byte has occurred.
7Ah	STATUS_VOUT	00	R/W	1		Output voltage related faults and warnings
	b7					VOUT Over--voltage Fault
	b6					VOUT Over-voltage warning
	b5					VOUT Under-voltage Warning
	b4					VOUT Under-voltage Fault
	b3					VOUT_MAX Warning, an attempt has been made to set output to a value higher that the highest permissible voltage. Not supported
	b2					TON_MAX_FAULT
	b1					TOFF_MAX Warning. Not supported
	b0					Not supported.

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
7Bh	STATUS_IOUT	00	R/W	1		Output Current related faults and warnings
	b7					IOUT Over current Fault
	b6					IOUT Over current And Low Voltage shutdown Fault. Not supported
	b5					IOUT Overcurrent Warning
	b4					IOUT Undercurrent Fault. Not supported
	b3					Current Share Fault. Not supported
	b2					Power Limiting. Not supported
	b1					POUT Overpower Fault. Not supported
	b0					POUT Overpower Warning
7Ch	STATUS_INPUT	00	R/W	1		Input related faults and warnings
	b7					VIN Overvoltage Fault
	b6					VIN Overvoltage Warning . Not supported
	b5					VIN Undervoltage Warning
	b4					VIN Undervoltage Fault
	b3					Unit is OFF for insufficient Input Voltage
	b2					IIN Overcurrent Fault . Not supported
	b1					IIN Overcurrent Warning . Not supported
	b0					PIN Overpower Warning . Not supported
7Dh	STATUS_TEMPERATURE	00	R/W	1		Temperature related faults and warnings
	b7					Overtemperature Fault
	b6					Overtemperature Warning
	b5					Undertemperature Warning . Not supported
	b4					Undertemperature Fault . Not supported
	b3:0					Reserved
7Eh	STATUS_CML	00	R/W	1		Communications, Logic and Memory
	b7					Invalid or unsupported Command Received
	b6					Invalid Data
	b5					Packet Error Check Failed
	b4					Memory Fault Detect, CRC Error
	b3					Not Supported
	b2					Not Supported
	b1					Not Supported
	b0					Not Supported
80h	STATUS_MFR_SPECIFIC	00	R/W	1		Manufacturer Status codes
	b7					Not Used
	b6					Not Used
	b5					Not Used
	b4					Not Used
	b3					Not Uesd
	b2					Not Uesd
	b1					Not Uesd
	b0					MFR SPECIFIC FAULT. FOR Trouble shooting

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
81h	STATUS_FANS_1_2	00	R/W	1		
	b7					Fan 1 Fault
	b6					Fan 2 Fault
	b5					Fan 1 Warning
	b4					Fan 2 Warning
	b3					Fan_1 Speed Overridden
	b2					Fan_2 Speed Overridden
	b1					Not Used
	b0					Not Used
86h	READ_EIN	-	R	2	Linear	Returns the accumulated input power over time
87h	READ_EOUT	-	R	2	Linear	Returns the accumulated output power over time
88h	READ_VIN	-	R	2	Linear	Returns input Voltage in Volts ac.
89h	READ_IIN	-	R	2	Linear	Returns input Current in Amperes
8Ah	READ_VCAP	-	R	2	Linear	Returns Bulk Capacitor voltage in Volts
8Bh	READ_VOUT	-	R	2	Linear	Returns the actual, measured voltage in Volts.
8Ch	READ_IOUT	-	R	2	Linear	Returns the output current in amperes.
8Dh	READ_TEMPERATURE_1	-	R	2	Linear	PSU's inter hot spot temperature typically that of the main output rall heat sink. Format is Linear-11
8Eh	READ_TEMPERATURE_2	-	R	2	Linear	PSU's system-side air inlet or internal ambient temperature . Format is Linear-11.
90h	READ_FAN_SPEED_1	-	R	2	Linear	Speed of Fan 1
96h	READ_POUT	-	R	2	Linear	Returns the output power, in W.
97h	READ_PIN	-	R	2	Linear	Returns the input power, in W.
98h	PMBus_REVISION	22	R	1	Linear	Reads the PMBus revision number
	b7:5	0001				Part 1 Revision 0000 – Revision 1.0 0001 – Revision 1.1
	b4:0	0001				Part 2 Revision 0000 – Revision 1.0 0001 – Revision 1.1
99h	MFR_ID	"ARTESYN"	BR	-	ASCII	Abbrev or symbol of manufacturers name. ASCII (artesyn)
9Ah	MFR_MODEL	"DS3000TE-3"	BR	15	ASCII	Manufacturers Model number, ASCII format
9Ch	MFR_LOCATION	"Philippines "	BR/W	6	ASCII	Manufacturers facility, ASCII format
A0h	MFR_VIN_MIN	EADO	R	2	Linear	Minimum Input Voltage (180Vac)
A1h	MFR_VIN_MAX	FA10	R	2	Linear	Maximum Input Voltage (264Vac)
A2h	MFR_IIN_MAX	DA30	R	2	Linear	Maximum Input Current (17.5A)
A3h	MFR_PIN_MAX	134B at High Line 0B14 at Low Line	R	2	Linear	Maximum Input Power (3372W for Hi Line and 1576W for Low Line)
A4h	MFR_VOUT_MIN	1747	R	2	Linear	Minimum Output Voltage Regulation Window. (11V)
A5h	MFR_VOUT_MAX	1933	R	2	Linear	Maximum Output Voltage. Regulation Window (12.6V)

DS3000TE-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
A6h	MFR_IOUT_MAX	F3E8 at High Line EB60 at Low Line	R	2	Linear	Maximum Output Current (250A for Hi Line and 248A for Low Line)
A7h	MFR_POUT_MAX	12EE at High Line 0AA3 at Low Line	R	2	Linear	Output Power (3000W for Hi Line and 1350W for Low Line)
A8h	MFR_TAMBIENT_MAX	E280	R	2	Linear	Maximum Operating Ambient Temperature (Secondary Ambient) (40degC)
A9h	MFR_TAMBIENT_MIN	000A	R	2	Linear	Minimum Operating Ambient Temperature (Secondary Ambient) (0degC)
AAh	MFR_EFFICIENCY_LL		BR	14		Default: 115V, 270W, 88 %, 675W, 91%, 1350W, 89%
ABh	MFR_EFFICIENCY_HL		BR	14		Default: 230V, 600W, 90 %, 1500W, 94 %, 3000W, 91 %
B0h	USER_DATA_00	-	BR/BW			
E0h	FW_PRI_VERSION		BR	8	ASCII	
E1h	FW_SEC_VERSION		BR	8	ASCII	
E2h	CONFIG_UNLOCK_CODE		BR	4	ASCII	
F1h	ISP_UNLOCK_CODE		BR/W	4	ASCII	00h,00h,00h,00h
F2h	ISP_CTRL_CMD		W	1		
F3h	ISP_STATUS_BYTE		R	1		
F4h	ISP_FLASH_ADDR		BR/W	4	Direct	
F5h	ISP_FLASH_DATA.		BR/W	4	Direct	

Application Notes

Current Sharing

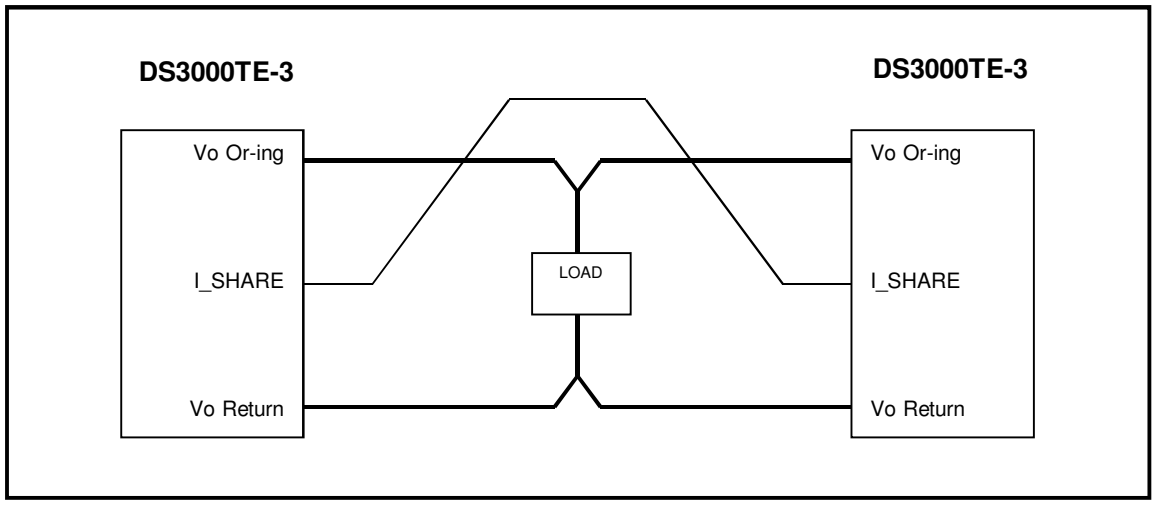
The DS3000TE-3 series' main output V_O is equipped with current sharing capability. This will allow up to 12 power supplies to be connected in parallel for higher power application, and more typically total of 8 in M+N mode in parallel. Current share accuracy is maximum 12A difference between any two supplies from 10% to 100% load per power supply unit. The current share signal Ishare, is expected to be a stable DC signal (oscillation free) and will be within the voltage range specified below. It will be capable of sinking 0.4 mA and sourcing 4 mA. The waveform for this signal will be provided to confirm stability during parallel mode operation.

Load of One Power Supply Unit	Minimum Ishare Voltage (V)	Typical Ishare Voltage (V)	Maximum Ishare Voltage (V)
100%	7.75	8	8.25
50%	3.85	4	4.15
0%	0	0.8	1.0
Load sharing Bandwidth (Hz)	-	400	-

Redundancy / Fault Tolerance

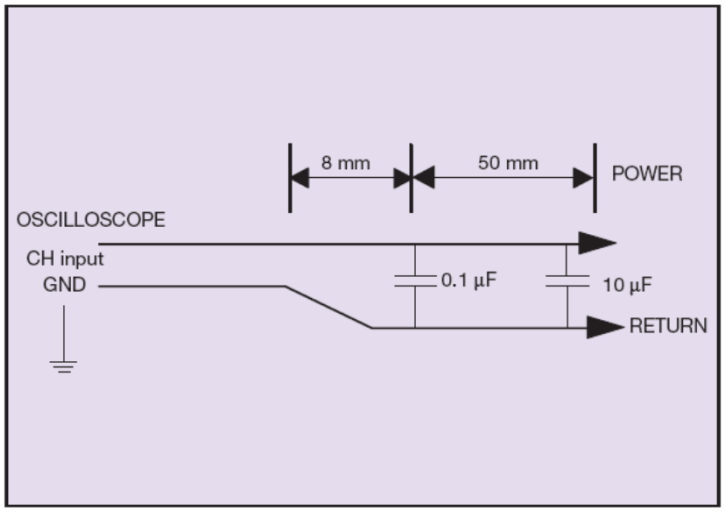
The DS3000TE-3 series power supplies is able to current share with 2(1+1) up to 12(6+6) power supplies in parallel and operate in a hot swap/redundant N+N configuration where N=1, 2, 3, 4, 5 or 6. The $12V_{SB}$ outputs of the power supplies are connected together in the system so that a failure or hot swap of a redundant power supply does not cause these outputs to go out of regulation in the system.

All power supply outputs will be designed for redundant mode operation. No internal failure in any power supply in this configuration will cause the bus voltage to fall below the regulation limits specified. All output voltages will stay within the regulation limits during cold swapping or hot swapping operation.



Output Ripple and Noise Measurement

The setup outlined in the diagram below has been used for output voltage ripple and noise measurements on the DS3000TE-3 Series. When measuring output ripple and noise, a scope jack in parallel with a 0.1uF ceramic chip capacitor, and a 10uF aluminum electrolytic capacitor will be used. Oscilloscope will be set to 20MHz bandwidth for this measurement.



Record of Revision and Changes

Issue	Date	Description	Originators
1.0	04.10.2017	First Issue	D. Hou

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