

1.1

Data Sheet

Total power: 44 W Input voltage: 12 V

KEY FEATURES:

- 8 A output current
- 12 V input voltage
- Wide-output voltage adjust
 - 1.2 Vdc to 5.5 Vdc for suffix 'W' and 0.8 Vdc to 1.8 Vdc for suffix 'L'
- Auto-track[™] sequencing
- Pre-bias start-up
- Efficiencies up to 93%
- Output ON/OFF inhibit
- Vertical through-hole mounting
- Point-of-Load-Alliance (POLA) compatible
- Undervoltage lockout
- Available RoHS compliant



PTV12010

12 Vin single output

Electrical Specification	IS	
Efficiency		See Tables on page 2
Insulation voltage		Non-isolated
Switching frequency		
Suffix 'W'	250-400 kHz	325 kHz typ.
Suffix 'L'	200-300 kHz	250 kHz typ.
Approvals and standards		EN60950
Approvais and standards		UL/cUL60950
Material flammability		UL94V-0
Dimensions		22.86 x 8.38 x 10.16 mm
	(= × v v × 1 1)	0.90 x 0.330 x 0.400 in
Weight		2.6 g (0.09 oz)
MTBF	Telcordia SR-332	5,000,000 hours



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Input		
Input voltage range	(See Note 3)	10.8 V-13.2 Vdc
Input standby current		10 mA typ.
Remote ON/OFF	(See Note 1)	Positive logic
Undervoltage lockout	(Increasing)	9.5 V typ.
Track input current	Pin 5 (See Notes 6 and 7)	-0.13 mA

Output		
Voltage adjustability (See Note 4)	Suffix (W/ Suffix (L)	1.2-5.5 Vdc
		0.8-1.8 Vdc
Setpoint accuracy	(See Note 8)	±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation	(See Note 8)	±3.0% Vo
Minimum load		0 A
Ripple and noise 20 MHz	Suffix 'W' Suffix 'L'	20 m/pk-pk 15 m/pk-pk
bandwidth		
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)		70 µs recovery time
		Overshoot/undershoot 100 mV

EMC Characteristics		
Electrostatic discharge	EN61000-4-2, IEC801-2	
Conducted immunity	EN61000-4-6	
Radiated immunity	EN61000-4-3	

ENVIRONMENTAL SPECIFICATIONS		
Thermal performance (See Note 2)	Operating ambient, temperature	-40 °C to +85 °C
	Non-operating	-40 °C to +125 °C

PROTECTION		
Overcurrent	Auto reset	16 A typ.

	INPUT	OUTPUT	OUTPUT	OUTPUT	EFFICIENCY	REGUL	ATION	MODEL
(MAX.)	VOLTAGE	VOLTAGE	(MIN.)	(MAX.) ⁽²⁾	(MAX.)	LINE	LOAD	NUMBER
15 W	10.8-13.2 Vdc	0.8-1.8 Vdc	0 A	8 A	87%	±10 mV	±12 mV	PTV12010L
44 W	10.8-13.2 Vdc	1.2-5.5 Vdc	0 A	8 A	92%	±10 mV	±12 mV	PTV12010W

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated C_{in} = 100 µF and 10 µF (Ceramic), C_{out} = 0 µF



Part Number System with Options



EFFICIENCY TABLE - PTV12010L (I ₀ = I _{0MAX})		
OUTPUT VOLTAGE	EFFICIENCY	
Vo = 1.8 V	87%	
Vo = 1.5 V	86%	
Vo = 1.2 V	84%	
Vo = 1.0 V	81%	
Vo = 0.8 V	78%	

Notes

- 1 Remote ON/OFF. Positive logic ON: Pin 7 open; or V > 2 V OFF: Pin 7 GND; or V < 0.6 V
- 2 See Figures 1, 2, 3 and 6 for safe operating curves.
- 3 A 100 μF electrolytic input capacitor is required for proper operation as well as a 10 μF high-frequency ceramic capacitor. The electrolytic capacitor must be rated for the minimum rms of ripple current.
- 4 An external output capacitor is not required for basic operation. Adding 100 μF of distributed capacitance at the load will improve the transient response.
- 5 1 A/ μ s load step, 50 to 100% I $_{omax}$, C3 = 100 μ F.
- 6 If utilized Vout will track applied voltage by ± 0.3 V (up to Vo set point).
- 7 The pre-bias start-up feature is not compatible with Auto-Track[™]. This is because when the module is under Auto-Track[™] control, it

EFFICIENCY TABLE - PTV12010W (I _o = I _{omax})				
OUTPUT VOLTAGE	EFFICIENCY			
Vo = 5.0 V	92%			
Vo = 3.3 V	90%			
Vo = 2.5 V	88%			
Vo = 1.8 V	85%			
Vo = 1.5 V	83%			
Vo = 1.2 V	80%			

is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track[™] function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 196 for more details.

- 8 The set-point voltage tolerance is affected by the tolerance and stability of $\rm R_{set}.$ The stated limit is unconditionally met if $\rm R_{set}$ has a tolerance of 1% with 100/°C or better temperature stability.
- 9 To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTV12010WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at

http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.



PTV12010W CHARACTERISTIC DATA



Figure 1 - Safe Operating Area Vin = 12 V, Output Voltage = 5 V (See Note A)



Figure 3 - Safe Operating Area Vin = 12 V, Output Voltage = 1.8 V (See Note A)



Figure 5 - Standard Application

Notes

- 1. SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- 2. Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.



Figure 2 - Safe Operating Area Vin = 12 V, Output Voltage = 3.3 V (See Note A)



Figure 4 - Efficiency vs Load Current Vin = 12 V (See Note B)



PTV12010L CHARACTERISTIC DATA



Figure 6 - Safe Operating Area Vin = 12 V, Output Voltage 1.8 V (See Note A)



Figure 8 - Standard Application

Notes

- 1. SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- 2. Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.





Figure 7 - Efficiency vs Load Current Vin = 12 V (See Note B)

PTV12010 Data Sheet



10 10 10 10

PIN CONNECTIONS

Americas 2900 South Diablo Way

Tempe, AZ 85282, USA

+1 888 412 7832

Suite B100

PIN NO.	FUNCTION
1	Ground
2	Vout
3	Vout
4	Vo Adjust
5	Track
6	Ground
7	Inhibit
8	Vin

Figure 9 - Mechanical Drawing and Pinout Table

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