#### NOV 2019 Version1.0

# Ascend Semicondutor Co.,Ltd

## **30V N-CHANNEL MOSFET**

ASDM30N65E

#### **Product Summary**

V <sub>DS</sub>	30	V
$R_{DS(on),typ} V_{GS}$ =10V	4.5	mΩ
Ι <sub>D</sub>	65	Α

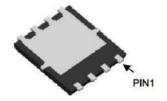
• Green Device Available

• Low Gate Charge

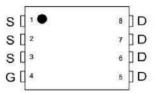
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

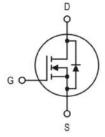
### **Applications**

- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.



DFN3.3\*3.3-8L





**Absolute Maximum Ratings** 

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	32	A
I⊳@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	n Current, V <sub>GS</sub> @ 10V <sup>1</sup> 26	
Ідм	Pulsed Drain Current <sup>2</sup>	100	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	61.3	
las	Avalanche Current	35	A
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation <sup>4</sup>	25	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150 °C	
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		60	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>		5	°C/W



Feature



65



### N-Channel Electrical Characteristics (TJ=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
Bravers	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		4.5	5.2	
Rds(ON)	Static Drain-Source On-Resistance-	V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		7.2	9	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.7	2.2	V
lana	Drain Source Lookage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , TJ=25℃			1	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , TJ=55℃			5 uA	uA
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		65		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	0.8	1.7	2.6	Ω
Qg	Total Gate Charge (4.5V)			9		
Qgs	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A		2.8		nC
$Q_{gd}$	Gate-Drain Charge			3.6		
T <sub>d(on)</sub>	Turn-On Delay Time			7		
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =3 $\Omega$		18.8		20
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =20A		19.5		ns
Tf	Fall Time			3.4		
Ciss	Input Capacitance			1113		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		436		pF
Crss	Reverse Transfer Capacitance			55		

#### **Diode Characteristics**

Symbol	ool Parameter Conditions		Min.	Тур.	Max.	Unit
ls	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			20	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°⊂			1	V

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =35A

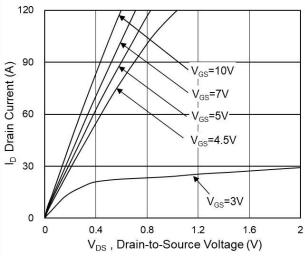
4.The power dissipation is limited by 150°C  $\,$  junction temperature

5. The data is theoretically the same as  $I_{\text{D}}$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

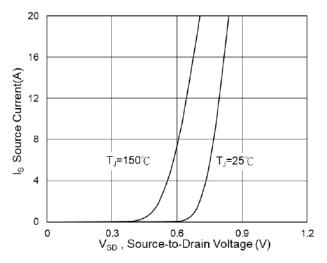




#### **N- Channel Typical Characteristics**







**Fig.3 Source Drain Forward Characteristics** 

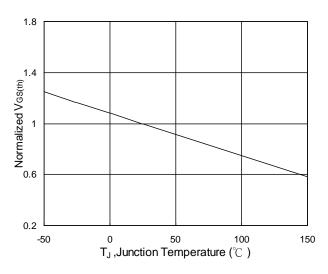


Fig.5 Normalized V<sub>GS(th)</sub> vs T<sub>J</sub>

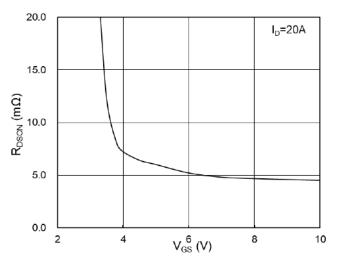


Fig.2 On-Resistance vs G-S Voltage

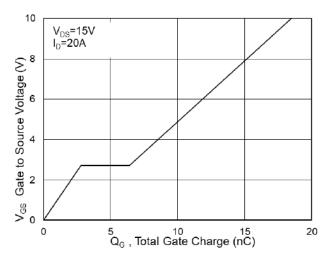


Fig.4 Gate-Charge Characteristics

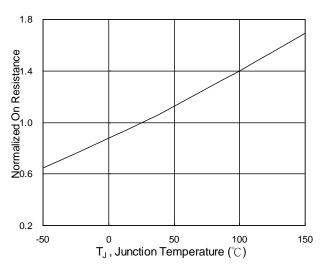
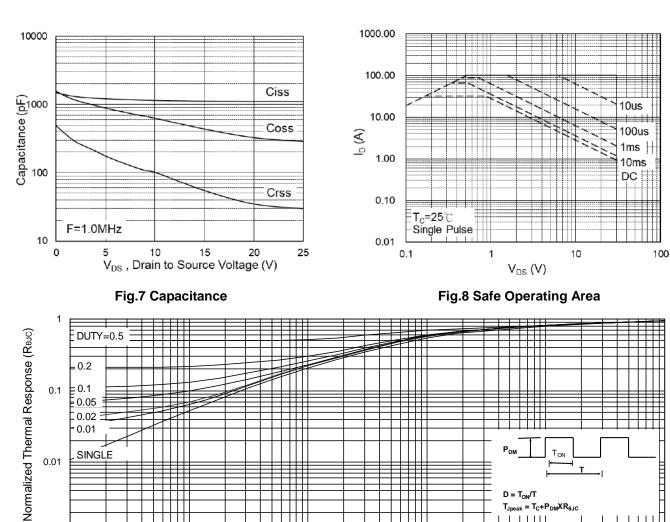


Fig.6 Normalized RDSON vs TJ

# ASDM30N65E





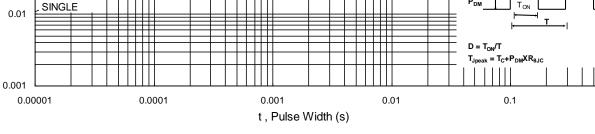
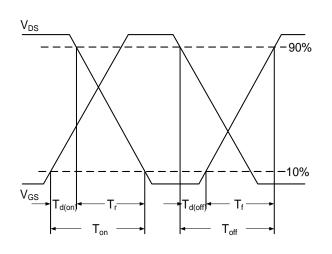


Fig.9 Normalized Maximum Transient Thermal Impedance





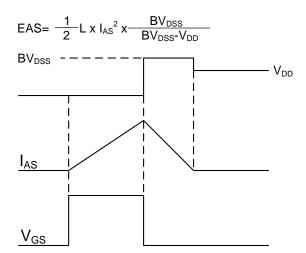


Fig.11 Unclamped Inductive Switching Waveform

**ASCENDSEMI** 

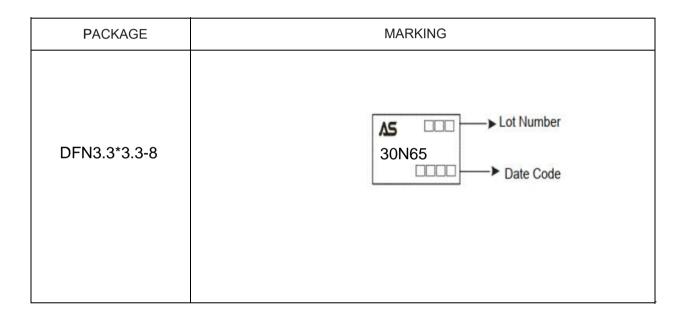
0.05 0.02 = 0.01

1



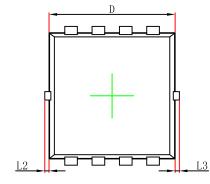
## **Ordering and Marking Information**

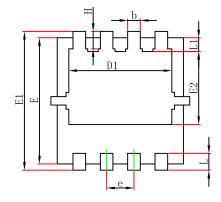
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM30N65E-R	30N65	DFN3.3*3.3-8	Tape&Reel	5000





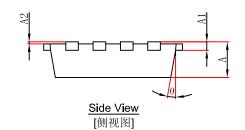
## DFN (3.3×3.3)-8L(P0.65T0.80) PACKAGE OUTLINE DIMENSIONS





<u>Top View</u> [顶视图]

<u>Bottom View</u> [背视图]



Symbol	<b>Dimensions In Millimeters</b>		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.650	0.850	0.026	0.033	
A1	0.152	REF.	0.006	REF.	
A2	0~0	).05	0~0	.002	
D	2.900	3.100	0.114	0.122	
D1	2.300	2.600	0.091	0.102	
E	2.900	3.100	0.114	0.122	
E1	3.150	3.450	0.124	0.136	
E2	1.535	1.935	0.060	0.076	
b	0.200	0.400	0.008	0.016	
е	0.550	0.750	0.022	0.030	
L	0.300	0.500	0.012	0.020	
L1	0.180	0.480	0.007	0.019	
L2	0~0.100		0~0.004		
L3	0~0	.100	0~0.004		
Н	0.315	0.515	0.012	0.020	
θ	9°	13°	9°	13°	



#### IMPORTANT NOTICE

Xi'an Ascend Semiconductor incorporated MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Xi'an Ascend Semiconductor Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Xi'an Ascend Semiconductor Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Xi'an Ascend Semiconductor Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume.

all risks of such use and will agree to hold Ascendsemi Incorporated and all the companies whose products are represented on Xi'an Ascend Semiconductor Incorporated website, harmless against all damages.

Xi'an Ascend Semiconductor Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Xi'an Ascend Semiconductor Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Xi'an Ascend Semiconductor Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

www.ascendsemi.com

## **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for MOSFET category:

Click to view products by Ascend manufacturer:

Other Similar products are found below :

614233C 648584F NTNS3A92PZT5G IRFD120 IRFF430 JANTX2N5237 2N7000 2SK2464-TL-E FCA20N60\_F109 FDZ595PZ AOD464 2SK2267(Q) 2SK2545(Q,T) 405094E 423220D MIC4420CM-TR VN1206L 614234A 715780A SSM6J414TU,LF(T 751625C IPP60R600P6XKSA1 RJK60S5DPK-M0#T0 BSC884N03MS G BSF024N03LT3 G PSMN4R2-30MLD TK31J60W5,S1VQ(O 2SK2614(TE16L1,Q) DMN1017UCP3-7 EFC2J004NUZTDG FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 NTE2969 NTE6400A DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 SSM6P54TU,LF DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7 STF5N65M6 STU5N65M6 C3M0021120D DMN13M9UCA6-7 BSS340NWH6327XTSA1