

-20V P-CHANNEL MOSFET

Features

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Motor control and drive
- Battery management
- UPS (Uninterrupible Power Supplies)

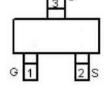
Product Summary

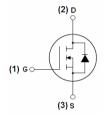


V DS	-20	V
$R_{DS(on),TYP}@V_{GS}=-4.5 V$	32	mΩ
I D	-4.1	А

top view







SOT-23

N-Channel

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _G S	±12	V
Continuous Drain Current	T _C =25℃		-4.1	A
	T _C =70 °C		-3.2	
	T _A =25℃	- I _D	-3	
	T _A =70°C		-2.3	
Drain Current -Pulsed (Note 1)		I _{DM}	-15	А
Maximum Power Dissipation		P _D	1.7	W
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	74	°C/W



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Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	•					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	- 20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	•					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-0.45	-0.7	-1.0	V
Duein Course On Otata Basistana	Б	V _{GS} =-4.5V, I _D =-4.1A	-	32	42	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-2.5V, I _D =-3A	-	50	75	
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-2A	6	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{Iss}		-	740	-	PF
Output Capacitance	Coss	V_{DS} =-4V, V_{GS} =0V, F=1.0MHz	-	290	-	PF
Reverse Transfer Capacitance	C _{rss}	F-1.0WINZ	-	190	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =-4 V , I_{D} =-3.3 A ,	-	35	-	nS
Turn-Off Delay Time	t _{d(off)}	R_L =-1.2 Ω , V_{GEN} =-4.5 V , R_g =1 Ω	-	30	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg		-	7.8	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-4V,I _D =-4.1A,V _{GS} =-4.5V	-	1.2	-	nC
Gate-Drain Charge	Q _{gd}		-	1.6	-	nC
Drain-Source Diode Characteristics	•					•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1.6A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is		-	-	1.6	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



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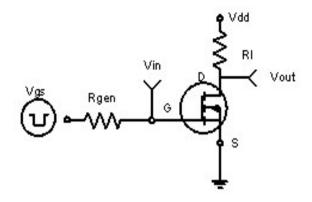
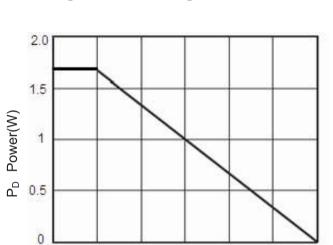


Figure 1:Switching Test Circuit



T_J-Junction Temperature(°ℂ)

0

25

Figure 3 Power Dissipation

125

150

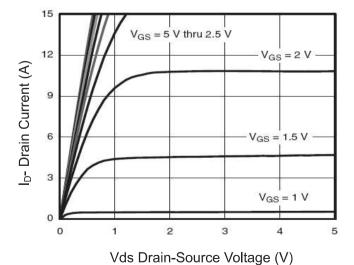


Figure 5 Output Characteristics

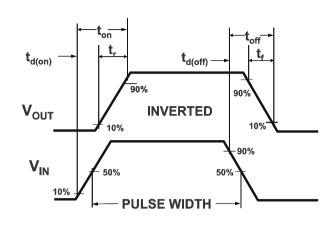
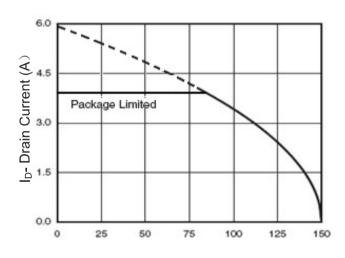


Figure 2:Switching Waveforms



T_J-Junction Temperature(°C)

Figure 4 Drain Current

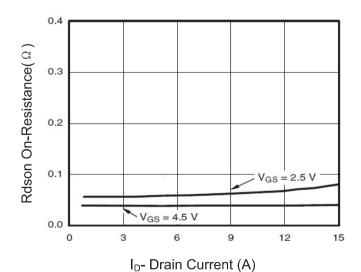


Figure 6 Drain-Source On-Resistance



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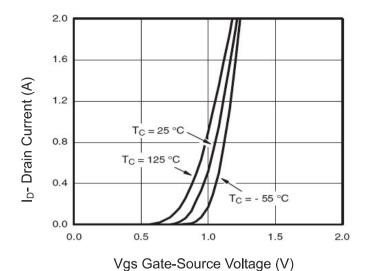
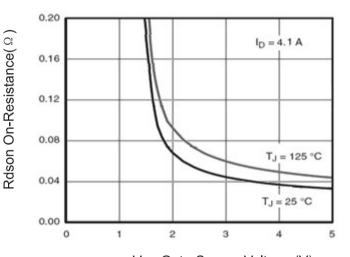


Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

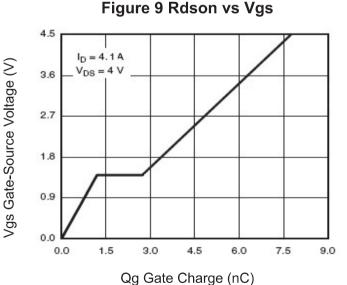


Figure 11 Gate Charge

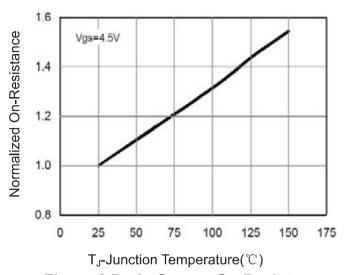
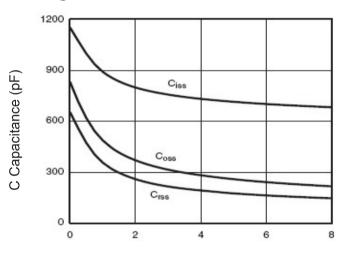


Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds

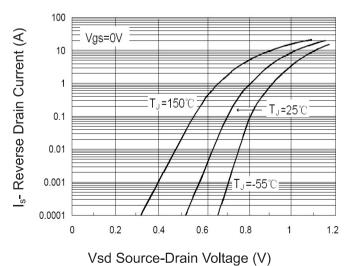
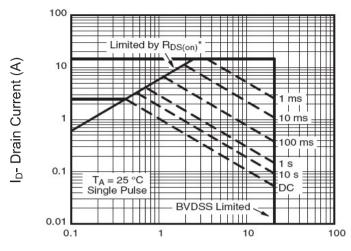


Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

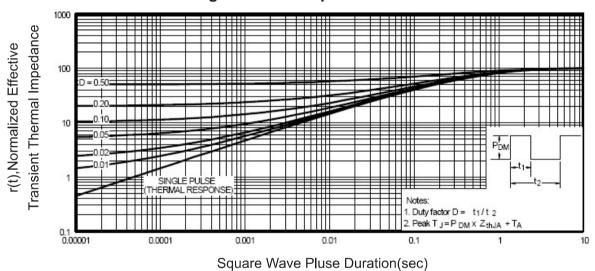


Figure 14 Normalized Maximum Transient Thermal Impedance





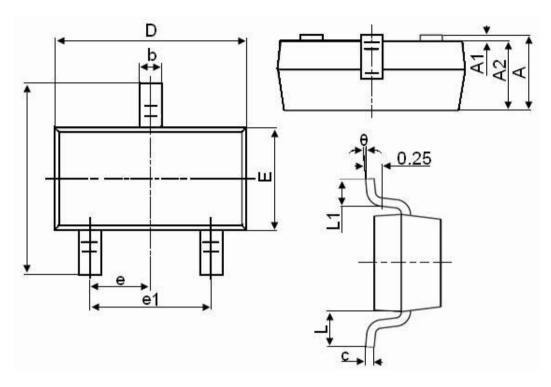
Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM3415ZA-R	3415	SOT23	Tape&Reel	3000/Reel

PACKAGE	MARKING	
SOT23	3415	



SOT-23 Package Information



Cumbal	Dimensions in Millimeters		
Symbol	MIN.	MAX.	
А	0.900	1.150	
A1	0.000	0.100	
A2	0.900	1.050	
b	0.300	0.500	
С	0.080	0.150	
D	2.800	3.000	
E	1.200	1.400	
E1	2.250	2.550	
е		0.950TYP	
e1	1.800	2.000	
L		0.550REF	
L1	0.300	0.500	
θ	0°	8°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $\hbox{5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.}\\$



ASDM3415ZA

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