

### General Features

- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

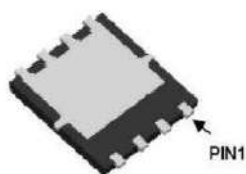
### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

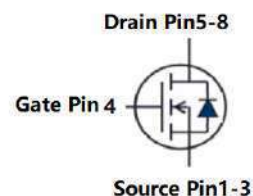
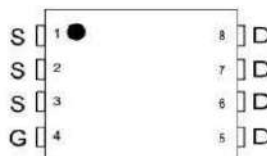
### Product Summary



$V_{DS}$	40	V
$R_{DS(on),Typ} @ V_{GS}=10V$	1.1	m $\Omega$
$I_D$	200	A



DFN5\*6-8



N-Channel

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous (Silicon Limited)	$I_D$	200	A
Drain Current-Continuous( $T_C=100^\circ\text{C}$ )	$I_D (100^\circ\text{C})$	145	A
Pulsed Drain Current	$I_{DM}$	800	A
Maximum Power Dissipation	$P_D$	114	W
Derating factor		0.91	W/ $^\circ\text{C}$
Single pulse avalanche energy	$E_{AS}$	238	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ\text{C}$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.1	$^\circ\text{C/W}$
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**Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	-	2.0	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =100A	-	1.1	1.4	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =100A	-	1.6	2.0	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =100A	-	160	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, F=1.0MHz	-	5400	-	PF
Output Capacitance	C <sub>OSS</sub>		-	1418	-	PF
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	81	-	PF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =20V, I <sub>D</sub> =100A V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω	-	24	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	84	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	62	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	20	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =100A, V <sub>GS</sub> =10V	-	45	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	15	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	18	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =100A	-	-	0.8	V
Diode Forward Current	I <sub>S</sub>		-	-	200	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = I <sub>S</sub>	-	88	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs <sup>(Note3)</sup>	-	185	-	nC



Typical Electrical and Thermal Characteristics

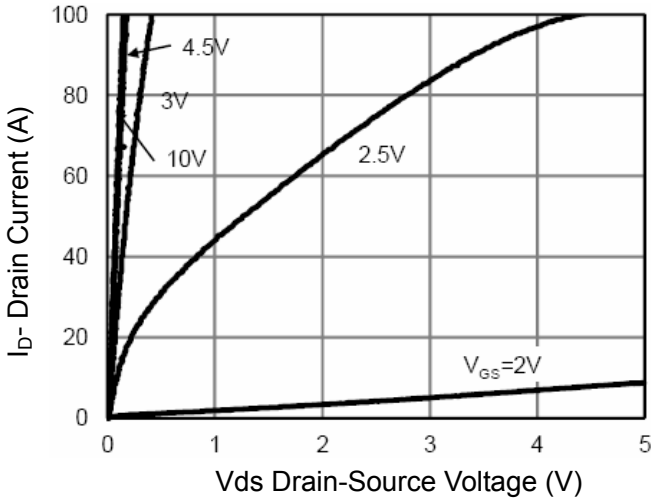


Figure 1 Output Characteristics

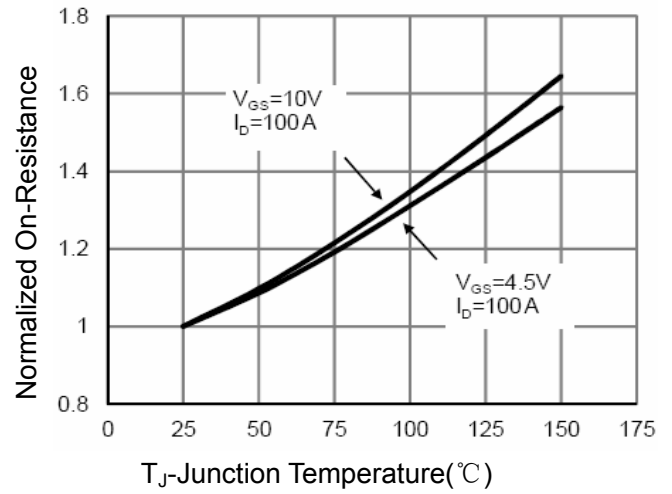


Figure 4 Rdson-Junction Temperature

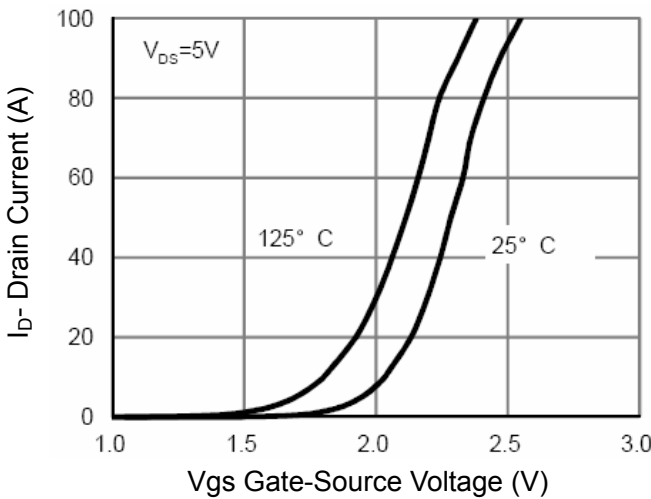


Figure 2 Transfer Characteristics

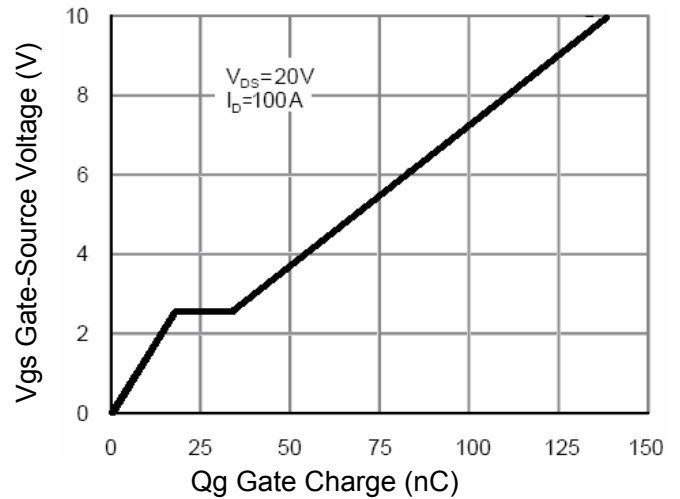


Figure 5 Gate Charge

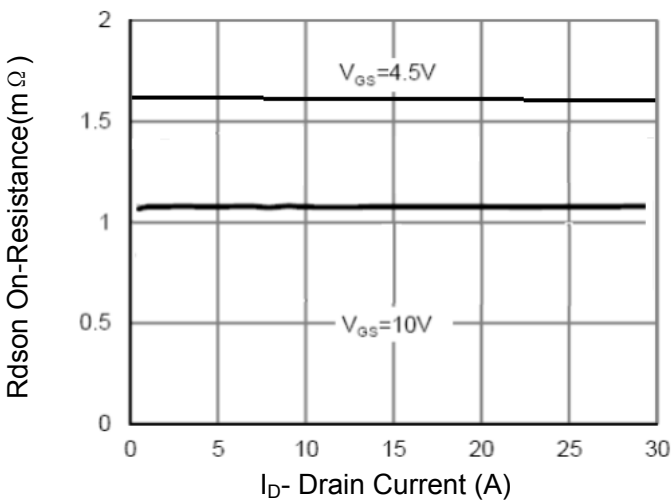


Figure 3 Rdson- Drain Current

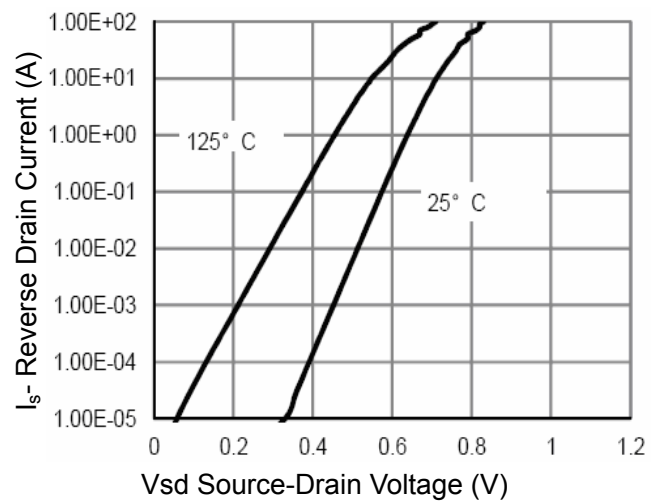


Figure 6 Source- Drain Diode Forward

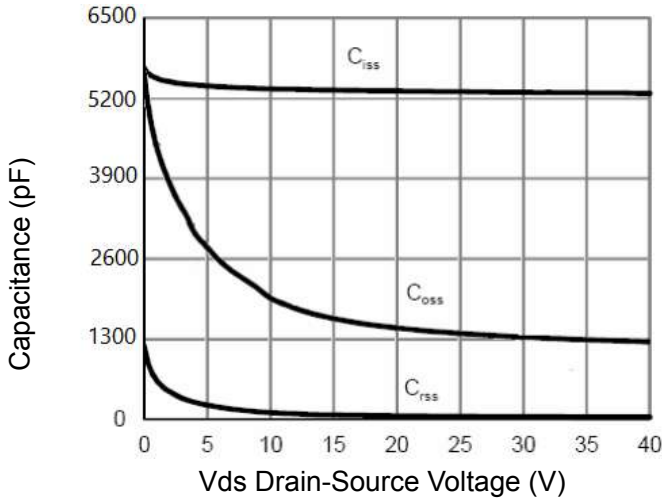


Figure 7 Capacitance vs Vds

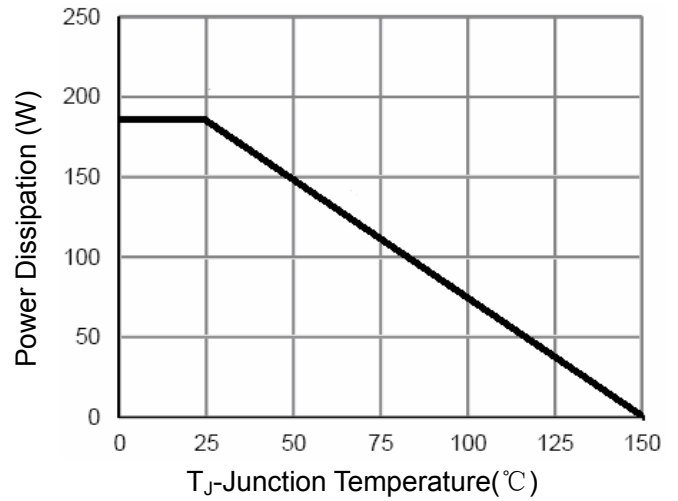


Figure 9 Power De-rating

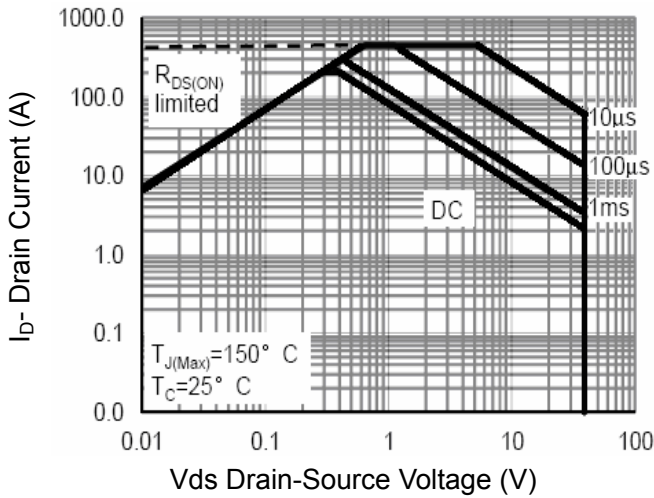


Figure 8 Safe Operation Area

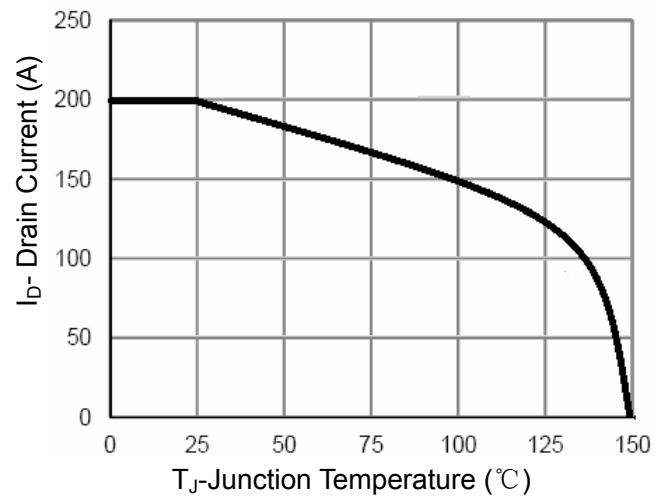


Figure 10 Current De-rating

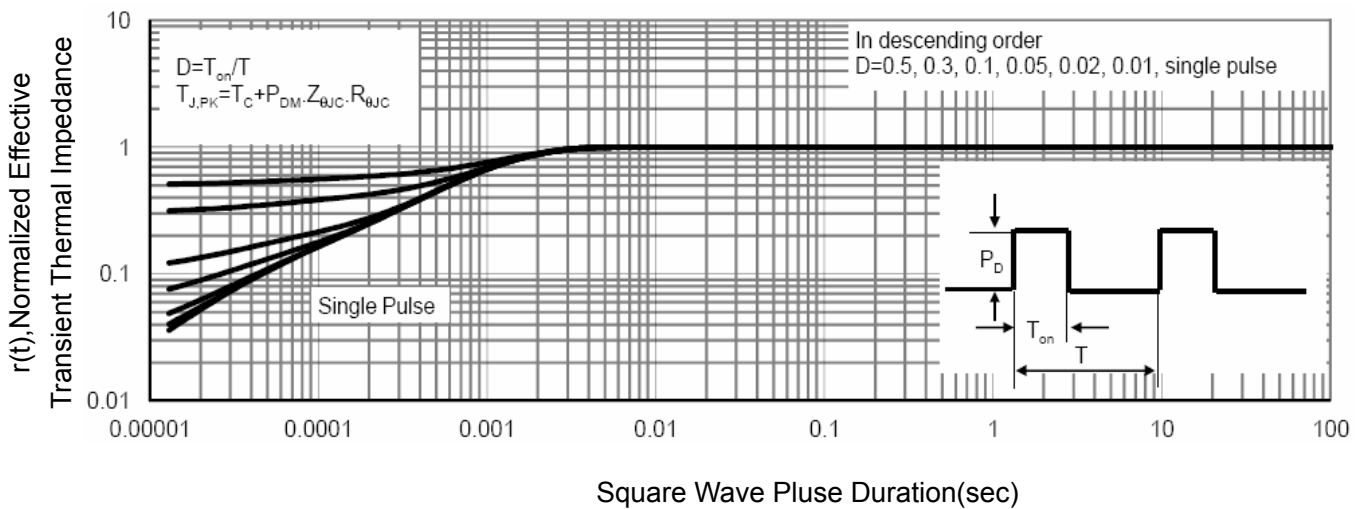
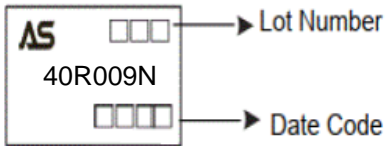


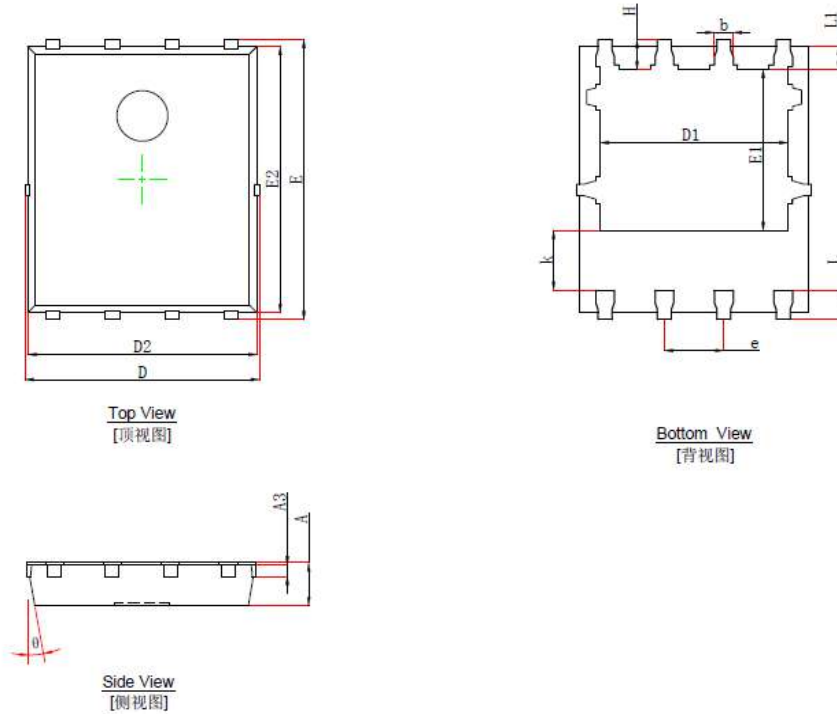
Figure 11 Normalized Maximum Transient Thermal Impedance

## Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM40R009NQ-R	40R009N	DFN5*6-8	Tape&Reel	4000/Reel

PACKAGE	MARKING
DFN5*6-8	 <p>The marking diagram shows a rectangular package with the following markings: 'AS' logo in the top left, '40R009N' in the center, and two sets of four squares (representing Lot Number and Date Code) at the bottom. Arrows point from the Lot Number and Date Code labels to their respective square markings.</p>

### DFN5x6\_P, 8 Leads



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°



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