

ASDM4410S

100V Dual N & P-Channel PowerTrench

Features

- N-Channel 100V/5A, R_{DS}(ON) = 95 mΩ @ VGS = 10V
- P-Channel -100V/-4A,
 R_{DS}(ON) = 185mΩ @ VGS = -10V

Application

- DC-DC primary bridgeDC-DC Synchronous rectificationHot swap
- •Fan drive

Product Summary



N-Channel

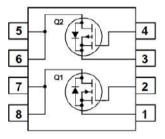
V ds	100	V
R DS(on), Typ@ VGS=10 V	95	mΩ
/ D	5	Α

• P-Channel

VDS	-100	V
R DS(on),Typ@ VGS=-10 V	185	mΩ
/ D	-4	А



Simplified Schematic



Absolute Maximum Ratings $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		N-CH	P-CH	Units
V _{DSS}	Drain-Source Voltage		100	-100	V
V _{GSS}	Gate-Source Voltage		±20	±20	V
I _D	Drain Current - Continuous	(Note 1a)	5	-4	A
	- Pulsed	Γ	20	-20	
PD	Power Dissipation for Dual Operation		2.5		W
	Power Dissipation for Single Operation	(Note 1a)	1.	.6	
		(Note 1b)	Î		
		(Note 1c)	0.	.9	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to	+150	°C

Thermal Characteristics

R _{0JA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case	(Note 1)	40	°C/W



Electrical Characteristics

Symbol	Parameter	Test Conditions	Туре	Min	Тур	Max	Units		
Off Char	Off Characteristics								
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA V _{GS} = 0 V, I _D = -250 μA	N-CH P-CH	100 -100			V		
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C I_D = -250 µA, Referenced to 25°C	N-CH P-CH		25 -22		mV/°C		
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24 V, V_{GS} = 0 V$ $V_{DS} = -24 V, V_{GS} = 0 V$	N-CH P-CH			1 -1	μA		
I _{GSSF}	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	All			100	nA		
I _{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = -20 V, V_{DS} = 0 V$	All			-100	nA		

 $T_A = 25^{\circ}C$ unless otherwise noted

On Characteristics (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ $V_{DS} = V_{GS}, I_D = -250 \ \mu A$	N-CH P-CH	1 -2	1.6 -	3 -4	V
$rac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C I_D = -250 µA, Referenced to 25°C			-4.3 4		mV/°C
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4 A, T _J =25°C	N-CH		95	100	mΩ
		V _{GS} = -10 V, I _D = -3 A,T _J =25°C	P-CH		185	200	11152
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V V _{GS} = -10 V, V _{DS} = -5 V	N-CH P-CH	5 -4			A
g fs	Forward Transconductance	V _{DS} = 5 V, I _D = 7 A V _{DS} = -5 V, I _D =-5 A	N-CH P-CH		11 11		S

Dynamic Characteristics

Ciss	Input Capacitance	N-CH	N-CH	620	pF
		V _{DS} = 10 V, V _{GS} = 0 V, f = 1.0 MHz	P-CH	620	-
Coss	Output Capacitance		N-CH	120	pF
		P-CH	P-CH	220	-
C _{rss}	Reverse Transfer Capacitance	V_{DS} = -10 V, V_{GS} = 0 V, f = 1.0 MHz	N-CH	31	pF
			P-CH	65	

Switching Characteristics (Note 2)

t _{d(on)}	Turn-On Delay Time	N-CH	N-CH	12	ns
-()	5	$V_{DD} = 10 V, I_D = 1 A,$	P-CH	14	
t _r	Turn-On Rise Time	V_{GS} = 10V, R_{GEN} = 6 Ω	N-CH	400	ns
			P-CH	160	
t _{d(off)}	Turn-Off Delay Time	P-CH	N-CH	20	ns
. ,	-	$V_{DD} = -10 V, I_D = -1 A,$	P-CH	35	
t _f	Turn-Off Fall Time	V_{GS} = -10V, R_{GEN} = 6 Ω	N-CH	120	ns
			P-CH	60	
Q _g	Total Gate Charge	N-CH	N-CH	12	nC
5		V_{DS} = 15 V, I_{D} = 4 A, V_{GS} = 10 V	P-CH	21	
Q _{gs}	Gate-Source Charge		N-CH	2.5	nC
		P-CH	P-CH	4.6	
Q_{gd}	Gate-Drain Charge	V _{DS} = -15 V, I _D = -3 A,V _{GS} = -10 V	N-CH	9.0	nC
5			P-CH	11.5	

Drain–Source Diode Characteristics and Maximum Ratings

Is	Maximum Continuous Drain-Source Diode Forward Current		-5	^
-		P-CH	-4	A
V _{SD}	Drain-Source Diode Forward $V_{GS} = 0 V$, $I_S = 1.3 A$ (Note 2	N-CH	1.5	
	Voltage $V_{GS} = 0 V$, $I_S = -1.3 A$ (Note 2	P-CH	-1.2	V



Typical Characteristics:N-channel

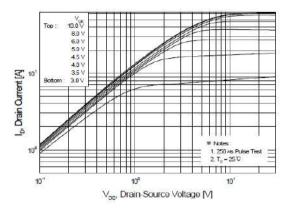
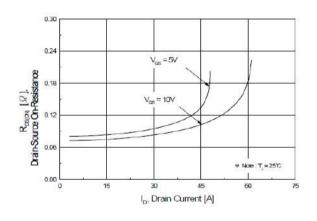
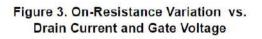


Figure 1. On-Region Characteristics





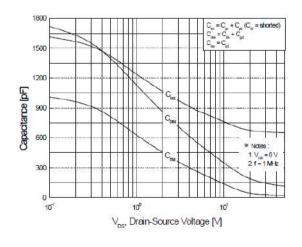
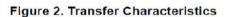
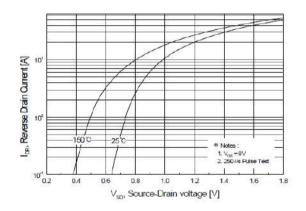


Figure 5. Capacitance Characteristics



V_{cs} , Gate-Source Voltage [V]





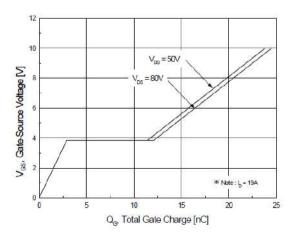
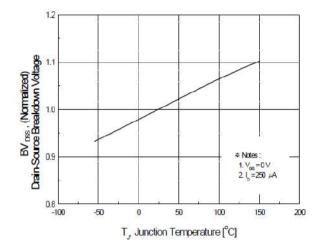


Figure 6. Gate Charge Characteristics

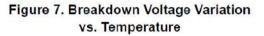


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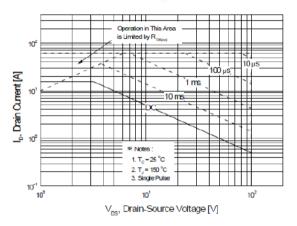


Figure 9. Maximum Safe Operating Area

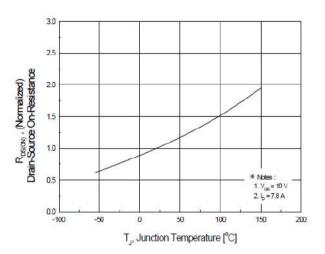


Figure 8. On-Resistance Variation vs. Temperature

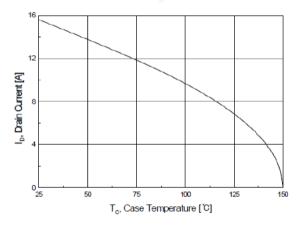


Figure 10. Maximum Drain Current vs. Case Temperature

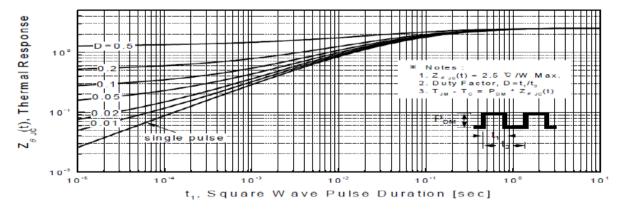


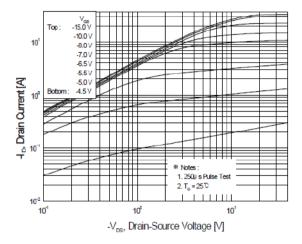
Figure 11. Transient Thermal Response Curve



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Typical Characteristics:P-channel





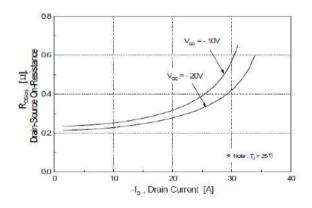


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

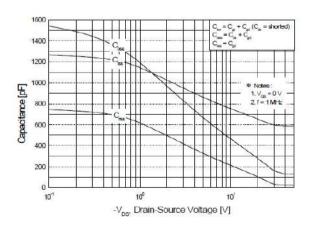


Figure 5. Capacitance Characteristics

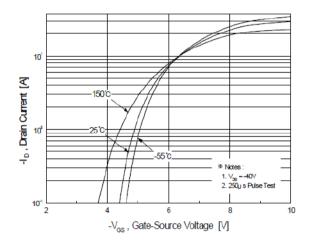


Figure 2. Transfer Characteristics

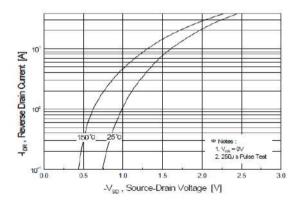


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

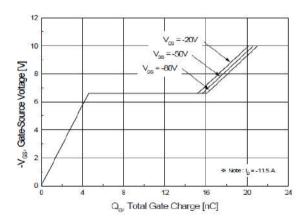
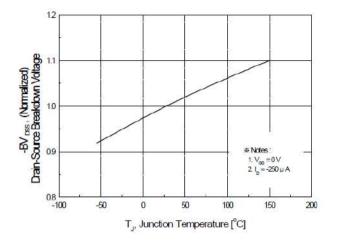
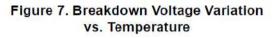


Figure 6. Gate Charge Characteristics



Typical Characteristics:P-channel





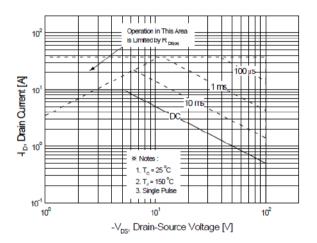


Figure 9. Maximum Safe Operating Area

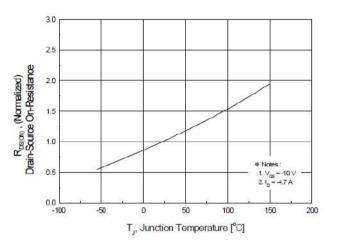


Figure 8. On-Resistance Variation vs. Temperature

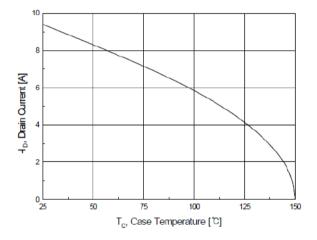


Figure 10. Maximum Drain Current vs. Case Temperature

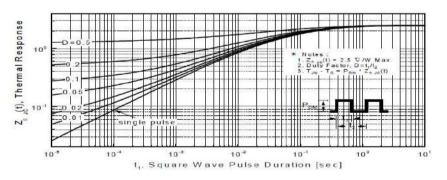


Figure 11. Transient Thermal Response Curve



Ordering and Marking Information

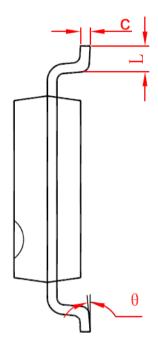
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM4100S-R	4100	SOP-8	Tape&Reel	4000

PACKAGE	MARKING
SOP-8	AS □□□ 4100 □□□□ → Date Code





D Ξ ΓT. b



C. m.h. a l	Dimensions Ir	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
A	1. 350	1. 750	0. 053	0. 069
A1	0. 100	0. 250	0.004	0. 010
A2	1. 350	1. 550	0.053	0. 061
b	0. 330	0. 510	0.013	0. 020
С	0. 170	0. 250	0.006	0. 010
D	4. 700	5. 100	0. 185	0. 200
E	3.800	4. 000	0. 150	0. 157
E1	5. 800	6. 200	0. 228	0. 244
е	1. 270 (BSC)		0. 050	(BSC)
Ĺ	0. 400	1. 270	0.016	0. 050
θ	0°	8°	0°	8°

SOP-8 PACKAGE IN FORMATION





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