



## Features

- High power and current handling capability
- Lead free product is acquired
- Surface mount package

## Application

- PWM applications
- Load switch
- Power management

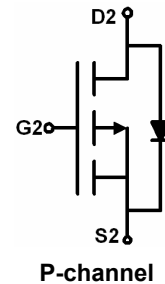
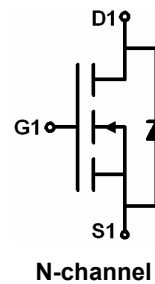
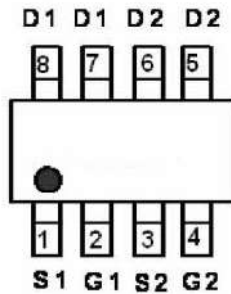
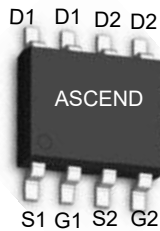
## Product Summary

### ● N-Channel

BVDSS	RDSON.max@10V	ID
60V	40mΩ	5A

### ● P-Channel

BVDSS	RDSON.max@10V	ID
-60V	90mΩ	-4A

**SOP-8 top view**

## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	-60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	V
Continuous Drain Current	I <sub>D</sub>	5	-4	A
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	20	-15	A
Maximum Power Dissipation	P <sub>D</sub>	2	1.2	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 150	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note2)	R <sub>θJA</sub>	N-Ch	89	°C/W
		P-Ch	90	

### N-CH Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=48V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1		2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=4A$	-		40	m $\Omega$
		$V_{GS}=4.5V, I_D=2A$	-		50	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=10V, I_D=4.5A$	-	20	-	S
<b>Dynamic Characteristics (Note4)</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V,$ $F=1.0MHz$	-	275	-	PF
Output Capacitance	$C_{oss}$		-	45	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	35	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, R_L=3\Omega$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	4.5	-	nS
Turn-on Rise Time	$t_r$		-	2.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	14.5	-	nS
Turn-Off Fall Time	$t_f$		-	3.5	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=30V, I_D=4.5$ $A, V_{GS}=10V$	-	5.2	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.85	-	nC
Gate-Drain Charge	$Q_{gd}$		-	1.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=3A$	-	-	1.3	V
Diode Forward Current (Note 2)	$I_S$		-	-	4	A

**..P..CH.Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3.5A	-		90	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.1A	-		115	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>GS</sub> =-5V, I <sub>D</sub> =-3.5A	12	-	-	S
<b>Dynamic Characteristics (Note4)</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, F=1.0MHz	-	730	-	PF
Output Capacitance	C <sub>oss</sub>		-	124	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	75	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V, R <sub>L</sub> =3.6Ω V <sub>GS</sub> =-10V, R <sub>GEN</sub> =3Ω	-	9	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	28	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	13.5	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V, I <sub>D</sub> =-3.5A, V <sub>GS</sub> =-10V	-	14	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.1	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3.	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A	-	-	-1.3	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production



### N-Channel Typical Electrical and Thermal Characteristics

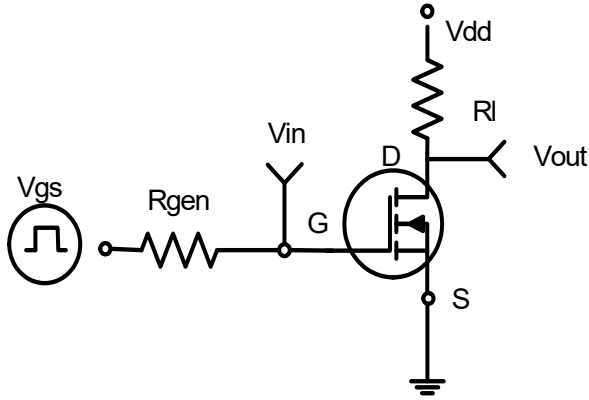


Figure 1: Switching Test Circuit

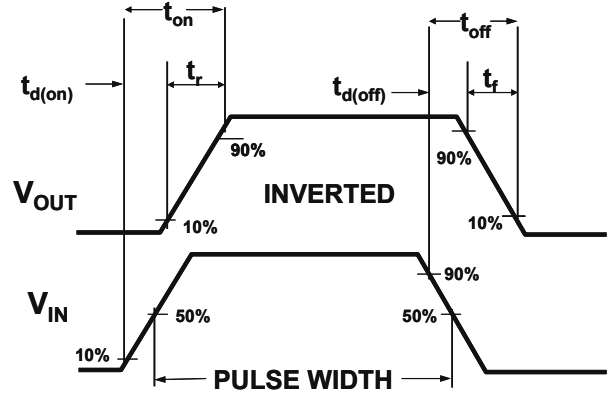


Figure 2: Switching Waveforms

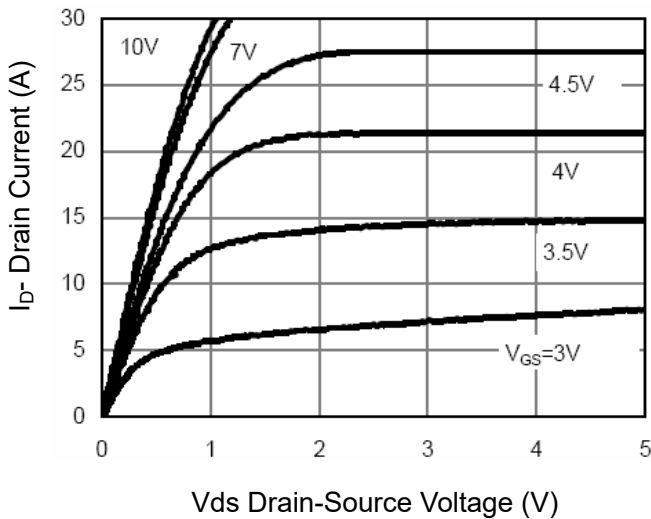


Figure 3 Output Characteristics

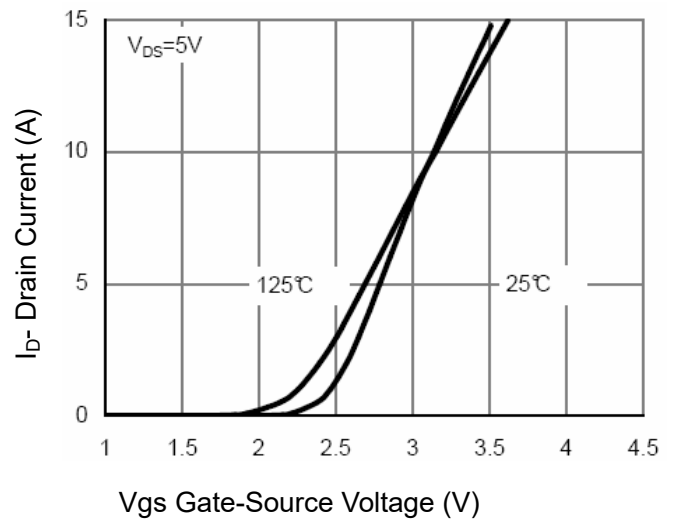


Figure 4 Transfer Characteristics

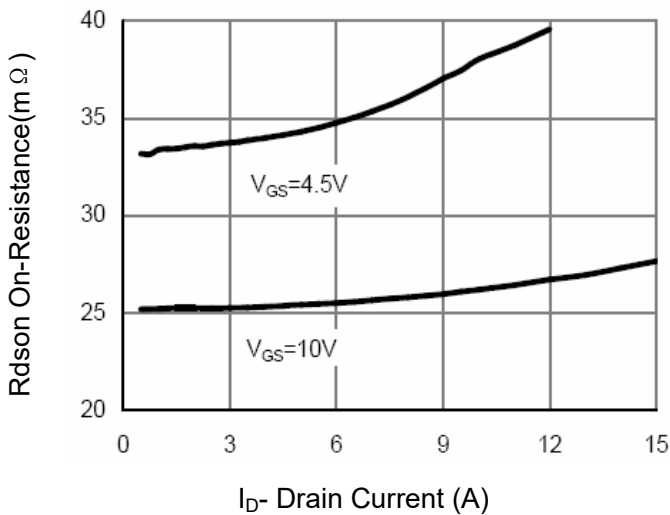


Figure 5 Drain-Source On-Resistance

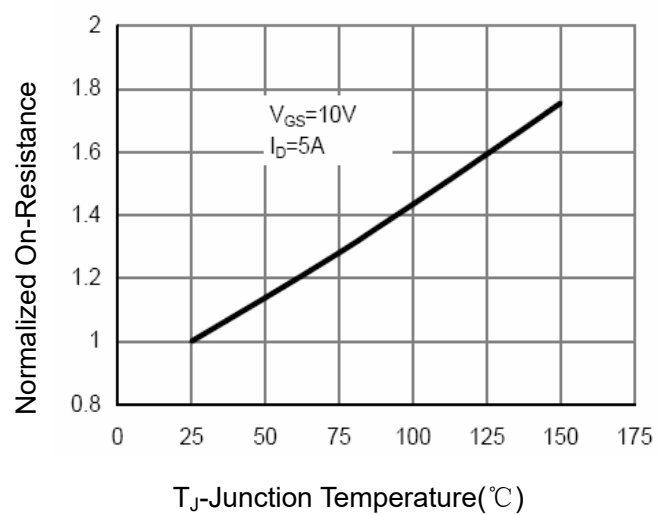
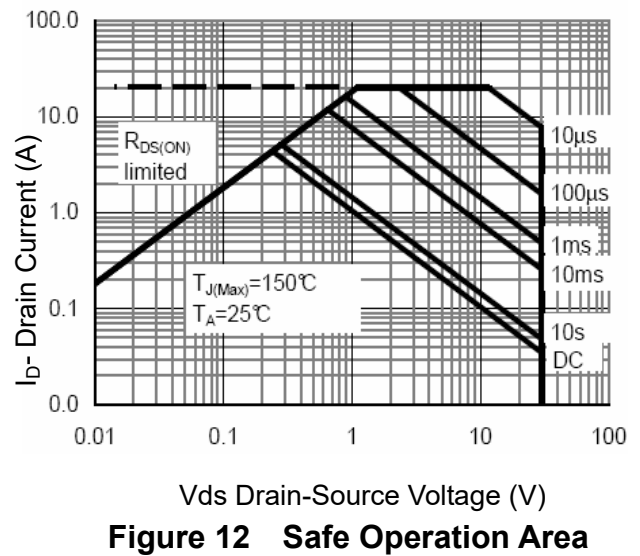
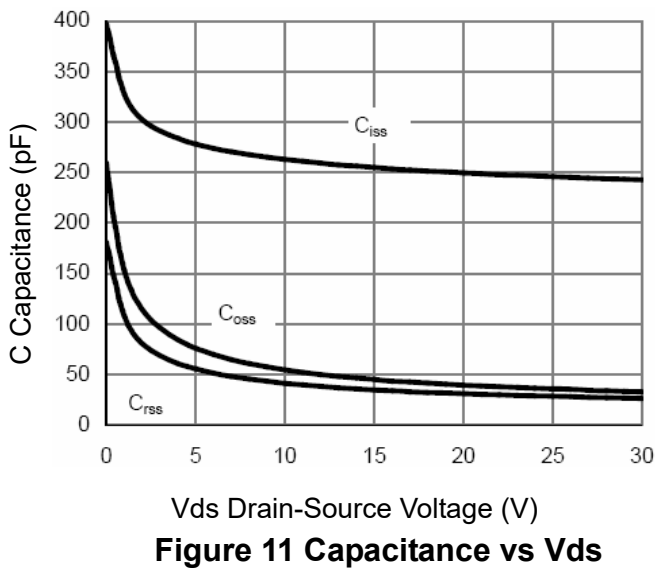
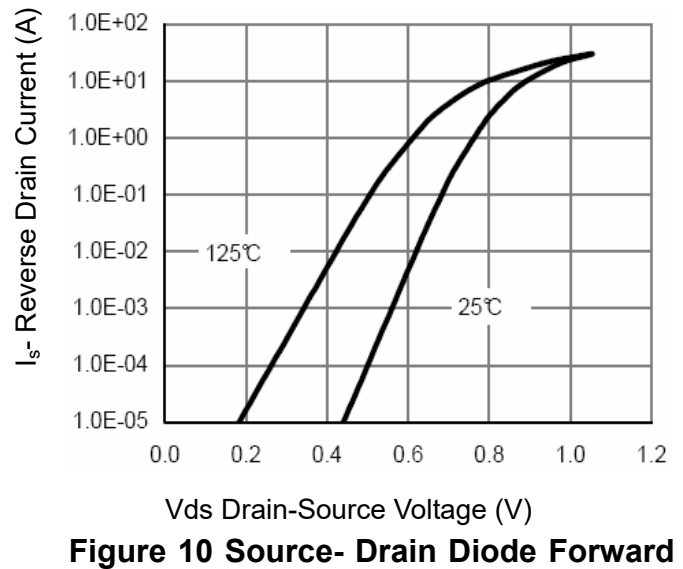
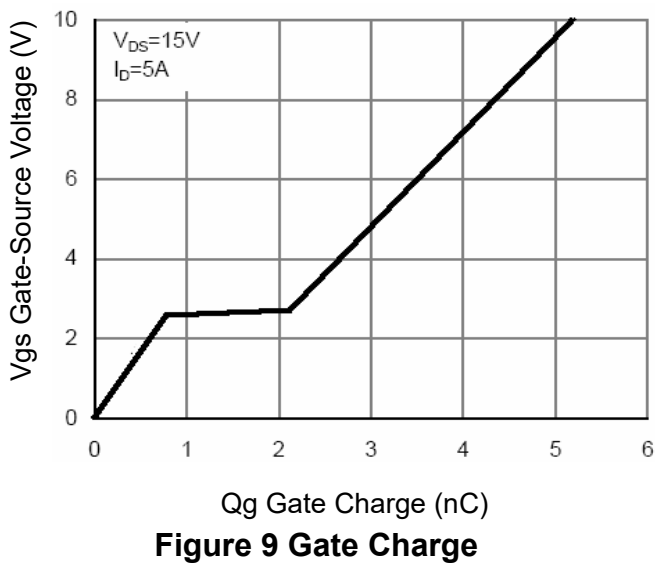
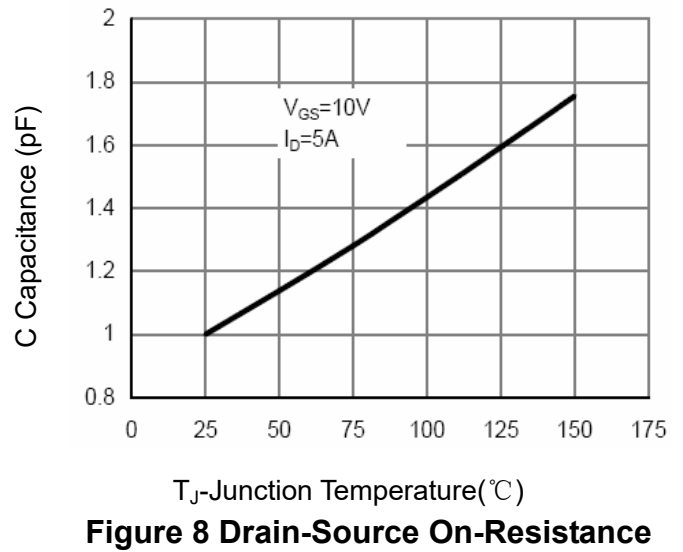
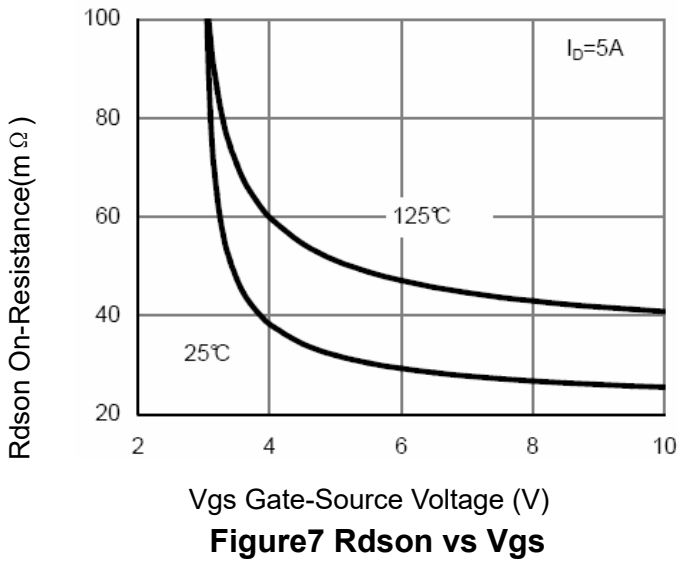
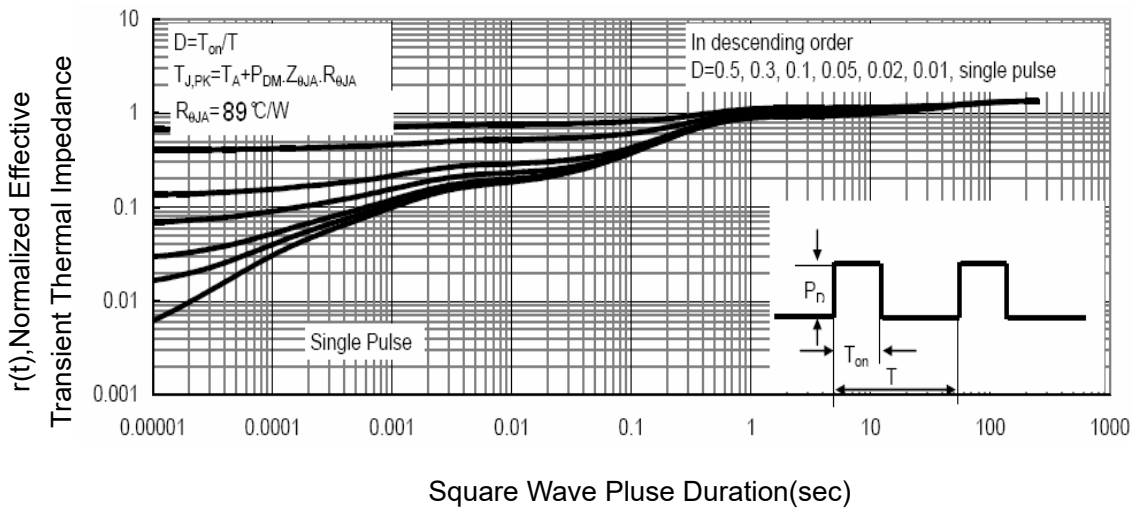


Figure 6 Drain-Source On-Resistance





**Figure 13 Normalized Maximum Transient Thermal Impedance**



### P-Channel Typical Electrical and Thermal Characteristics

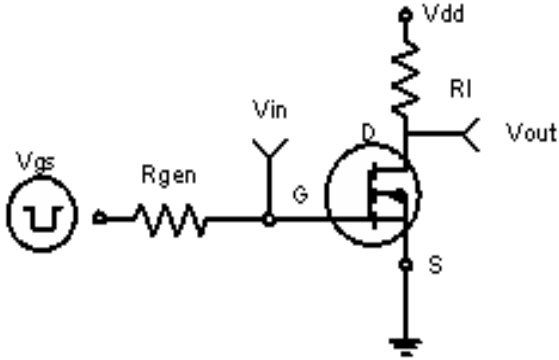


Figure 1: Switching Test Circuit

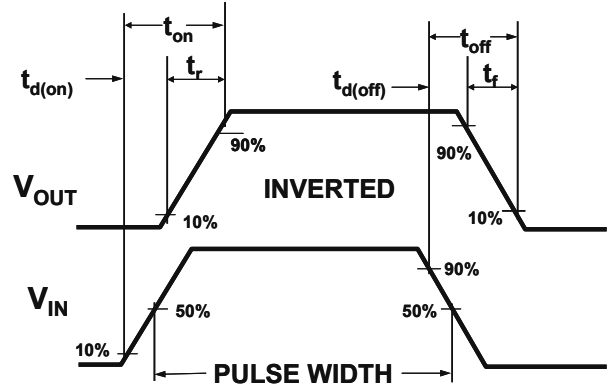


Figure 2: Switching Waveforms

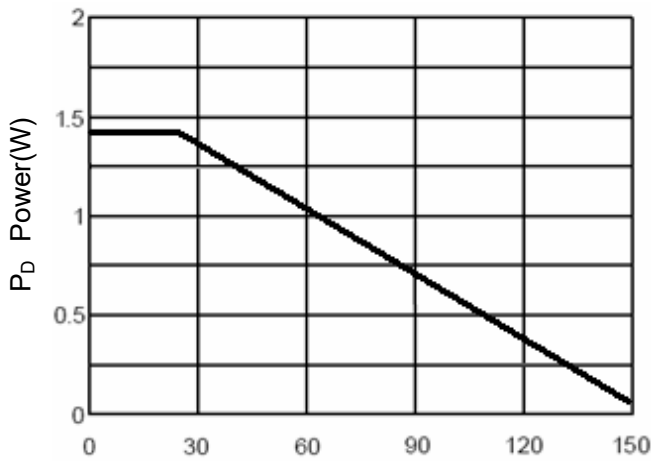


Figure 3 Power Dissipation

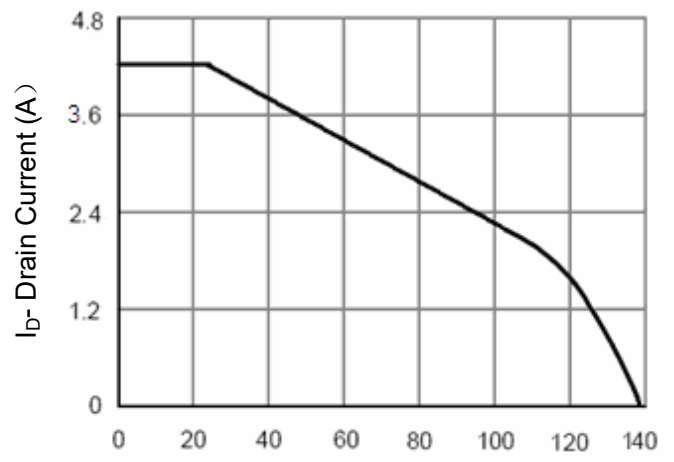


Figure 4 Drain Current

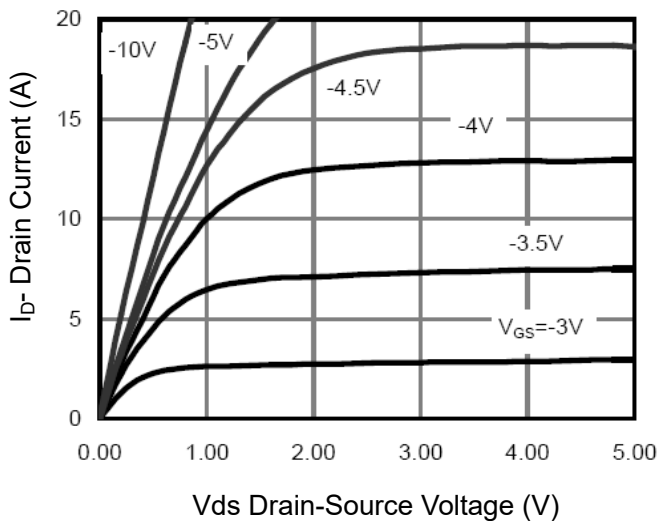


Figure 5 Output CHARACTERISTICS

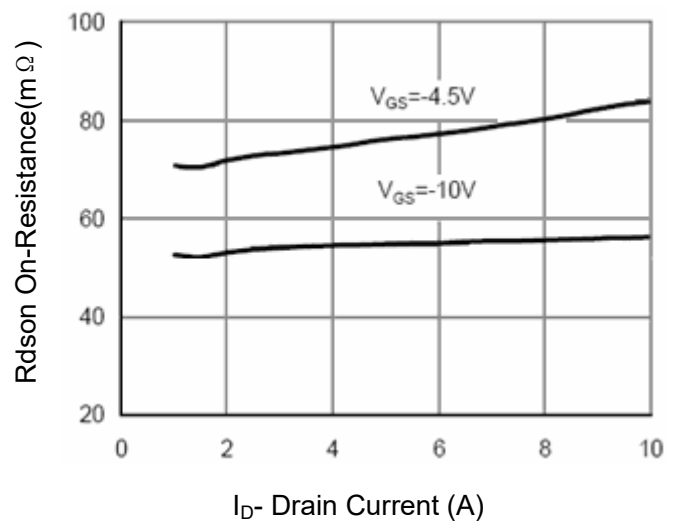
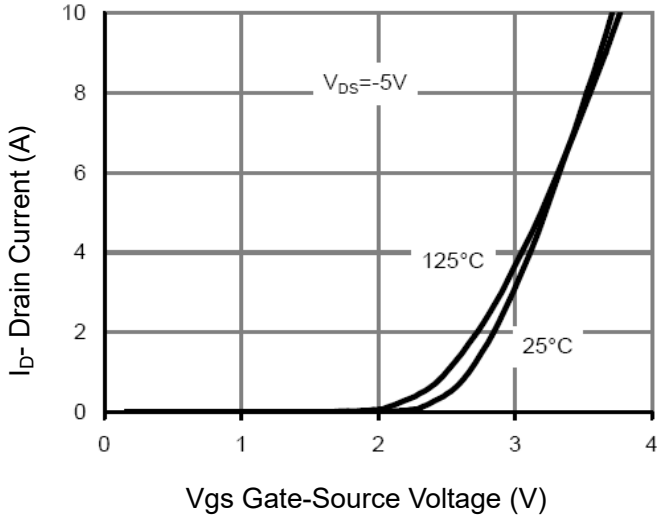
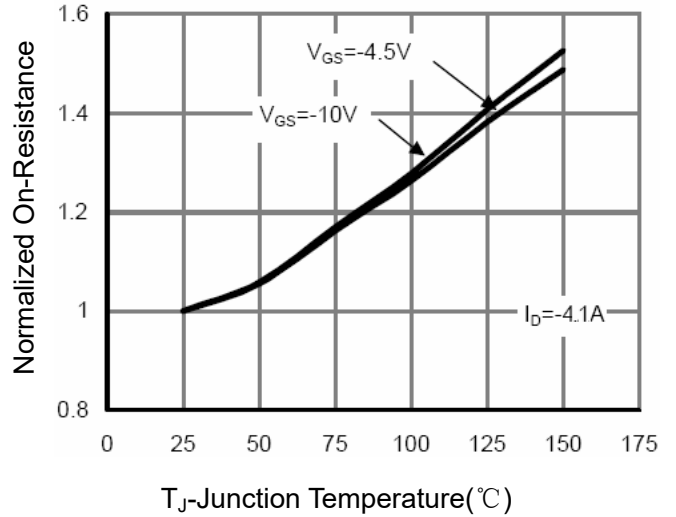


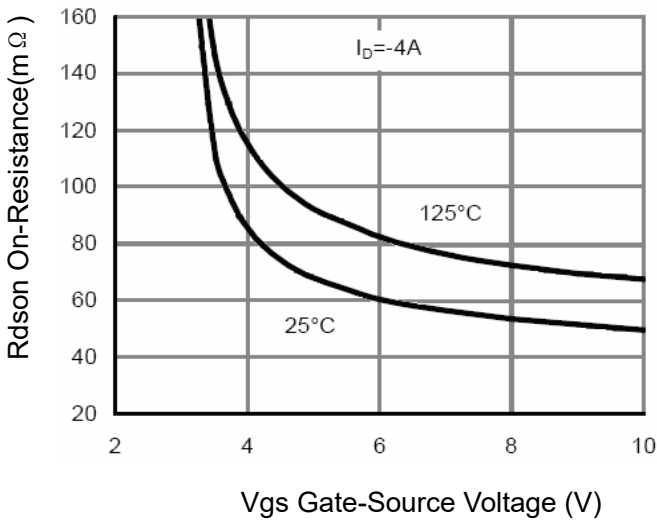
Figure 6 Drain-Source On-Resistance



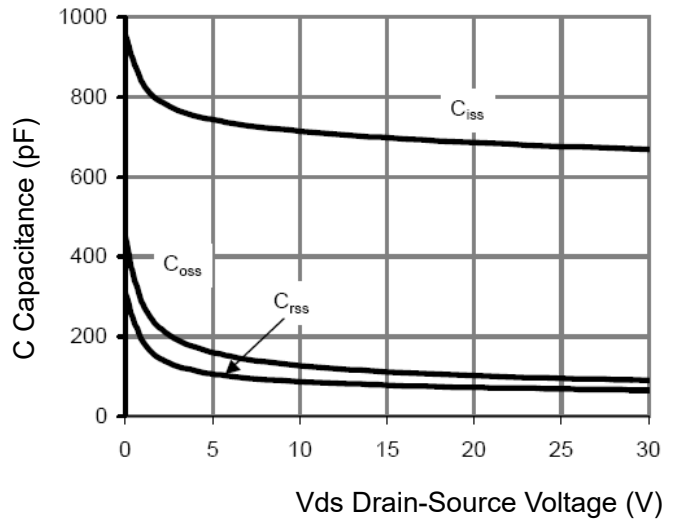
**Figure 7 Transfer Characteristics**



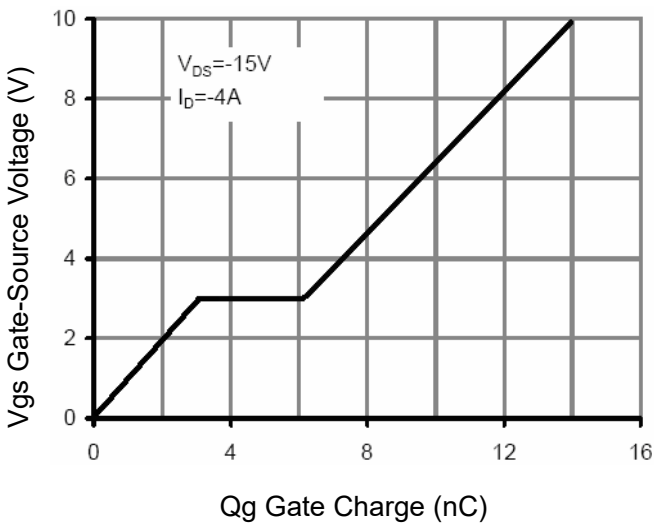
**Figure 8 Drain-Source On-Resistance**



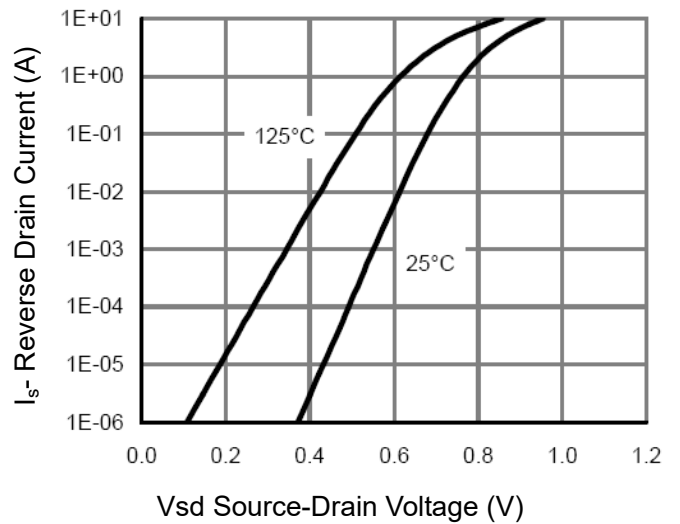
**Figure 9 Rdson vs Vgs**



**Figure 10 Capacitance vs Vds**

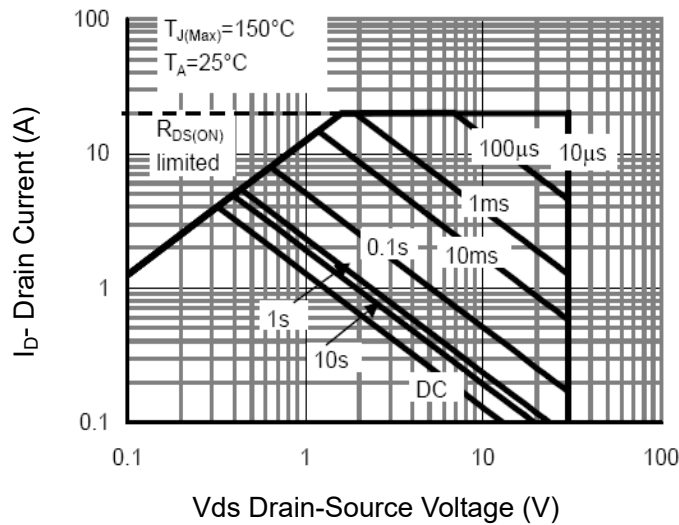


**Figure 11 Gate Charge**

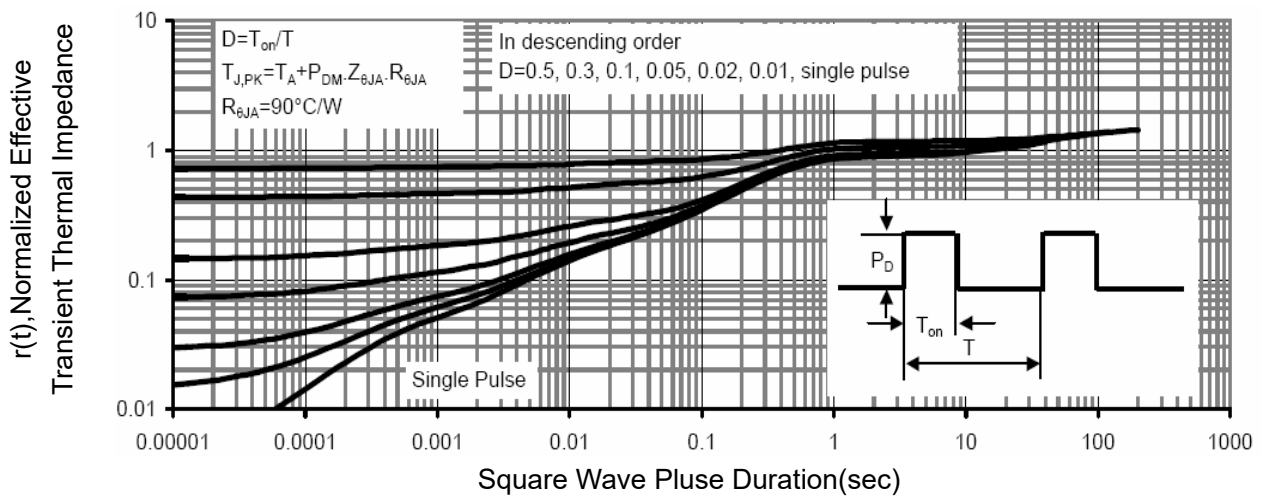


**Figure 12 Source- Drain Diode Forward**





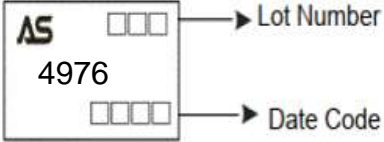
**Figure 13 Safe Operation Area**



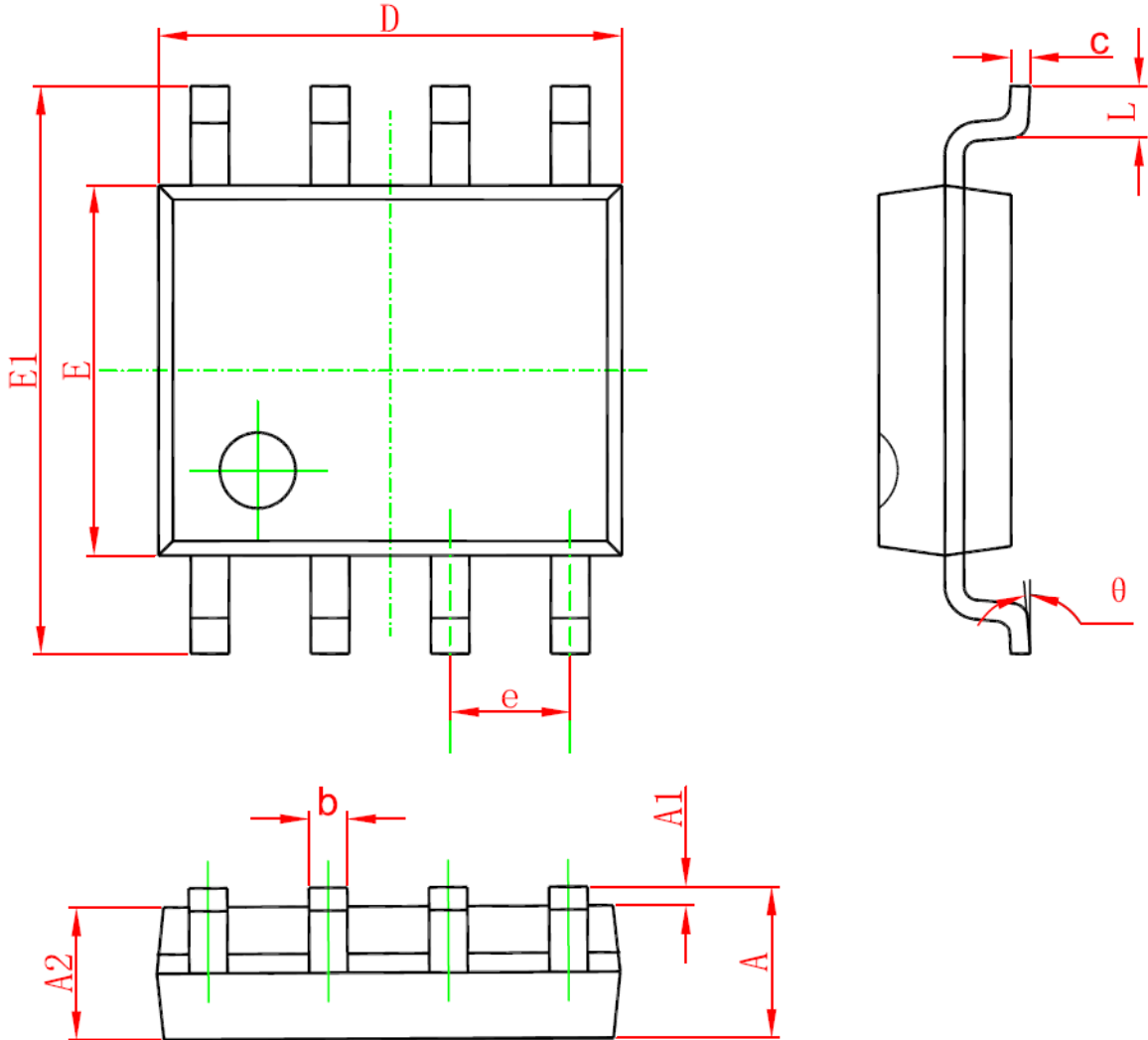
**Figure 14 Normalized Maximum Transient Thermal Impedance**

### Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM4976S-R	4976	SOP-8	Tape&Reel	4000

PACKAGE	MARKING
SOP-8	 <p>The diagram shows a rectangular marking area on a component. It contains the letters 'AS' in the top left, the number '4976' in the center, and two rows of empty boxes. The top row has two boxes and is labeled 'Lot Number'. The bottom row has four boxes and is labeled 'Date Code'.</p>

## SOP-8 PACKAGE IN FORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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[SSM6P54TU,LF](#) [DMP22D4UFO-7B](#) [IPS60R3K4CEAKMA1](#) [DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#) [STU5N65M6](#)  
[C3M0021120D](#) [DMN13M9UCA6-7](#) [BSS340NWH6327XTSA1](#) [MCM3400A-TP](#) [DMTH10H4M6SPS-13](#) [IRF40SC240ARMA1](#)  
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