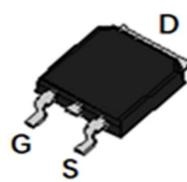


### Features

- Trench Power Technology
- Low  $R_{DS(ON)}$
- Low Gate Charge
- Optimized for Fast-switching Applications

### Applications

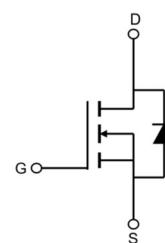
- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial



TO-252

### Product Summary

$V_{DS}$	68	V
$R_{DS(on),Typ} @ V_{GS}=10\text{ V}$	7.7	mΩ
$I_D$	80	A



Schematic Diagram

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS} = 0\text{V}$ )		$V_{DSS}$	68	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	80	A
	$T_C = 100^\circ\text{C}$		49	
Pulsed Drain Current	(note1)	$I_{DM}$	320	A
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Single Pulse Avalanche Energy	(note2)	$E_{AS}$	79	mJ
Avalanche Current		$I_{As}$	23	A
Power Dissipation (note3)	$T_C = 25^\circ\text{C}$	$P_D$	120	W
	$T_C = 100^\circ\text{C}$		60	W
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~+175	°C

### Thermal Resistance

Parameter	Symbol	Value	Unit
		TO-252	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.4	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	





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ASDM68N80KQ

68V N-Channel MOSFET

**Specifications  $T_J = 25^\circ\text{C}$ , unless otherwise noted**

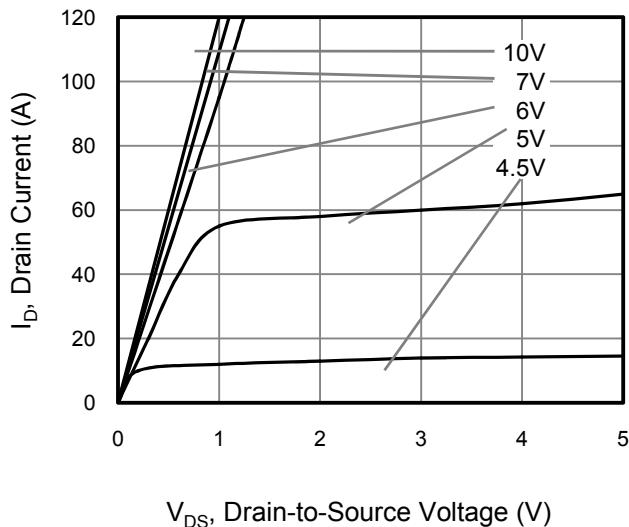
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	68	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 68\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 68\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 100^\circ\text{C}$	--	--	25	
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	--	4	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 30\text{A}$	--	7.7	9.5	$\text{m}\Omega$
Forward Transconductance	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_D = 30\text{A}$	17.1	--	--	S
<b>Dynamic</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 34\text{V}, f = 1.0\text{MHz}$	--	3360	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	1037	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	540	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 34\text{V}, I_D = 50\text{A}, V_{\text{GS}} = 10\text{V}$	--	70	--	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		--	20	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	17	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 34\text{V}, I_D = 50\text{A}, R_G = 2.5\Omega$	--	8	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	7	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	40	--	
Turn-off Fall Time	$t_f$		--	15	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	80	$\text{A}$
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	320	
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 30\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 30\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	--	30	--	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		--	45	--	nC

**Notes**

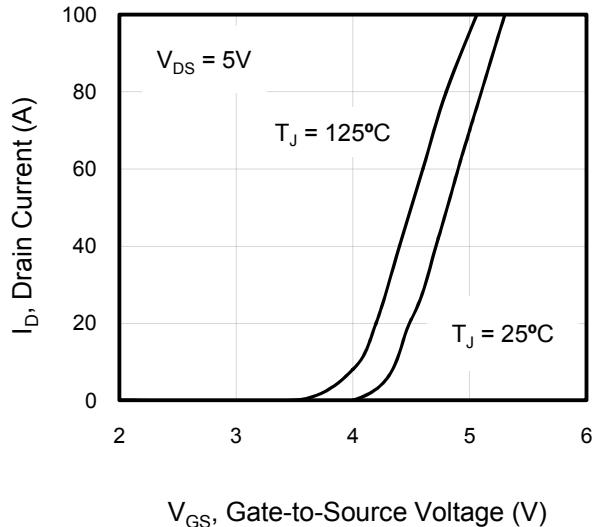
1. Repetitive Rating: Pulse Width limited by maximum junction temperature
2.  $I_{\text{AS}} = 42.5\text{A}, L=0.3\text{mH}, V_{\text{DD}} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3. The power dissipation PD is based on  $T_J(\text{MAX})=175^\circ\text{C}$ , using junction-to-case thermal resistance.

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

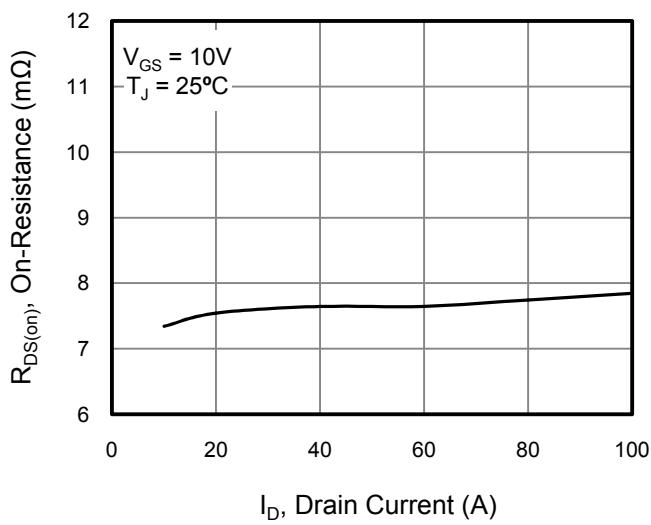
**Figure 1. Output Characteristics**



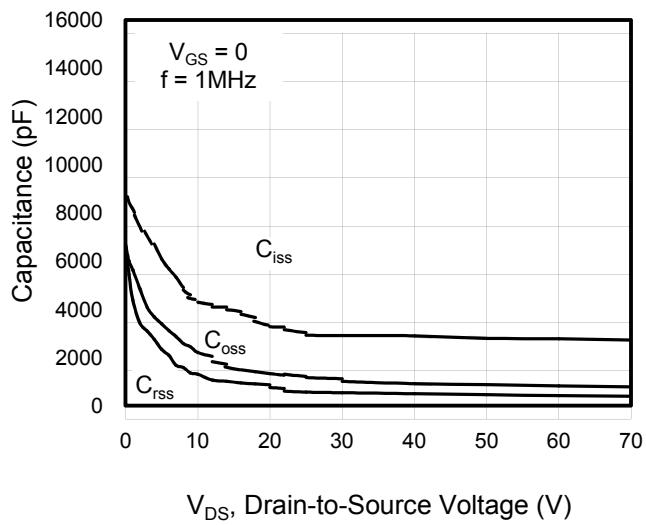
**Figure 2. Transfer Characteristics**



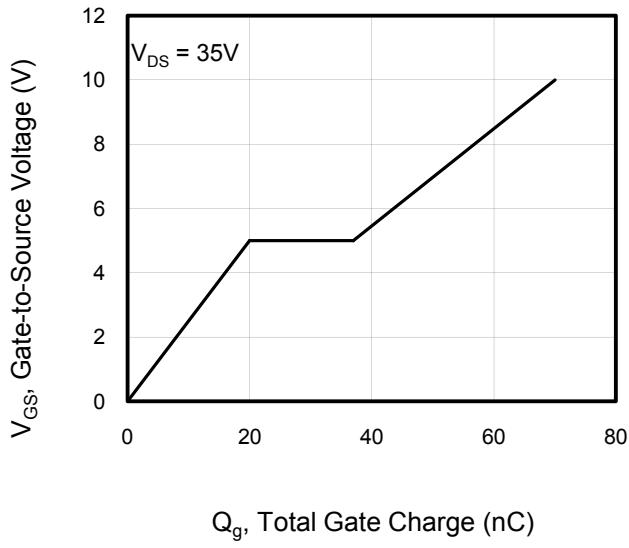
**Figure 3. On-Resistance vs. Drain Current**



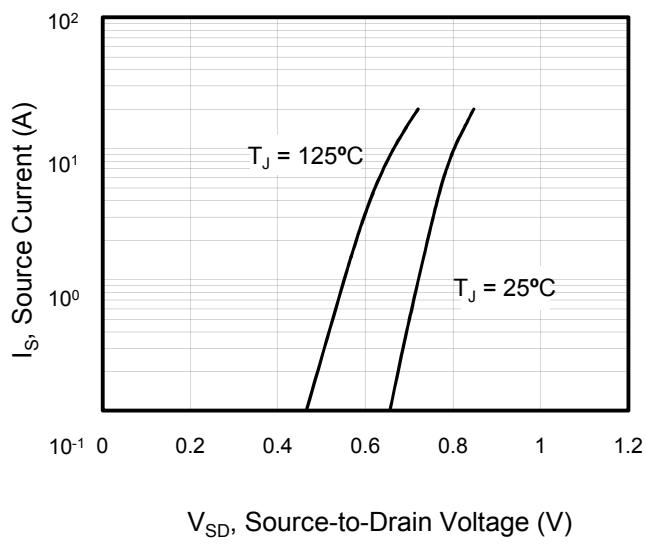
**Figure 4. Capacitance**



**Figure 5. Gate Charge**

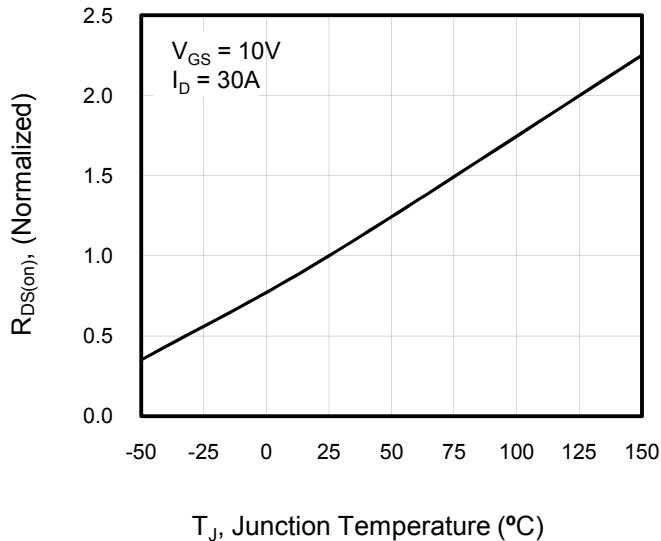


**Figure 6. Body Diode Forward Voltage**

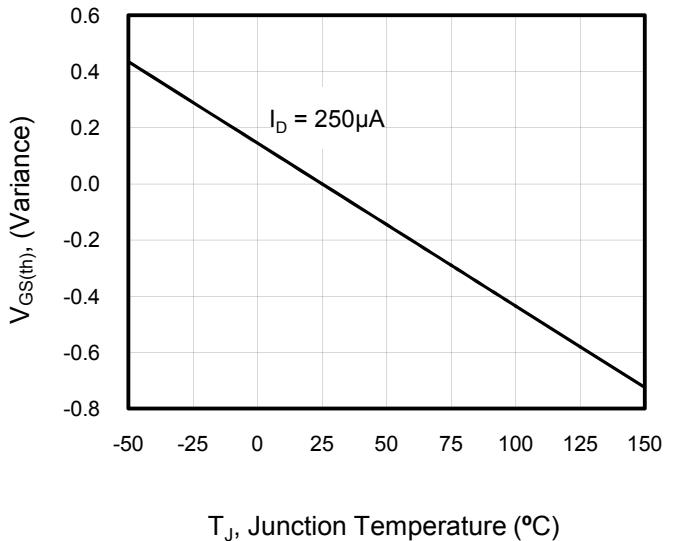


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

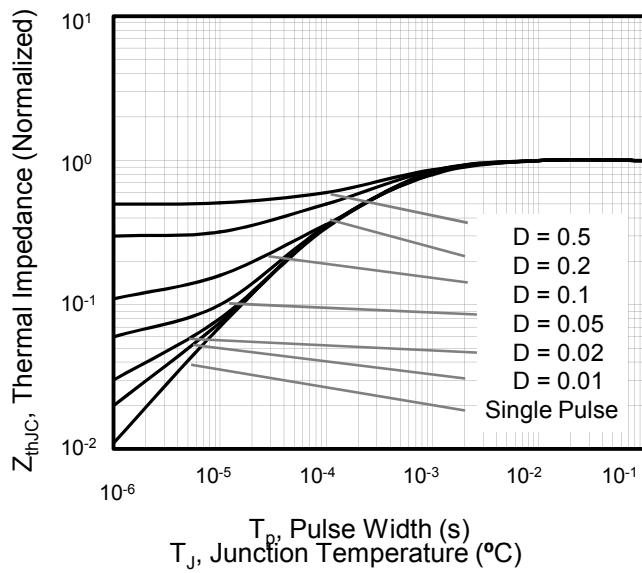
**Figure 7. On-Resistance vs. Temperature**



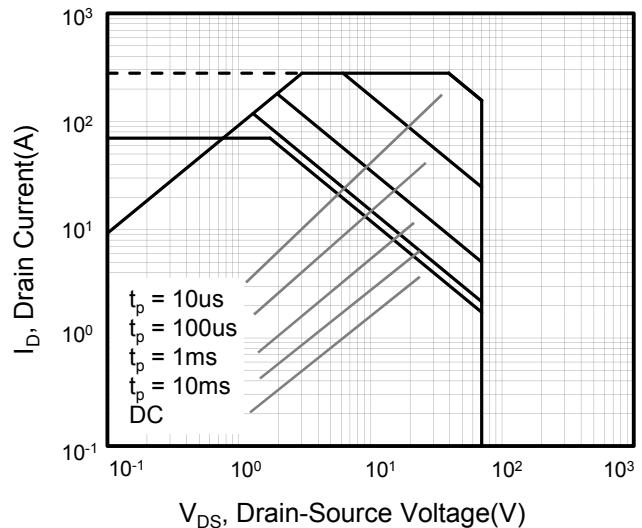
**Figure 8. Threshold Voltage vs. Temperature**



**Figure 9. Transient Thermal Impedance**



**Figure 10. Safe Operation Area**





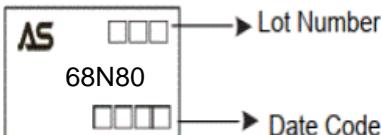
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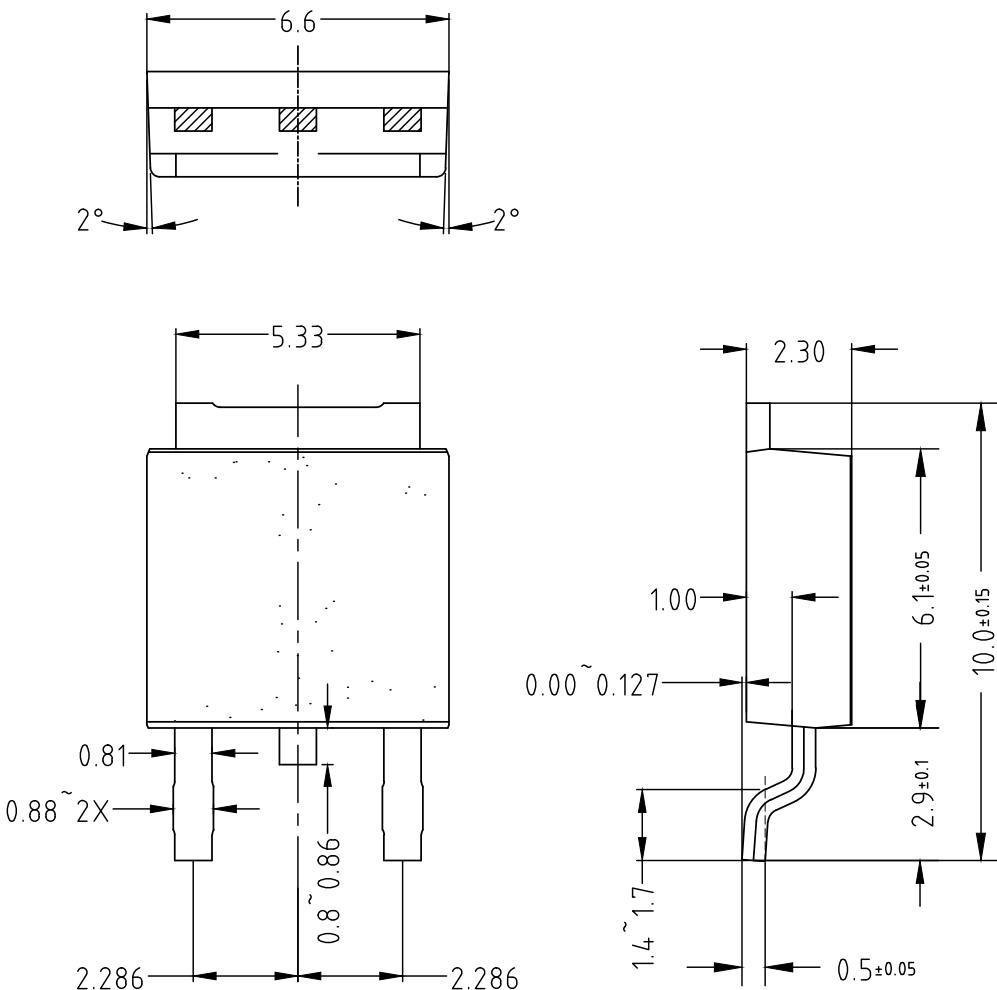
68V N-Channel MOSFET

## Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM68N80KQ-R	68N80	TO-252	Tape&Reel	2500/Reel

PACKAGE	MARKING
TO-252	

## TO-252





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ASDM68N80KQ

68V N-Channel MOSFET

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