	1	2		3	4		5				6			7			
А	1928 0.	.79 TYP.		$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$											A		
В				4 1 0 29 20 30 0 90 29 40 50 20 60 80 10 10 10 10 10 10 70 90 10 10 10 10 10 2.54TYP.	22 0 15 17 17 17 17 17 17 17 17 17 17 17 17 17	No.OF	Dimension Pin (mm)	A	В	C	D	E F	G	Н	1		В
С		B10000000	R	G ECOMMENDED P.C. BOARD	LAYOUT	DIP DIP DIP	- 28P - 32P - 44P	15.56 18.10 20.60 23.48 25.88	18.10 18.10 23.48	11.66 1 14.17 1 16.78 1	11.66 11.69 16.78	12.70 12.7		8 17.78	21.25 23.00 28.50		С
D						DIP         -         68P         31.66         31.66         24.35         24.35         20.32         20.32         25.40         25.40         39.30           DIP         -         84P         36.66         36.66         29.38         29.38         25.40         25.40         30.48         46.85           NOTES:         SPECIFICATIONS:         CURPENT         DATING:1         AMD											
Е	2.54 TP						CURRENT RATING:1 AMP. VOLTAGE:250V AC/DC MAX. DIELECTRIC WITHSTANDING:500VAC FOR ONE MINUTE. CONTACT RESISTANCE:30mΩ (MAX). INSULATION RESISTANCE:1000MΩ (MIN). OPERATING TEMPERATURE:-55°C+105°C RETENTION FORCE:0.30KG PER PIN (MIN). MATERIALS:										
F	2.54			44P			BODY: PBT+30%GF UL 94V-0. COVER:PBT+30%GF UL 94V-0. CONTACTS:PHOSOR BRONZE. CONTACT PLATING TIN/GOLD.										F
		Scal								Nam	ne It	tem No. <b>A-(</b>	CSx	x-Z-F	R (Tin)		
G		X.X	ERANCE ±0.40				Drawr		.07.11	Chap		A-0	CSx	x-G-l	R (Gol	<b>d)</b>	G
	X.XX ±0.25 X.XXX ±0.15			- 1			Approv					Title Chip-Carrier PLCC socket, 2.54MM pitch					
н	Ro	HS Angl						SS	SMAN			_ Drawing No.			rev00	00	н
				Modifica	tion Date	e Name		\% <b>\\$</b> %\/ compon			Replace			She			
	1	2		3	4	4		5			6			7			

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