

## 极低噪声系数，应用于全球导航卫星系统的低噪声放大器

### 特性

- 采用专利的智能线性度增强技术 (SLT) 以减轻射频环境干扰；
- 极低的噪声系数：0.60dB；
- 高功率增益：18.5dB；
- 高线性度 IIP3oob：+3.6dBm；
- 高输入 1dB 压缩点：-9.4dBm；
- 简单的 PCB 应用，只需一个外置的匹配电感；
- 输出内部匹配到 50 欧姆；
- 工作电压：1.5V~3.6V；
- 工作频率：1550~1615MHz；
- 纤小的 1.1mmX0.7mmX 0.42mm LGA 6L 封装
- HBM 静电保护（包括 RFIN 和 RFOUT 引脚）

### 应用

- 手机、平板电脑、数码相机
- 个人导航设备、射频前端模组
- 完整的 GPS 芯片模组
- 防盗保护设备

### 描述

AW5025 是一款适用于 GPS 格洛纳斯，伽利略和北斗等全球导航卫星系统(GNSS) 的低噪声放大器。在射频端口内置隔直电容，所以输入端口无需另加电容，芯片可以尽量靠近天线，减少了线路损耗。其外围元器件简单，只需要一个外置输入匹配电感，节省占板面积，是一款经济高效的解决方案。

AW5025 采用专利的智能线性度增强技术 (SLT)，具有极低噪声系数，高线性度，高增益等特性，可支持低至 1.5V，高至 3.6V 的供电电压。所有这些特性使得 AW5025 成为 GNSS 低噪声放大器的最佳选择，极低的噪声系数大大地改善了灵敏度，高线性度使得系统能更好地抵抗带外干扰，并且降低了前级的滤波要求，进而降低了 GNSS 接收机的总成本。

AW5025 采用纤小的 1.1mm x 0.7mm x 0.42 mm LGA-6L 封装，额定的工作温度范围为-40°C至 85°C。

### 引脚分布及标记图

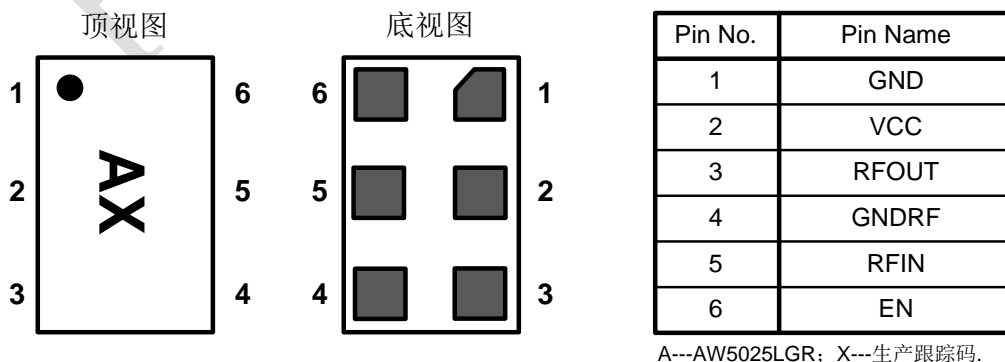


图 1. AW5025 引脚分布及标识图

## Ultra-Low Noise Amplifier for Global Navigation Satellite Systems (GNSS)

### FEATURES

- Reduce RF environment Interference with patented Smart-Linearity-Technology (SLT);
- Ultra low noise figure(NF)=0.60dB;
- High power gain=18.5dB;
- High linearity IIP3oob=+3.6dBm;
- High input 1dB-compression point=-9.4dBm ;
- Requires only one input matching inductor;
- RF output internally matched to 50 ohm;
- Supply voltage: 1.5V to 3.6V;
- Operating frequencies: 1550~1615MHz;
- Slim LGA-6L package:1.1mmX0.7mmX 0.42mm
- HBM ESD protection (including RFIN and RFOUT pin)

### APPLICATIONS

- Smart phones, feature phones,
- Tablet PCs,
- Personal Navigation Devices,
- Digital Still Cameras, Digital Video Cameras;
- RF Front End modules;
- Complete GPS chipset modules;
- Theft protection(laptop, ATM);

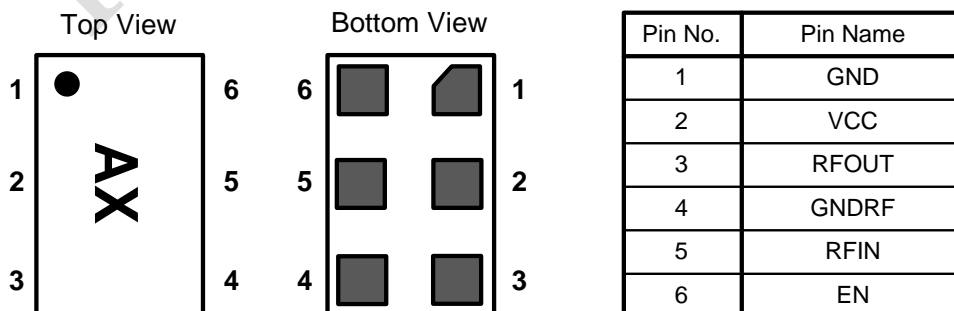
### INTRODUCTION

The AW5025 is a Low Noise Amplifier designed for Global Navigation Satellite Systems (GNSS) as GPS, GLONASS, Galileo and Compass. With on-chip DC blocking capacitors at RFIN and RFOUT, The AW5025 can be close to the antenna, requires only one external input matching inductor, and reduces assembly complexity and the PCB area, enabling a cost-effective solution.

The AW5025 with patented Smart Linearity Technology (SLT) achieves ultra low noise figure, high linearity, high gain, over a wide range of supply voltages from 1.5V up to 3.6V. All these features make AW5025 an excellent choice for GNSS LNA as it improves sensitivity with low noise figure and high gain, provide better immunity against out-of-band jammer signals with high linearity, reduces filtering requirement of preceding stage and hence reduces the overall cost of the GNSS receiver.

The AW5025 is available in a small lead-free, RoHS-Compliant, 1.1mm x 0.7mm x 0.42mm 6-pin LGA package.

### PIN CONFIGURATION AND MARKING



A---AW5025LGR; X---Manufactory trace No.

Figure 1. AW5025 Pin Configuration and Marking

## TYPICAL APPLICATION

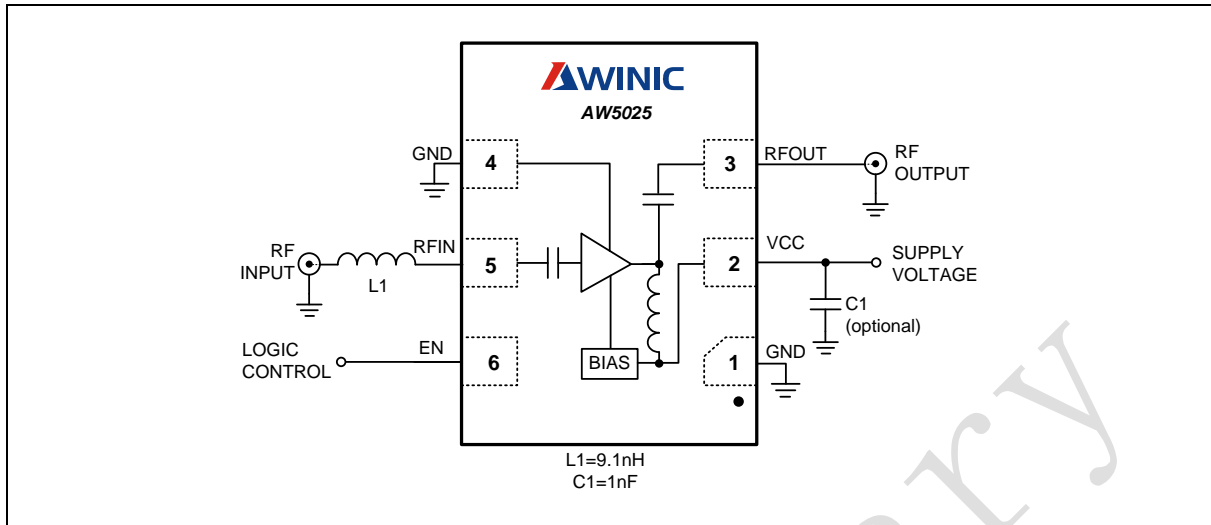


Figure 2. Application Schematic AW5025

For a list of components see [Table 6](#) and [Table 7](#).

## ORDER INFORMATION

Table 1. Order Information

Part Number	Temperature	Package	RoHS	Mark	SPQ
AW5025LGR	-40°C ~ 85°C	1.1mm x 0.7 mm x 0.42mm LGA-6L	Yes	A	Tape and Reel 3000pcs/Reel

AW5025 □ □ □  
 R : Tape&Reel  
 LG: LGA

## ABSOLUTE MAXIMUM RATINGS <sup>1)</sup>

Table 2 . Limiting Values

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Supply Voltage at pin VCC	V <sub>CC</sub>	-0.3	-	5.0	V
Voltage at pin EN <sup>2)</sup>	V <sub>EN</sub>	-0.3	-	5.0	V
Current into pin VCC	I <sub>CC</sub>	-	-	30	mA
RF input power <sup>3)</sup>	P <sub>IN</sub>	-	-	10	dBm
Package thermal resistance	θ <sub>JA</sub>	-	TBD		°C/W
Junction temperature	T <sub>J</sub>	-	-	150	°C
Storage temperature range	T <sub>STG</sub>	-65	-	150	°C
Ambient temperature range	T <sub>amb</sub>	-40	-	85	°C
Solder temperature(10s)		-	260	-	°C
<b>ESD range</b>					
HBM <sup>4)</sup>				TBD	V
<b>Latch-up</b>					
Standard : JEDEC STANDARD NO.78D NOVEMBER 2011			+IT: TBD -IT: TBD		mA mA

**Note1:** Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

**Note2:** Warning: due to internal ESD diode protection, the applied DC voltage should not exceed 5.0V in order to avoid excess current.

**Note3:** The RF input and RF output are AC coupled through internal DC blocking capacitor.

**Note4:** HBM standard: MIL-STD-883H Method 3015.8.

## ELECTRICAL CHARACTERISTICS

**Table 3 . Electrical Characteristics**

(AW5025 EVB<sup>1)</sup>;  $V_{CC}=1.5$  to  $3.6V$ ,  $T_A=-40\sim+85^\circ C$ ,  $f=1550MHz$  to  $1615MHz$ ; Typical values are at  $V_{CC}=2.8V$  and  $T_A=+25^\circ C$ ,  $f=1575.42MHz$ , unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>DC ELECTRICAL CHARACTERISTICS</b>					
$V_{CC}$	Supply Voltage	1.5	-	3.6	V
$I_{SD}$	Shut-Down Current	EN=Low		1.0	$\mu A$
$I_{CC}$	Supply Current	EN=High	6.9	15.0	mA
$V_{EN}$	Digital Input-Logic High	0.80			V
$V_{EN}$	Digital Input-Logic Low			0.45	V
<b>AC ELECTRICAL CHARACTERISTICS</b>					
$G_p$	Power Gain		18.5		dB
$RL_{in}$	Input Return Loss		7.0		dB
ISL	Reverse Isolation		31.0		dB
$RL_{out}$	Output Return Loss		11.0		dB
NF	Noise Figure <sup>2)</sup>	Zs=50 ohm; No jammer	0.60		dB
Kf	Stability factor	f=20MHz...10GHz	1		
$NF_j$	Noise Figure with jammer	Pjam=-20dBm; fjam=850MHz	TBD		dB
		Pjam=-20dBm; fjam= 1850MHz	TBD		dB
		Pjam=-30dBm; fjam=850MHz	TBD		dB
		Pjam=-30dBm; fjam= 1850MHz	TBD		dB
$IP_{1dB}$	Inband input 1dB-compression point	f=1575.42MHz;	-9.4		dBm
$IIP3_{oob}$	Out-of-band input 3 <sup>rd</sup> -order intercept point	f1= 1712.7MHz; f2=1850MHz; Pin=-20dBm	3.4		dBm
$IIP3_{oob}$	Out-of-band input 3 <sup>rd</sup> -order intercept point	f1= 1712.7MHz; f2=1850MHz; Pin=-30dBm	3.6		dBm
$IIP2_{oob}$	Out-of-band input 2 <sup>rd</sup> -order intercept point	f1= 2400MHz; f2=824.6MHz; Pin=-20dBm	8.4		dBm
$IIP2_{oob}$	Out-of-band input 2 <sup>rd</sup> -order intercept point	f1= 2400MHz; f2=824.6MHz; Pin=-30dBm	8.7		dBm
H2-input referred	LTE band-13 2 <sup>nd</sup> Harmonic	f=787.76MHz; Pin=-25dBm; f <sub>H2</sub> =1575.52MHz	TBD		dBm

**Table 3 . Characteristics.....continued**

(AW5025 EVB<sup>1)</sup>;  $V_{CC}=1.5$  to  $3.6V$ ,  $T_A=-40\sim+85^{\circ}C$ ,  $f=1550MHz$  to  $1615MHz$ ; Typical values are at  $V_{CC}=2.8V$  and  $T_A=+25^{\circ}C$ ,  $f=1575.42MHz$ , unless otherwise noted.)

PARAMETER		CONDITIONS	MIN	TYP	MAX	UNITS
<b>AC ELECTRICAL CHARACTERISTICS</b>						
$t_{on}$	Turn-on time <sup>3)</sup>			2.2		$\mu s$
$t_{off}$	Turn-off time <sup>3)</sup>			1.7		$\mu s$

Preliminary

**Table 4 . Electrical Characteristics**

(AW5025 EVB<sup>1)</sup>; VCC=1.5 to 3.6V, T<sub>A</sub>=-40~+85°C, f=1550MHz to 1615MHz; Typical values are at VCC=1.8V and T<sub>A</sub>=+25°C, f=1575.42MHz, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>DC ELECTRICAL CHARACTERISTICS</b>					
V <sub>CC</sub>	Supply Voltage	1.5	-	3.6	V
I <sub>SD</sub>	Shut-Down Current	EN=Low		1.0	μA
I <sub>CC</sub>	Supply Current	EN=High	6.2	15.0	mA
V <sub>EN</sub>	Digital Input-Logic High	0.80			V
V <sub>EN</sub>	Digital Input-Logic Low			0.45	V
<b>AC ELECTRICAL CHARACTERISTICS</b>					
G <sub>p</sub>	Power Gain		18.0		dB
RL <sub>in</sub>	Input Return Loss		6.5		dB
ISL	Reverse Isolation		30.0		dB
RL <sub>out</sub>	Output Return Loss		14.0		dB
NF	Noise Figure <sup>2)</sup>	Zs=50 ohm; No jammer			dB
Kf	Stability factor	f=20MHz...10GHz	1		
NF <sub>j</sub>	Noise Figure with jammer	Pjam=-20dBm; fjam=850MHz		TBD	dB
		Pjam=-20dBm; fjam= 1850MHz		TBD	dB
		Pjam=-30dBm; fjam=850MHz		TBD	dB
		Pjam=-30dBm; fjam= 1850MHz		TBD	dB
IP <sub>1dB</sub>	Inband input 1dB-compression point	f=1575.42MHz	-15.0		dBm
IIP3 <sub>oob</sub>	Out-of-band input 3 <sup>rd</sup> -order intercept point	f1= 1712.7MHz; f2=1850MHz; Pin=-20dBm;	-4.6		dBm
IIP3 <sub>oob</sub>	Out-of-band input 3 <sup>rd</sup> -order intercept point	f1= 1712.7MHz; f2=1850MHz; Pin=-30dBm;	-1.1		dBm
IIP2 <sub>oob</sub>	Out-of-band input 2 <sup>rd</sup> -order intercept point	f1= 824.6MHz; f2=2400MHz; Pin=-20dBm	7.0		dBm
IIP2 <sub>oob</sub>	Out-of-band input 2 <sup>rd</sup> -order intercept point	f1= 824.6MHz; f2=2400MHz; Pin=-30dBm	7.3		dBm
H2-input referred	LTE band-13 2 <sup>nd</sup> Harmonic	f=787.76MHz; Pin=-25dBm; f <sub>H2</sub> =1575.52MHz		TBD	dBm

**Table 4 . Characteristics**.....continued

(AW5025 EVB<sup>1)</sup>; VCC=1.5 to 3.6V, TA=-40~+85°C, f=1550MHz to 1615MHz; Typical values are at VCC=1.8V and TA=+25°C, f=1575.42MHz, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>AC ELECTRICAL CHARACTERISTICS</b>					
t <sub>on</sub>	Turn-on time <sup>3)</sup>		2.2		μs
t <sub>off</sub>	Turn-off time <sup>3)</sup>		1.7		μs

**Note1:** input matched to 50 ohm using a high quality-factor 9.1nH inductor.

**Note2:** 0.08dB PCB losses are subtracted.

**Note3:** Within 10% of the final gain.

Preliminary



## TEST CIRCUITS

1. DC Characteristics test: including power supply, pin voltage, supply current, standby current

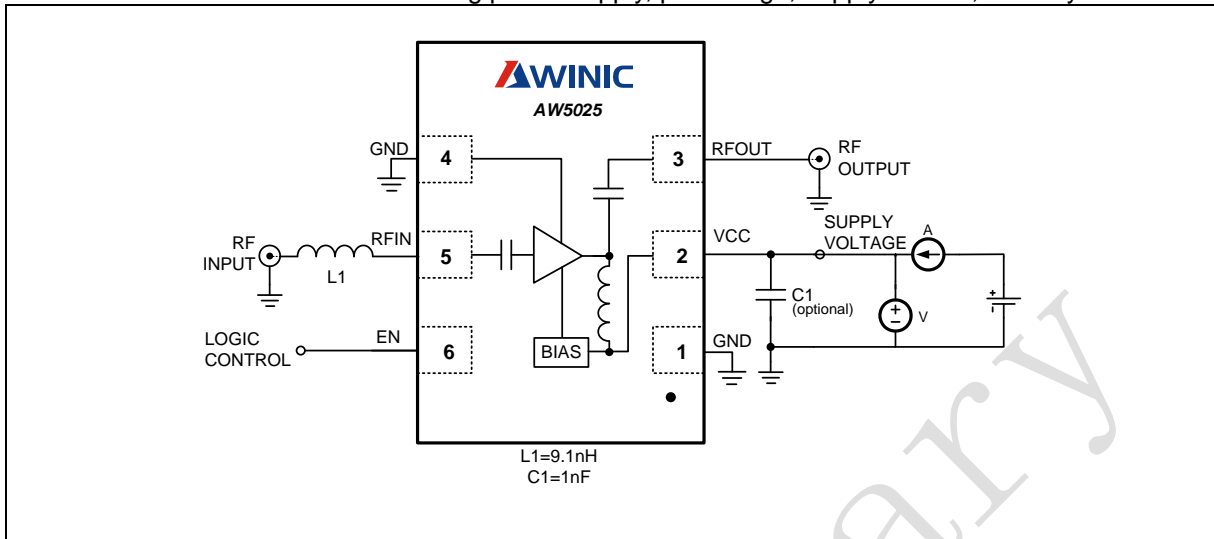


Figure 5. Circuit for DC test

2. S Parameter test: including input return loss, output return loss, reverse isolation, forward gain, 1dB gain compression.

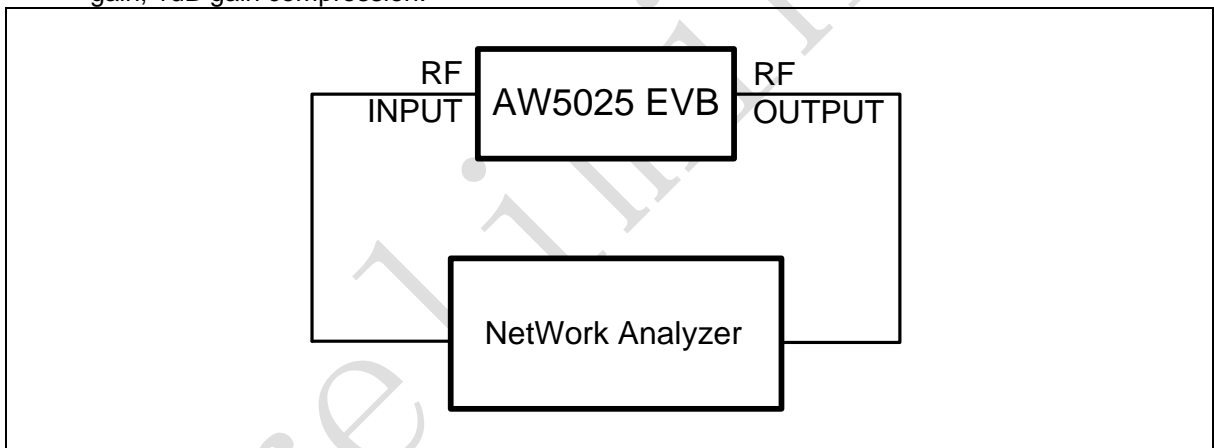


Figure 6. Circuit for S Parameter test

3. Noise Figure test: including noise figure, power gain.

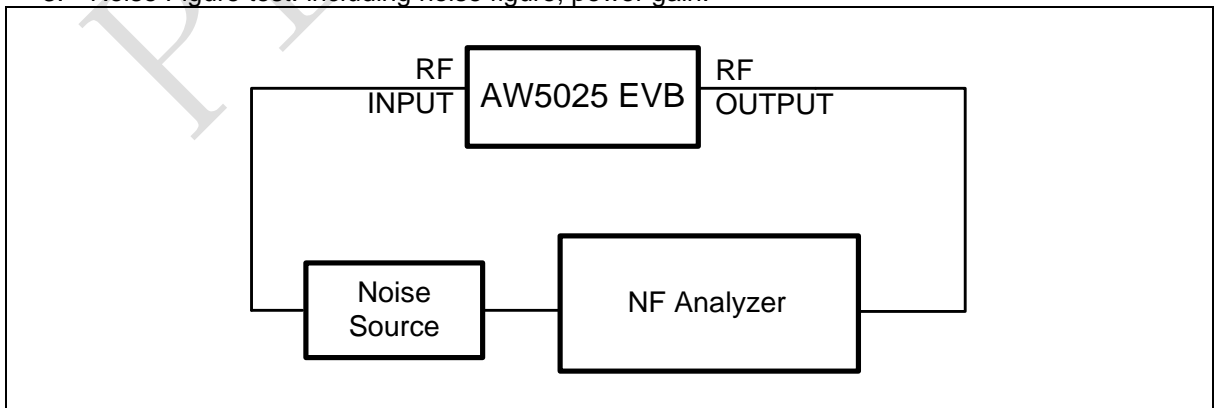


Figure 7. Circuit for Noise Figure test

4. Intermodulation distortion test: including third-order intercept point.

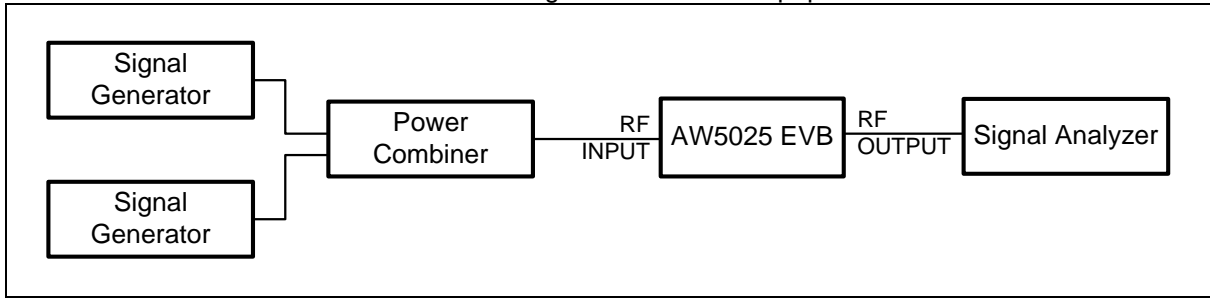


Figure 8. Circuit for intermodulation distortion test

Preliminary

## APPLICATION INFORMATIONS

### 1.1 EN control

The AW5025 includes an internal switch to turn off the entire chip: apply logic high to EN to turn on, and a logic low to shut down.

### 1.2 List of components

1. The AW5025 requires only one external inductor for input matching. If the device/phone manufacturers implement very good power supply filtering on their boards, the bypass capacitor mentioned in this application circuit may be optional. With the capacitor we can get better performance like a little higher gain etc. The value is optimized for the best gain, noise figure, return loss performance. Typical value of inductor is 9.1nH, capacitor is 1nF. For schematics see [Figure2](#).

2. The output of AW5025 is internally matched to 50 ohm and a DC blocking capacitor is integrated on-chip, thus no external component is required at the output.
3. The AW5025 should be placed close to the GPS antenna with the input-matching inductor. Use 50-ohm microstrip lines to connect RF INPUT and RF OUTPUT. Bypass capacitor should be located close to the device. For long Vcc lines, it may be necessary to add more decoupling capacitors. Proper grounding of the GND pins is very important.

Table6 lists the recommended inductor types and values; Table 7 lists the recommended capacitor types and values.

**Table6: list of inductor**

Part Number	Inductance	Q(min)	Q Test Frequency	Supplier	Size
Units	nH		MHz		
LQW15A	9.1	25	250	Murata	0402
SDWL1005C	9.1	24	250	Sunlord	0402

**Table7: list of capacitor**

Part Number	Capacitance	Rated Voltage	Supplier	Size
Units	pF	V		
GRM155	1000	50	Murata	0402

## PACKAGE INFORMATION

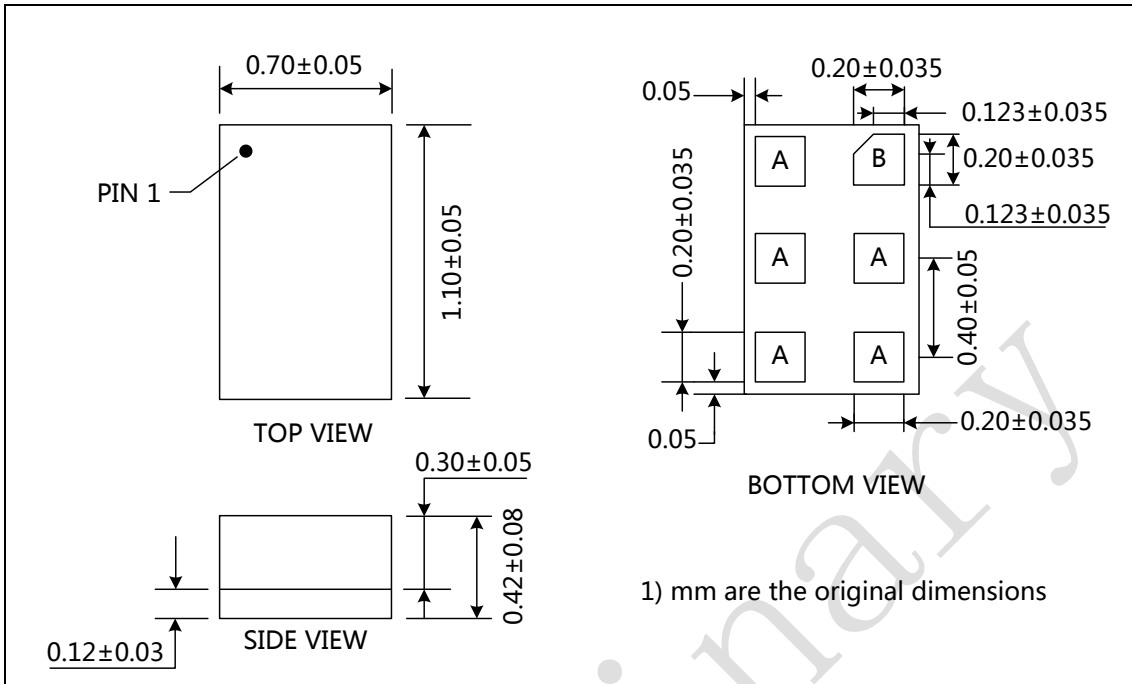


Figure 9. Package Outline

## FOOTPRINT INFORMATION

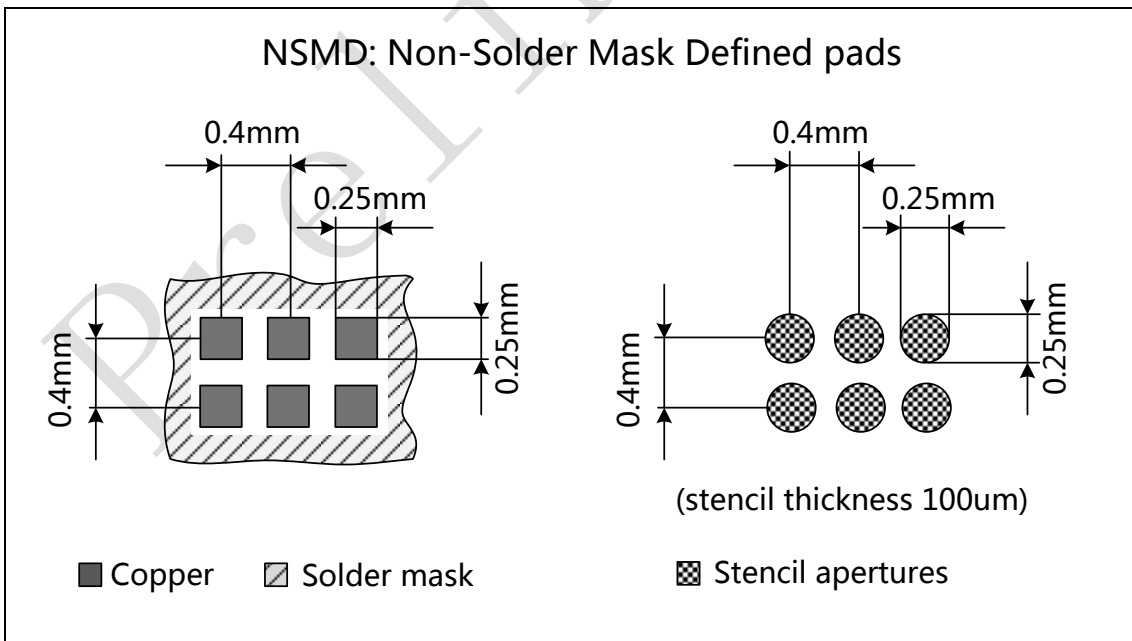


Figure 10. Footprint

## REVISION HISTORY

Table 8. Revision history

Document ID	Release date	Change notice	Supersedes
AW5025_V0.8	2014-05	Preliminary data sheet	-

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