AM9435 空瑟萊特科技股份有限公司 AXElite Technology Co.,Ltd

# -30V P-Channel Enhancement Mode MOSFET

#### **DESCRIPTION**

The AM9435 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application, notebook computer power management and other battery powered circuits where high-side switching.

#### FEATURE

- -30V/-5.2A,  $R_{DS(ON)} < 60m\Omega@V_{GS} = -10V$
- -30V/-4.0A,  $R_{DS(ON)} < 90m\Omega@V_{GS} = -4.5V$
- Super high density cell design for extremely low R<sub>DS(ON)</sub>
- Exceptional on-resistance and maximum DC current capability
- Full RoHS compliance
- SOP-8 package design

#### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

#### **PIN CONFIGURATION**



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## PART MARKING INFORMATION



# ORDERING INFORMATION

Part Number	Package Code	Package	Shipping
AM9435S-A	S	SOP-8	2500 /Tape&Reel

% Year Code : 00 ~ 99% Week Code : 01~52

% SOP-8 : Only available in tape and reel packaging. (A reel contains 2500 devices)

#### **ABSOLUTE MAXIMUM RATINGS (** $T_A = 25^{\circ}C$ Unless otherwise noted )

Symbol	Parameter		Typical	Unit
Vdss	Drain-Source Voltage		-30	V
Vgss	Gate-Source Voltage		±20	V
lD	Continuous Drain Current (TJ=150℃)	V <sub>GS</sub> = -10V	-5.2	А
ldм	Pulsed Drain Current	-20	А	
ls	Continuous Source Current (Diode Conduction)		-2.4	А
TJ	Operation Junction Temperature		-55~150	°C
Tstg	Storage Temperature Range		-55~150	°C
PD	Power Dissipation	Ta=25℃ Ta=70℃	2.8 1.8	W
Reja	Thermal Resistance-Junction to Ambient		70	°C /W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

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Symbol	Parameter	Condition	Min	Тур	Max	Unit
Static Parame	eters		1	1	1	1
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>D</sub> =-250µA	-30			V
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250µA	-1.0		-3.0	V
lgss	Gate Leakage Current	V <sub>DS</sub> =0V,V <sub>GS</sub> =±20V			±100	nA
ldss	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V			-1	
		V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V T <sub>J</sub> =55℃			-5	μA
ID(ON)	On-State Drain Current	$V_{\text{DS}} \leq \textbf{-5V}, V_{\text{GS}} \leq \textbf{-10V}$	-25			А
Rds(on)	Drain-source On-Resistance	V <sub>GS</sub> =-10V,I <sub>D</sub> =-5.2A		48	60	mO
		$V_{GS}$ =-4.5V, I <sub>D</sub> =-4.0A		74	90	11152
Gfs	Forward Transconductance	VDS =-10V,ID =-5.2A		10		S
Source-Drain	Doide					
ls	Diode forward Current (Max.)				2.6	А
Vsd	Diode Forward Voltage	Is=-2.0A,VGS=0V		-0.8	-1.2	V
Dynamic Para	ameters					
Qg	Total Gate Charge			15	10	
Qgs	Gate-Source Charge	V <sub>DS</sub> =-15V,V <sub>GS</sub> =-10V I <sub>D</sub> =-5 0A		4.0		nC
Q <sub>gd</sub>	Gate-Drain Charge			2.0		
Ciss	Input Capacitance			680		
Coss	Output Capacitance	VDS =-15V,VGS =0V f =1MHz		120		pF
Crss	Reverse Transfer Capacitance	1 1111112		75		
t <sub>d(on)</sub>				7.0	15	nS
tr	Tum-On Time	$V_{DD} = -15V, RL = 15\Omega$		10	20	
td(off)		ID = -1.0A, VGEN = -10V Rg = 6 $\Omega$		40	80	
tr				20	40	

### **ELECTRICAL CHARACTERISTICS**( $T_A = 25$ ° Unless otherwise noted)

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 Note : 1. Pulse test: pulse width <= 300us, duty cycle<= 2%</td>

 2. Static parameters are based on package level with recommended wire-bonding



## **TYPICAL CHARACTERISTICS (25**°C Unless Note)



# TYPICAL CHARACTERISTICS(25°C Unless Note)

SOP-8 PACKAGE DIMENSIONS





Dimensions					
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
A	0.055	0.069	1.40	1.75	
A1	0.040	0.100	0.10	0.25	
A2	0.051	0.059	1.30	1.50	
В	0.013	0.020	0.33	0.51	
С	0.0075	0.010	0.19	0.25	
D	0.189	0.209	4.80	5.30	
E	0.146	0.161	3.70	4.10	
e	-	-	-	-	
Н	0.228	0.244	5.79	6.20	
L	0.015	0.050	0.38	1.27	
У	-	0.004	-	0.10	
θ	0°	8°	0°	8°	

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